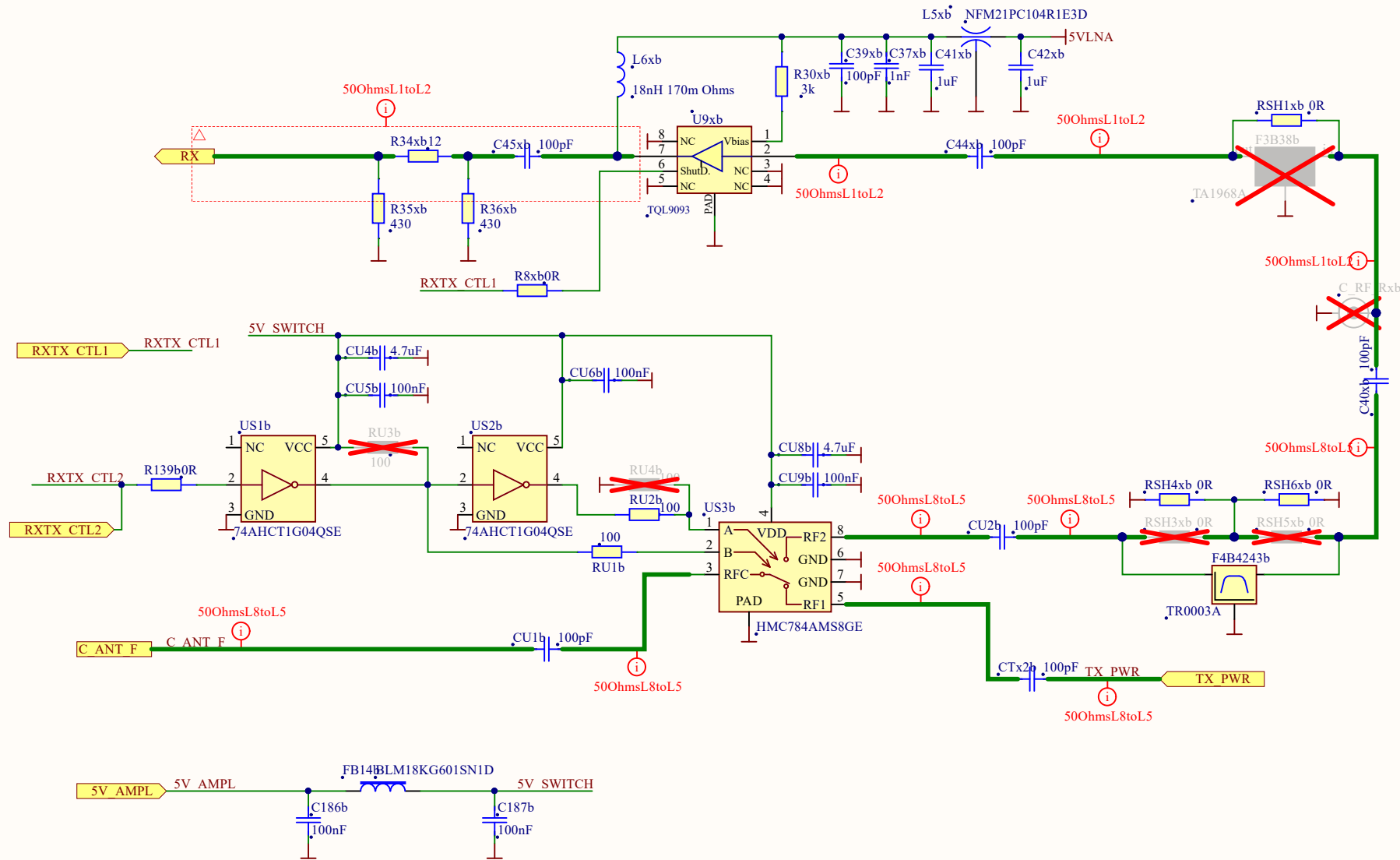
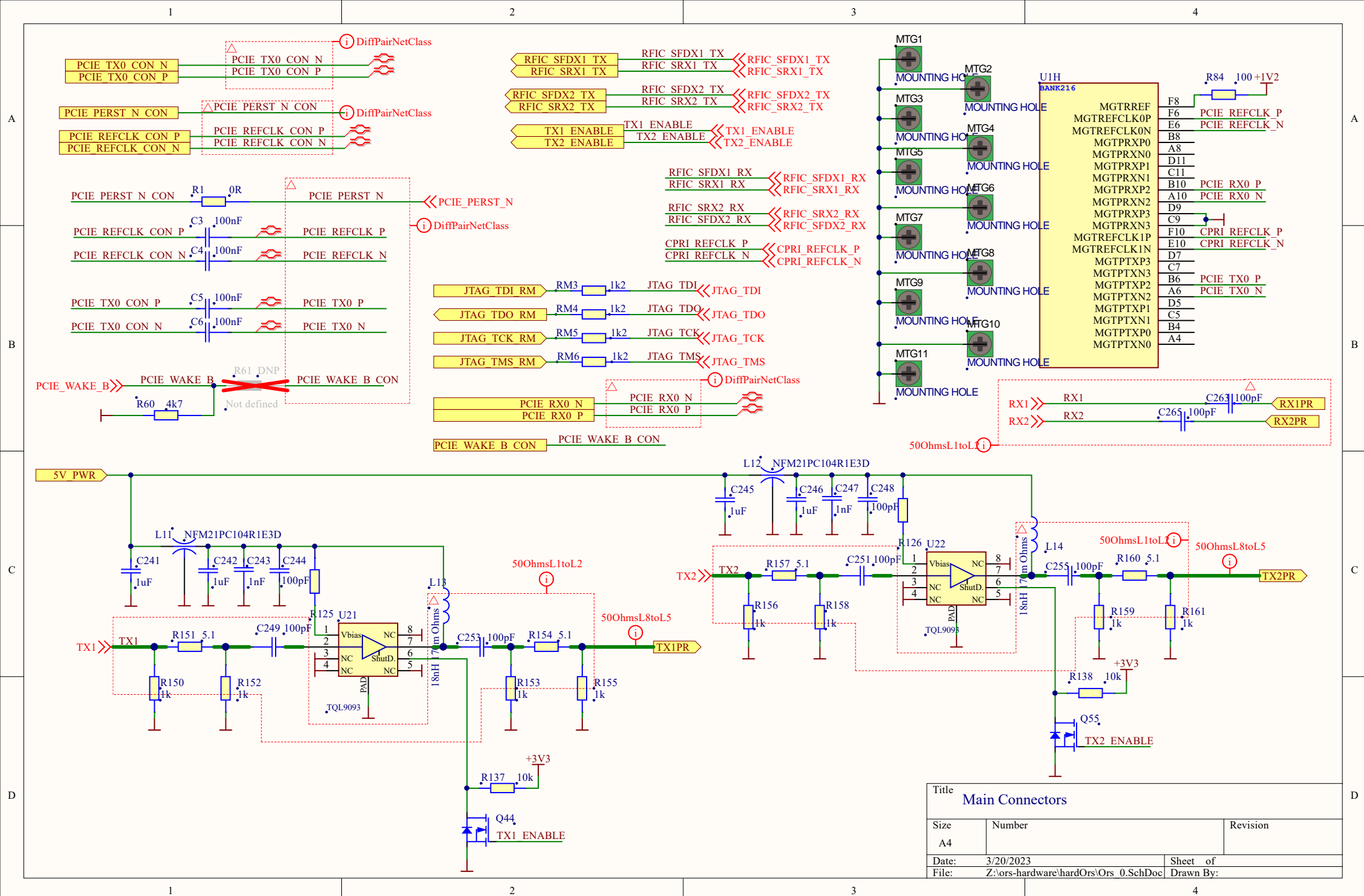


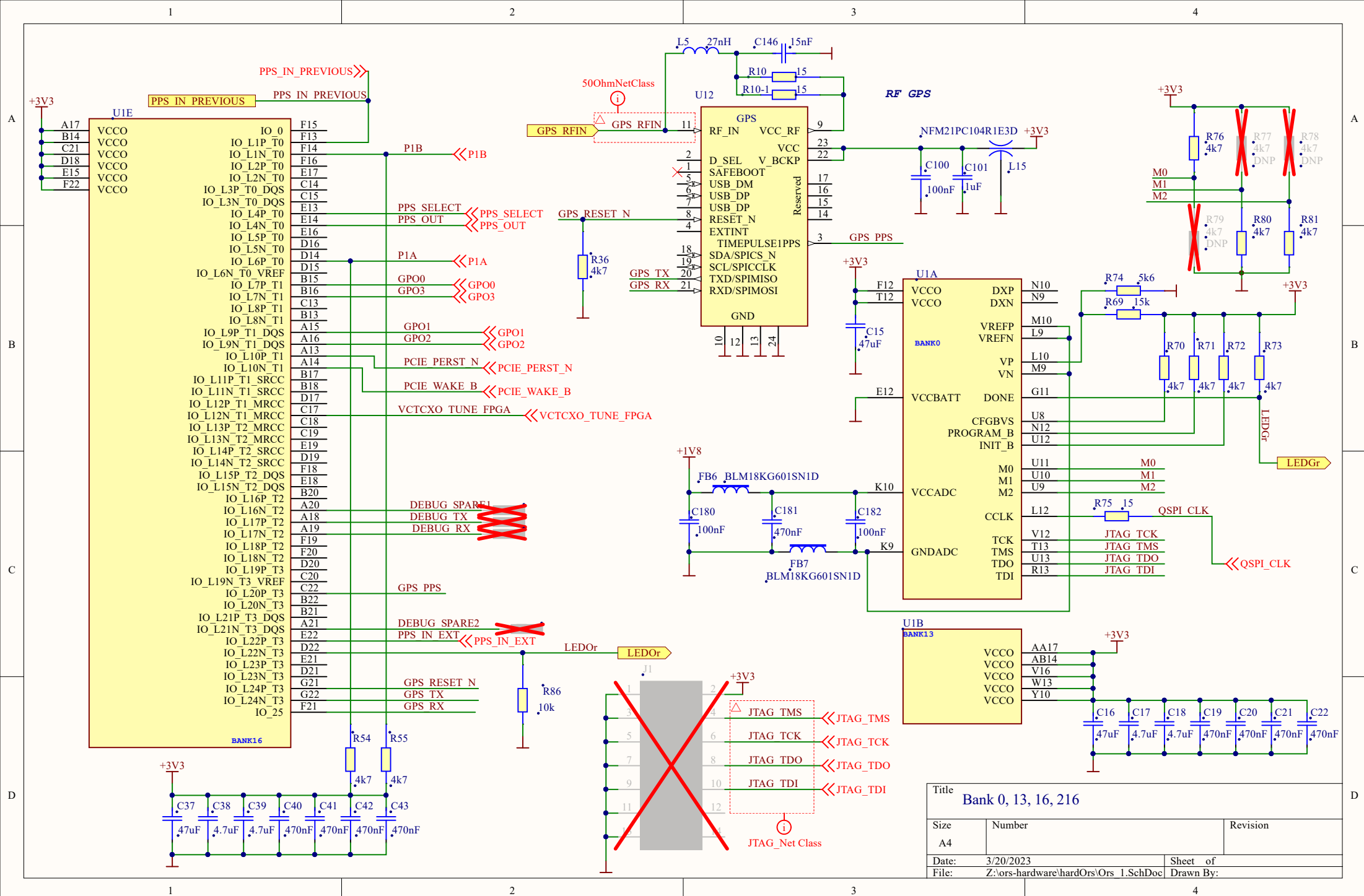
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Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Pwr 1.SchDoc	Drawn By:

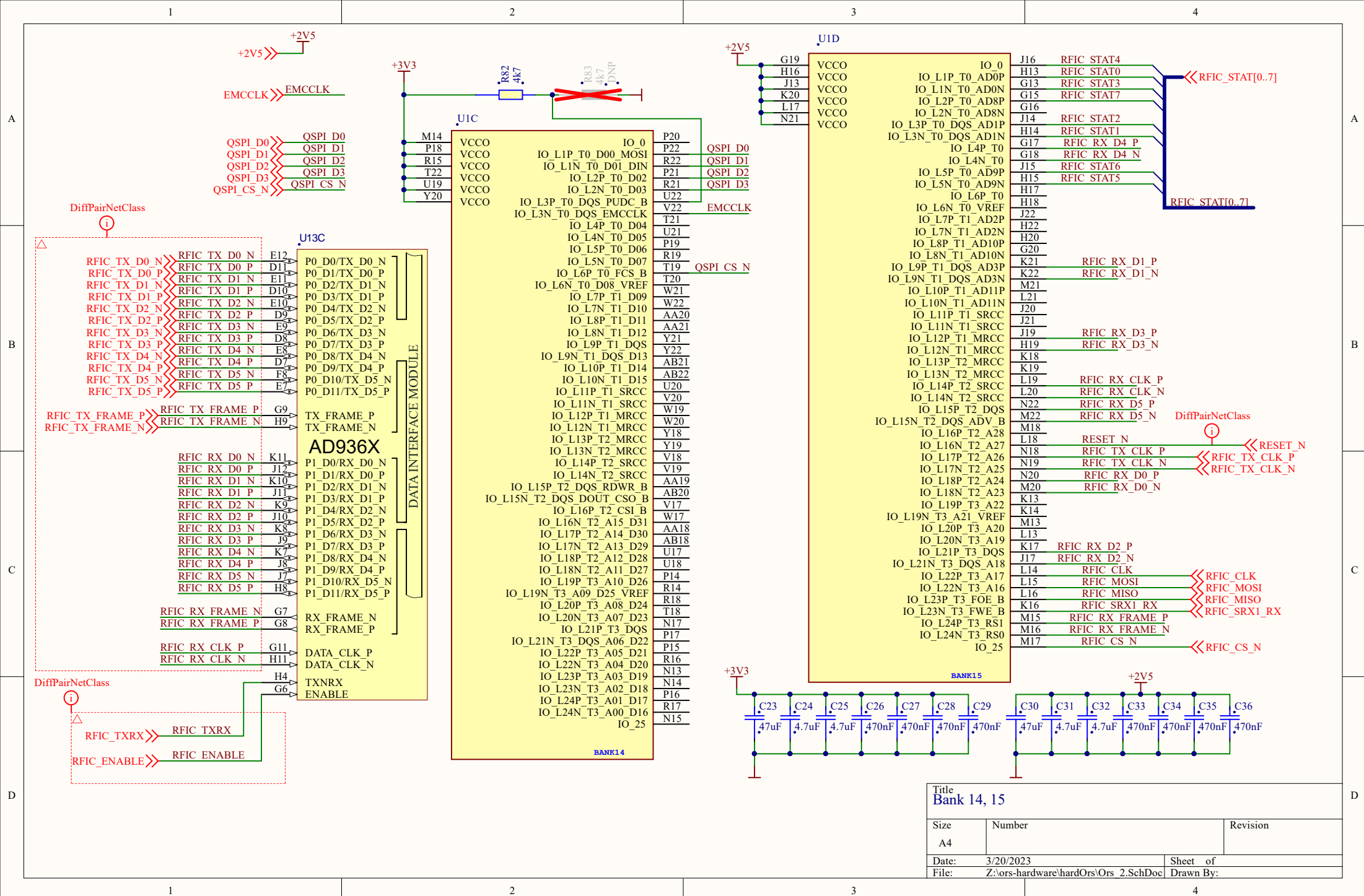




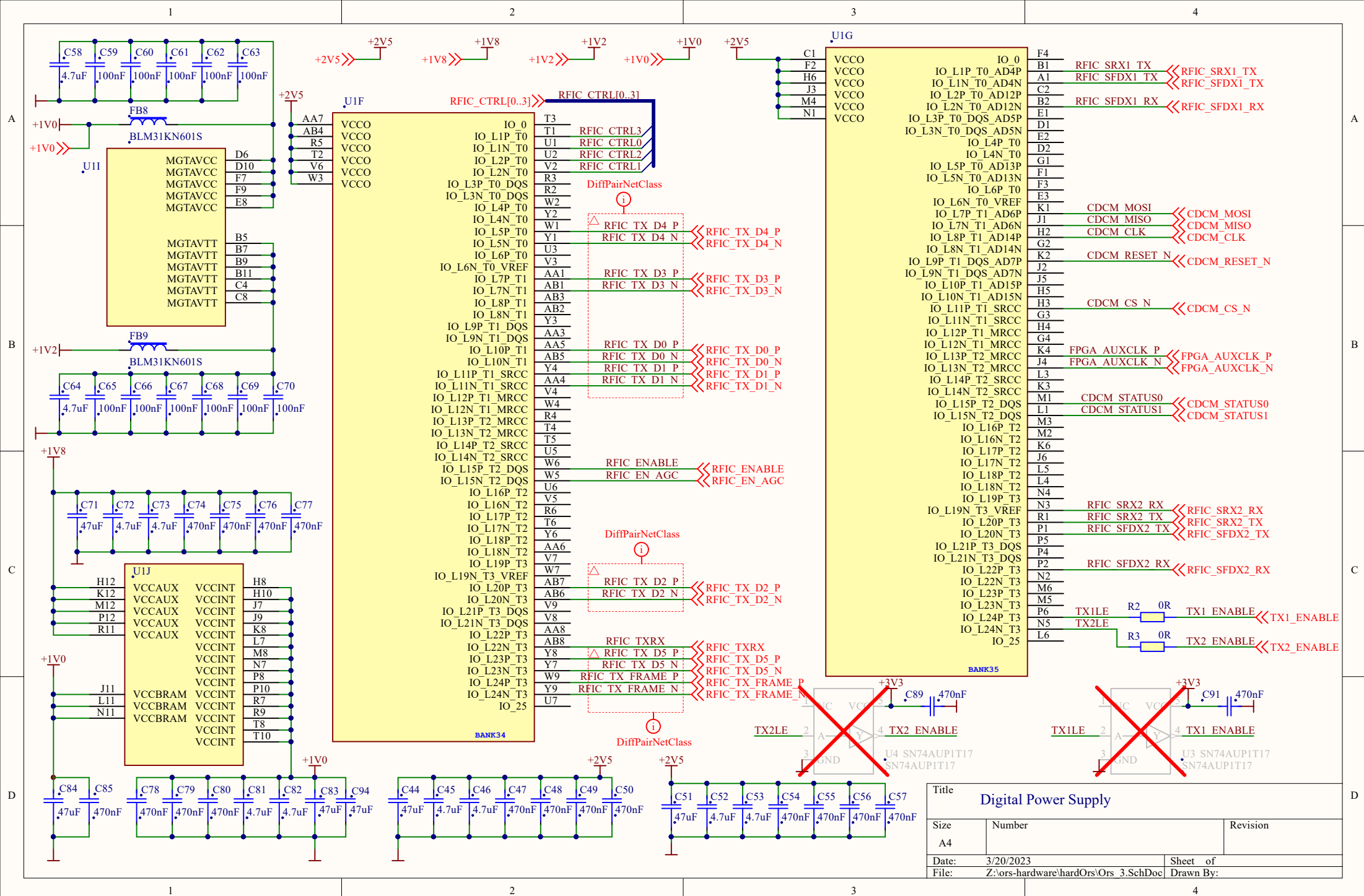
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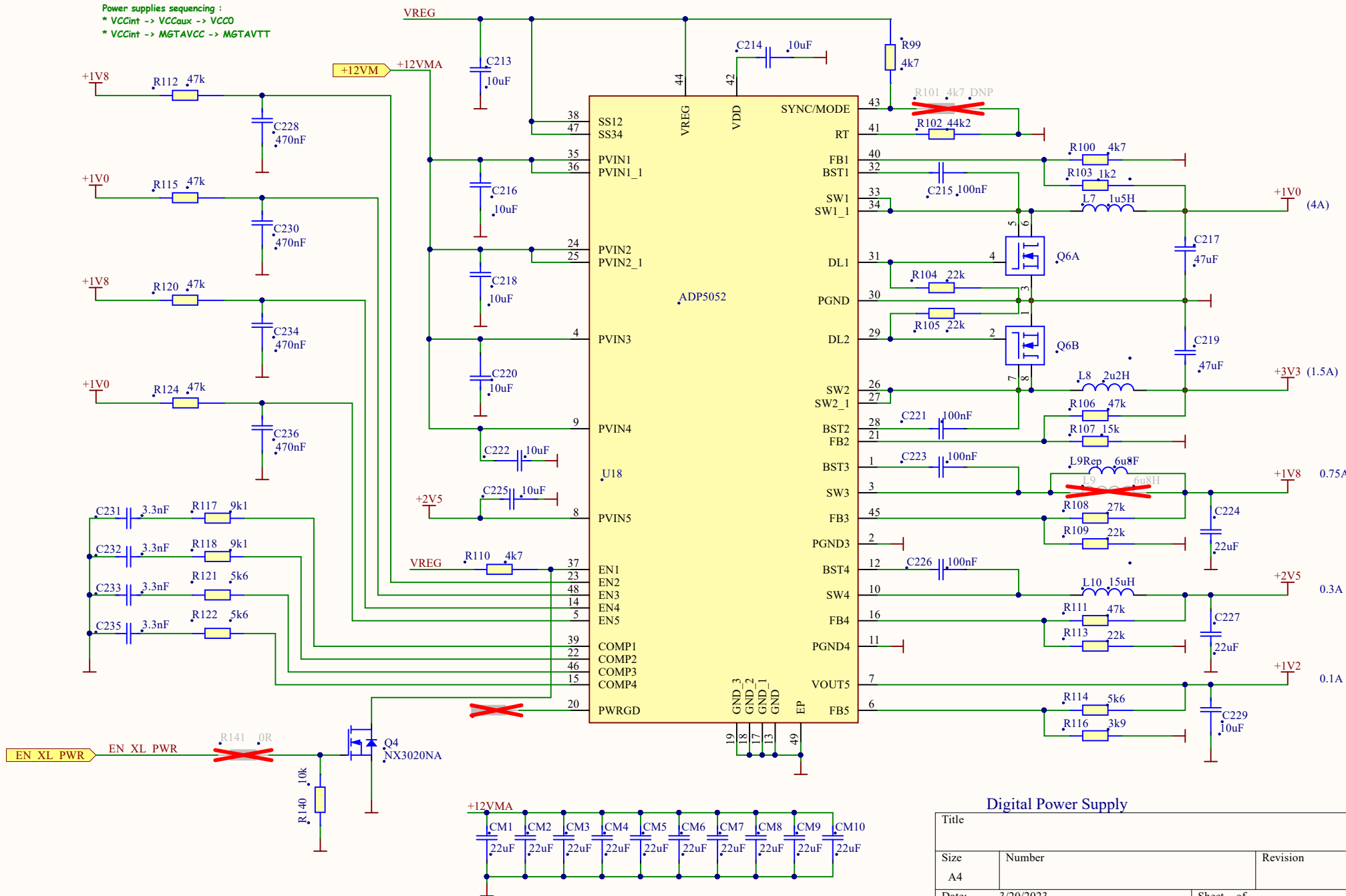




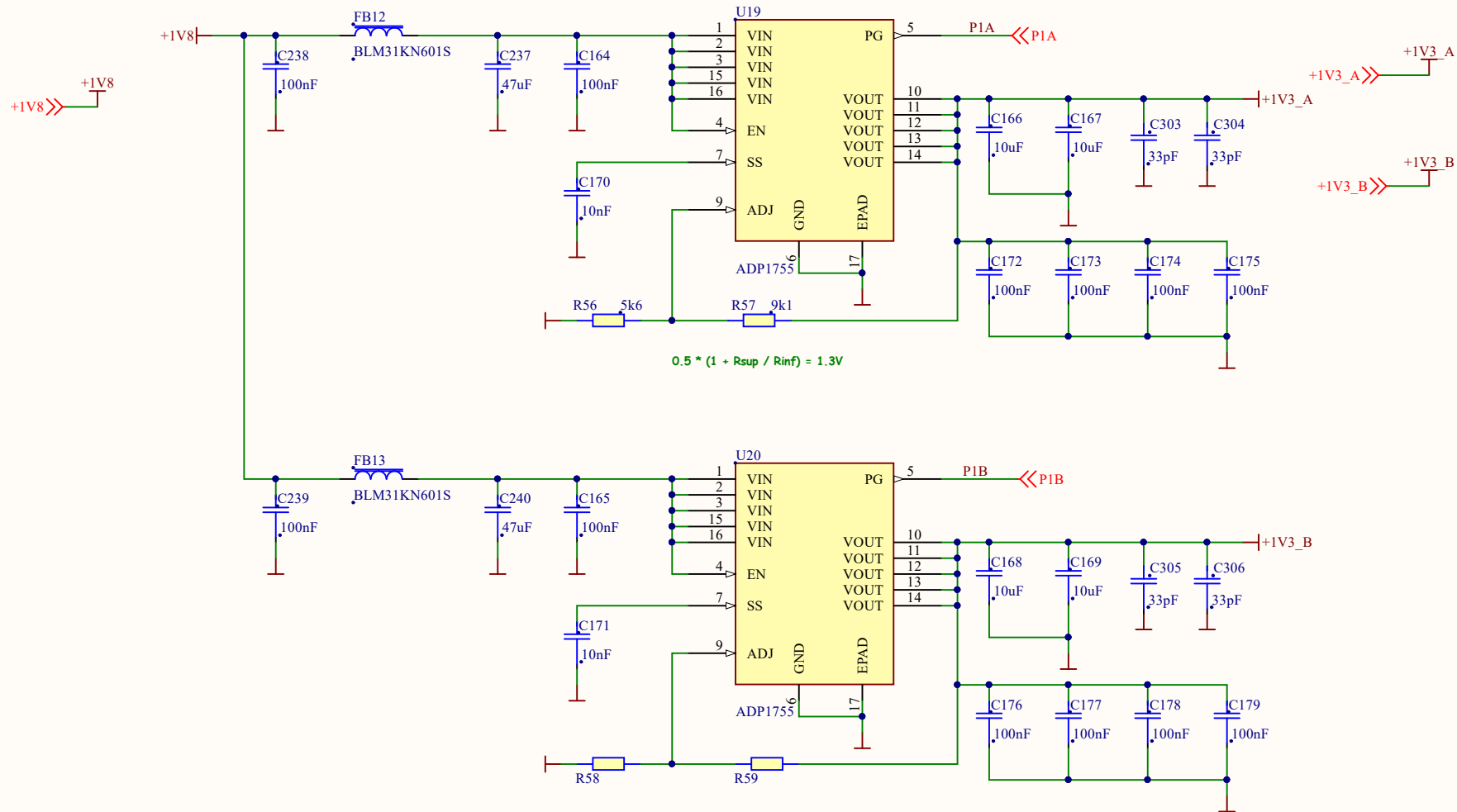




Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT

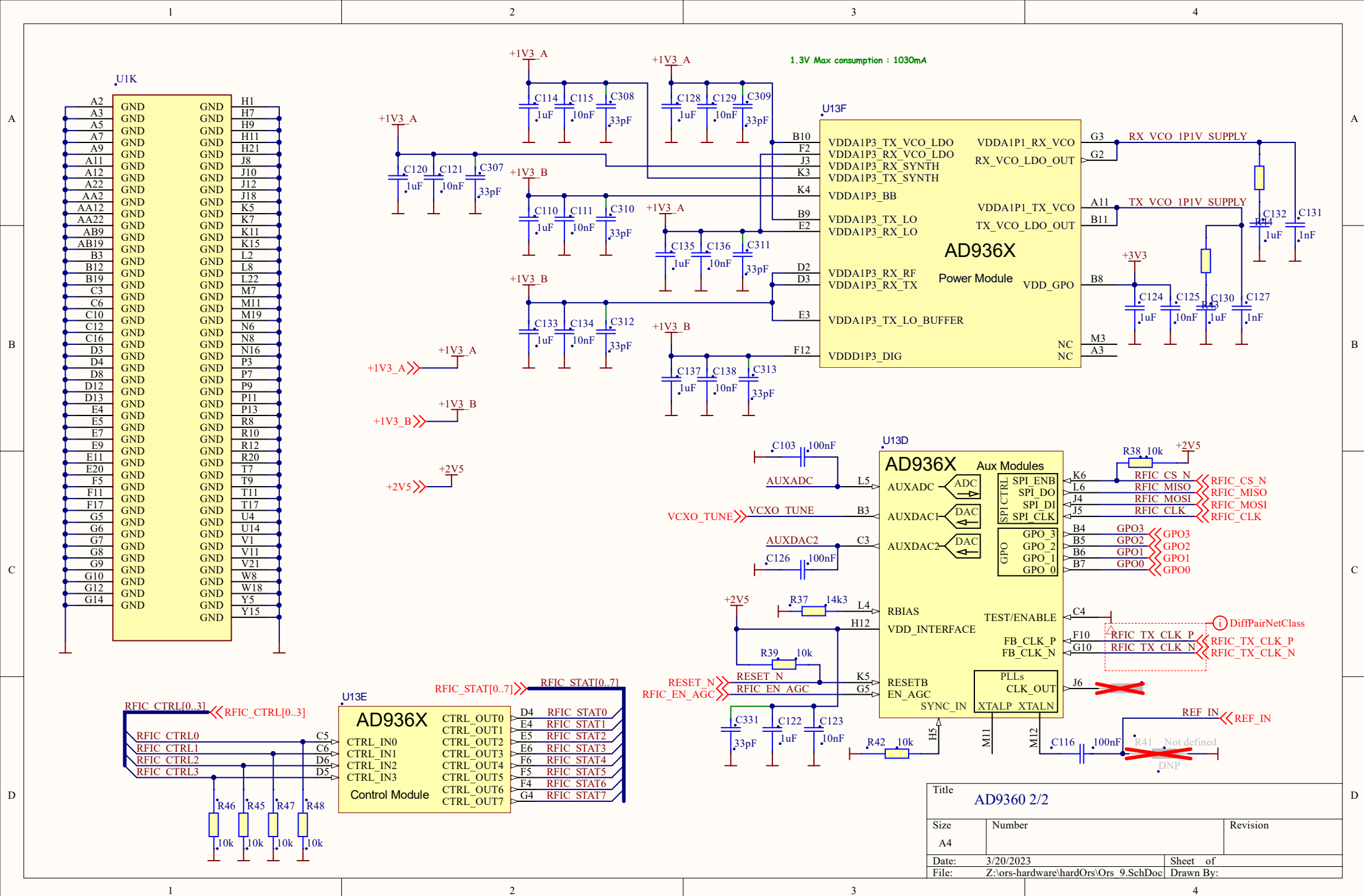


### Digital Power Supply



Title RF Power Supply		
Size A4	Number	Revision
Date: 3/20/2023	Sheet of	
File: Z:\ors-hardware\hardOrs\Ors_7.SchDoc	Drawn By:	

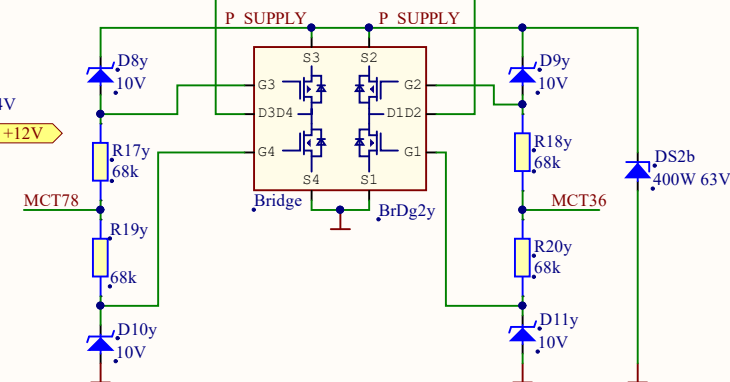
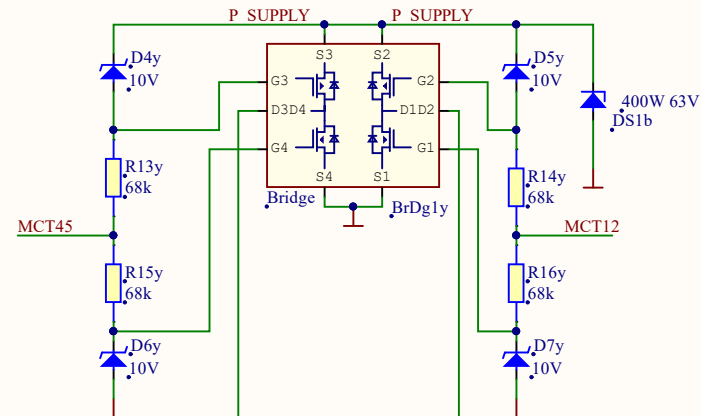
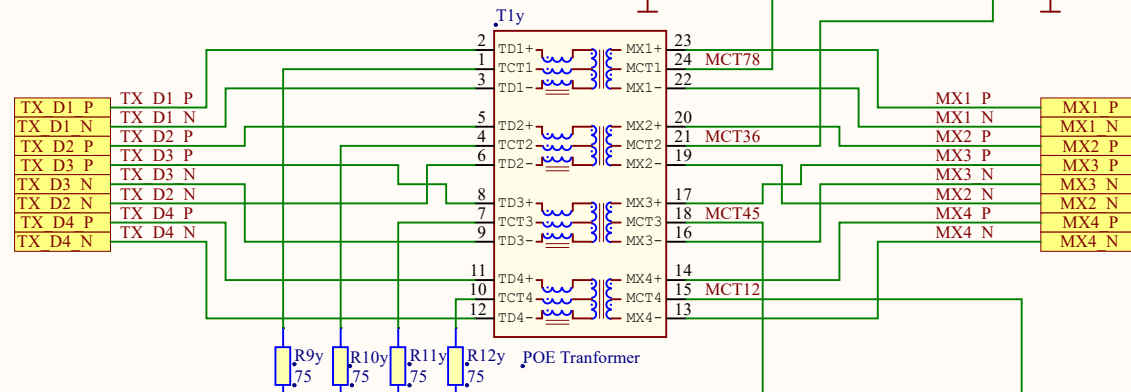
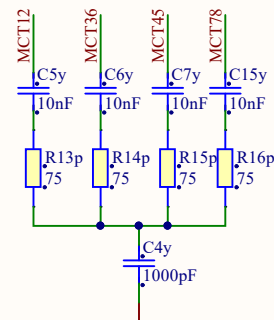
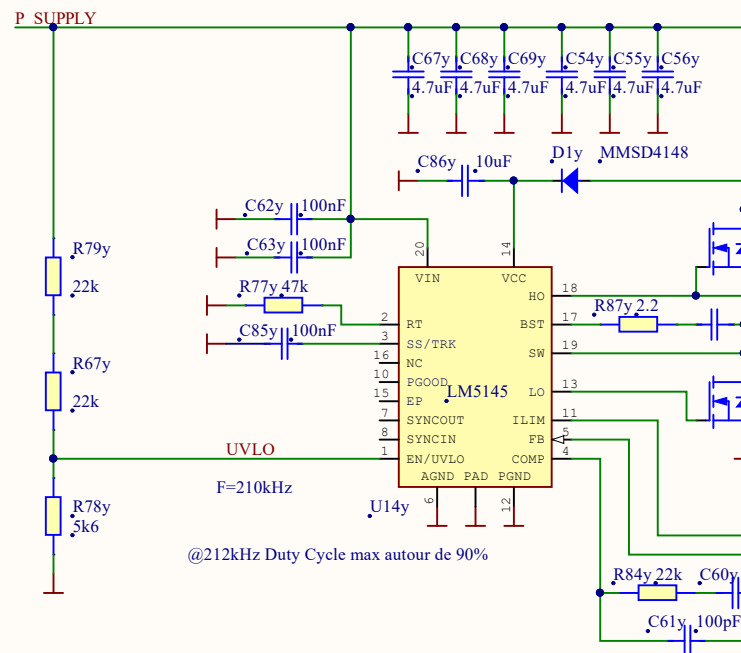
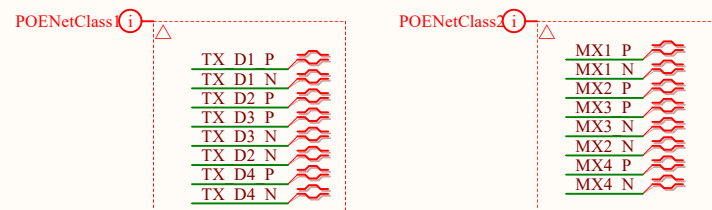
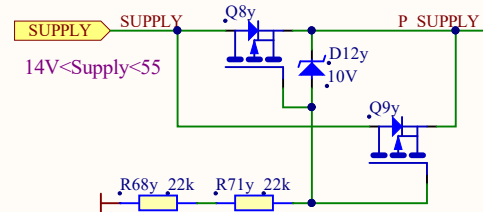




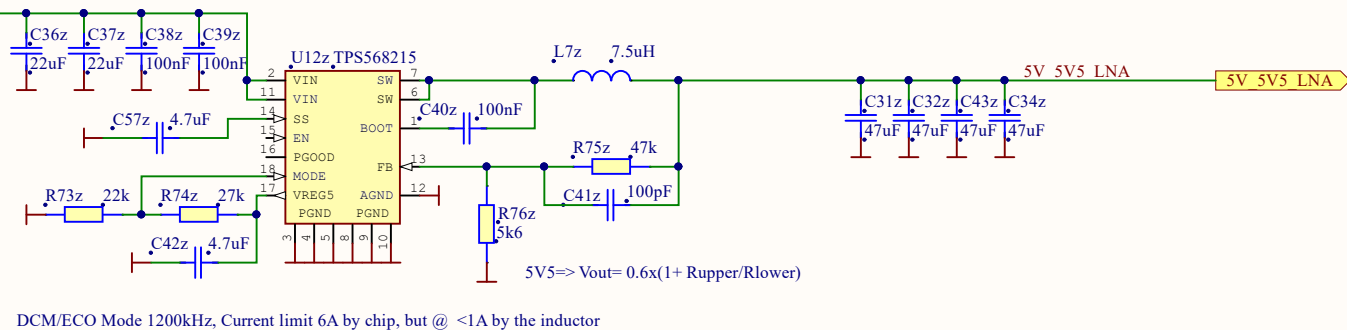
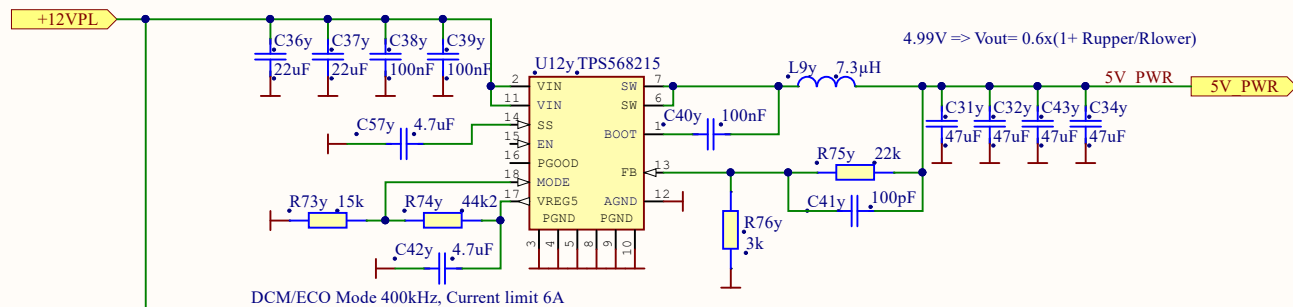




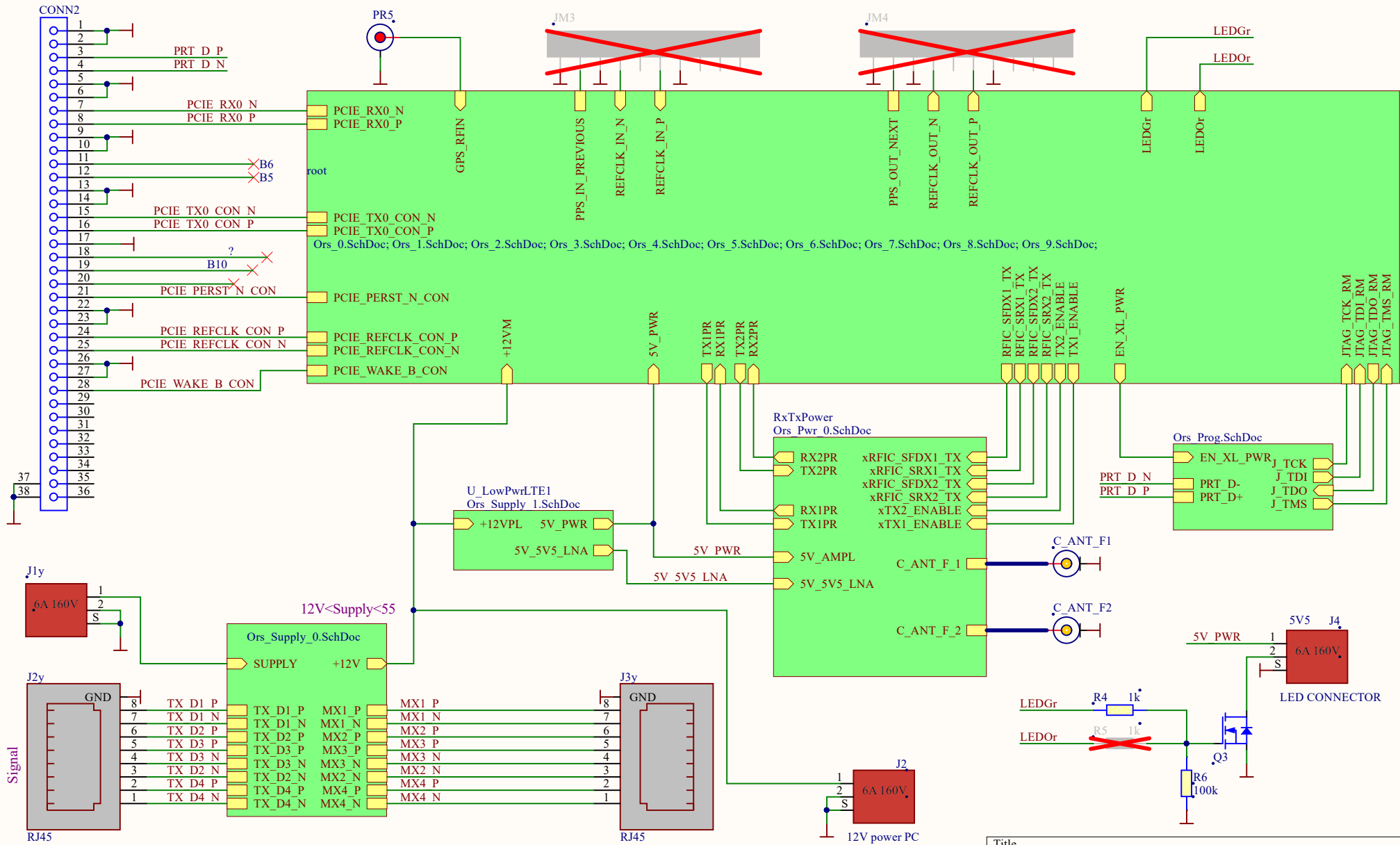




Title <b>POE/5V</b>		
Size <b>A4</b>	Number	Revision
Date: 3/20/2023	Sheet of	
File: Z:\ors-hardware\...\Ors Supply 0.SchDoc	Drawn By:	



Title Main Connectors		
Size A4	Number	Revision
Date: 3/20/2023	Sheet of	
File: Z:\ors-hardware\...\Ors_Supply_1.SchDoc	Drawn By:	



Title		
Main Connectors		
Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_Top.SchDoc	Drawn By: