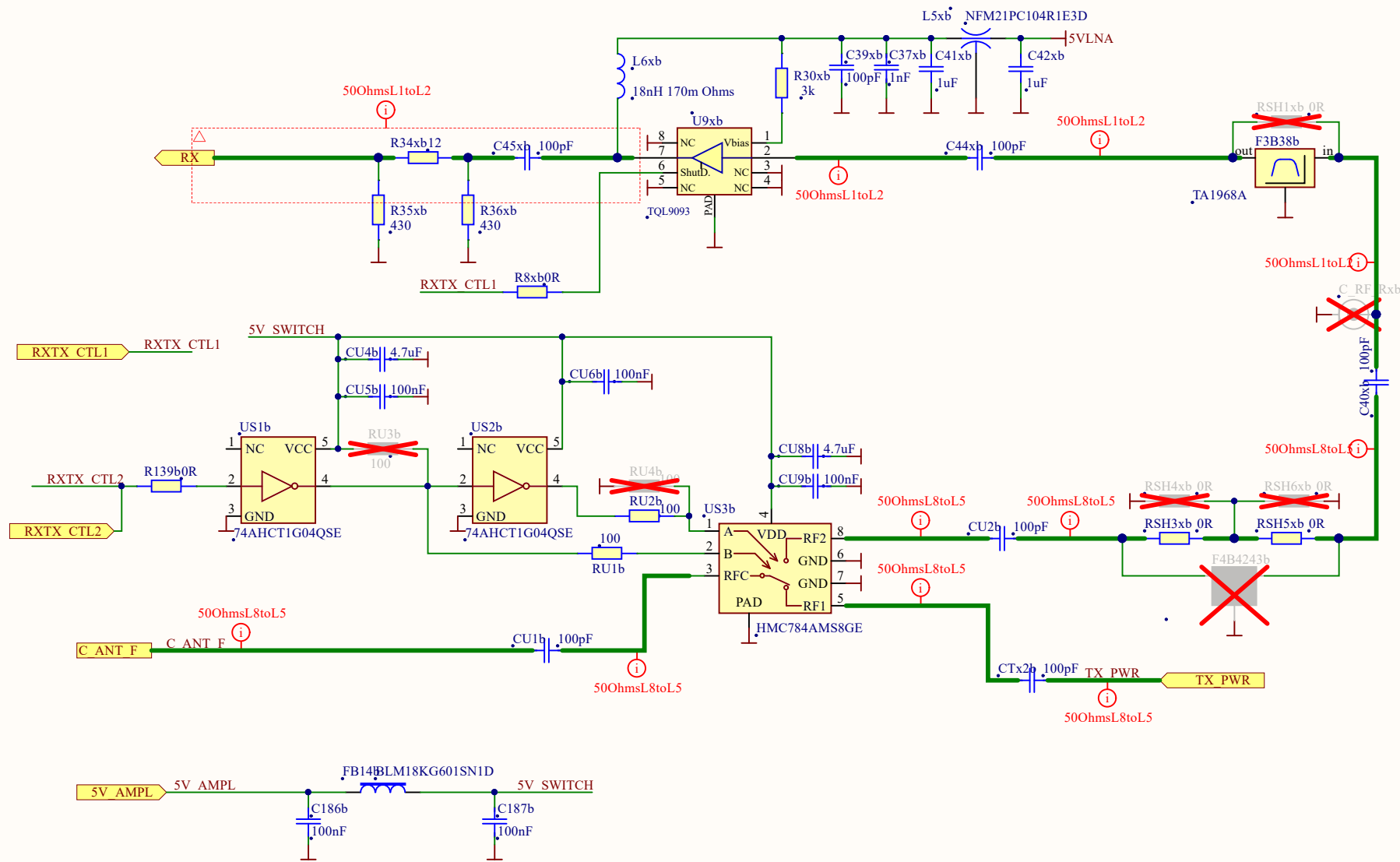
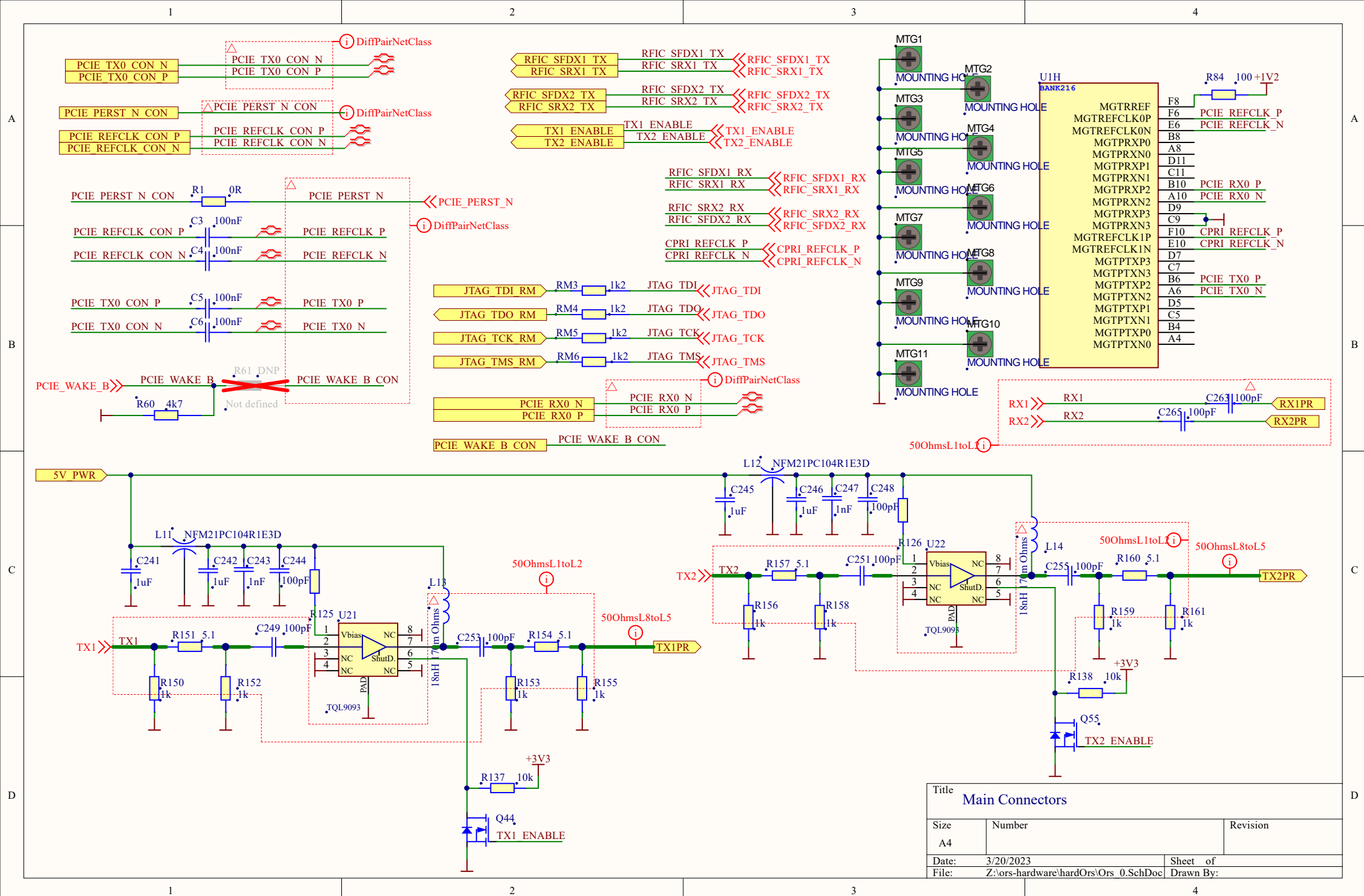
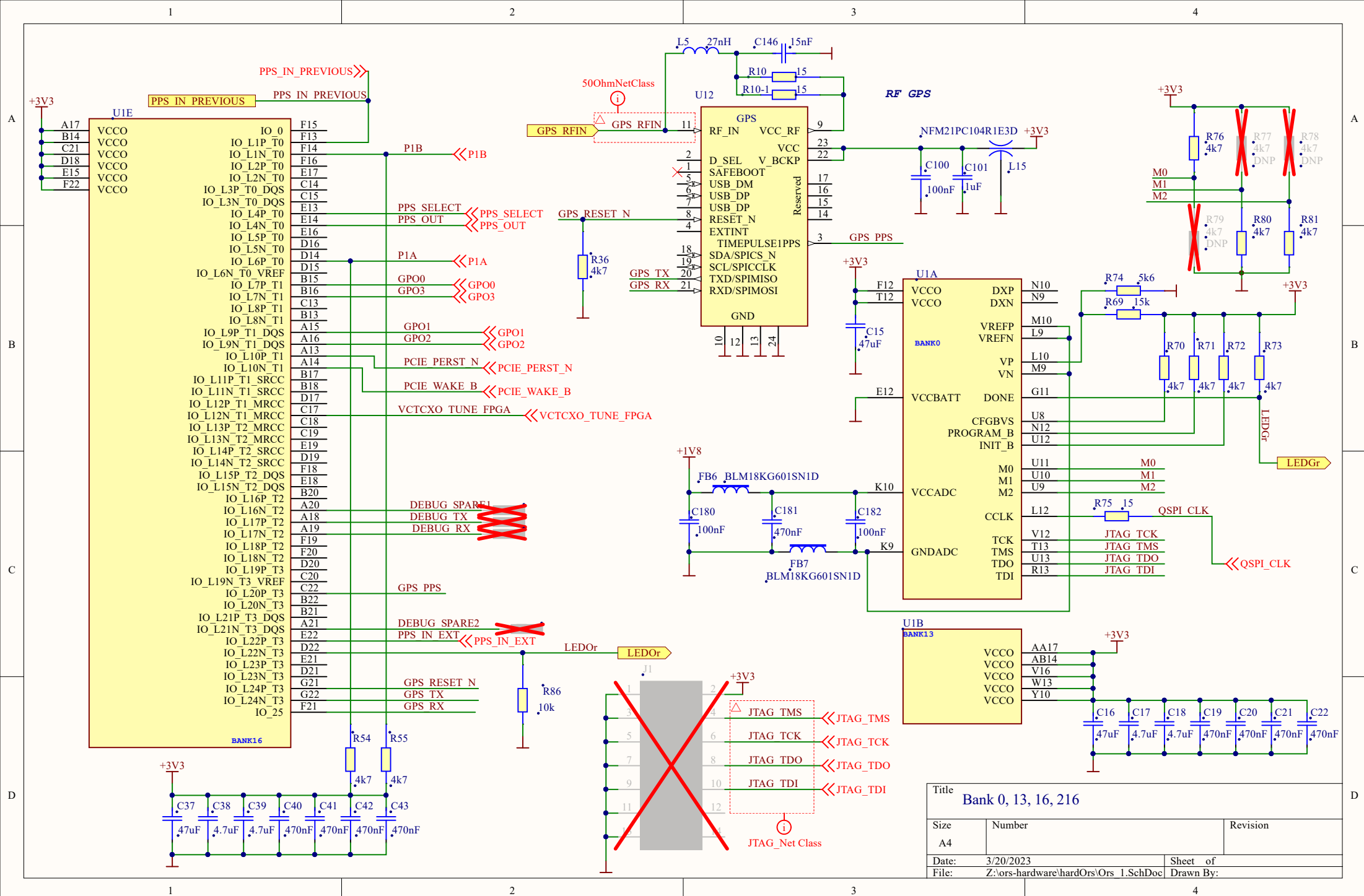


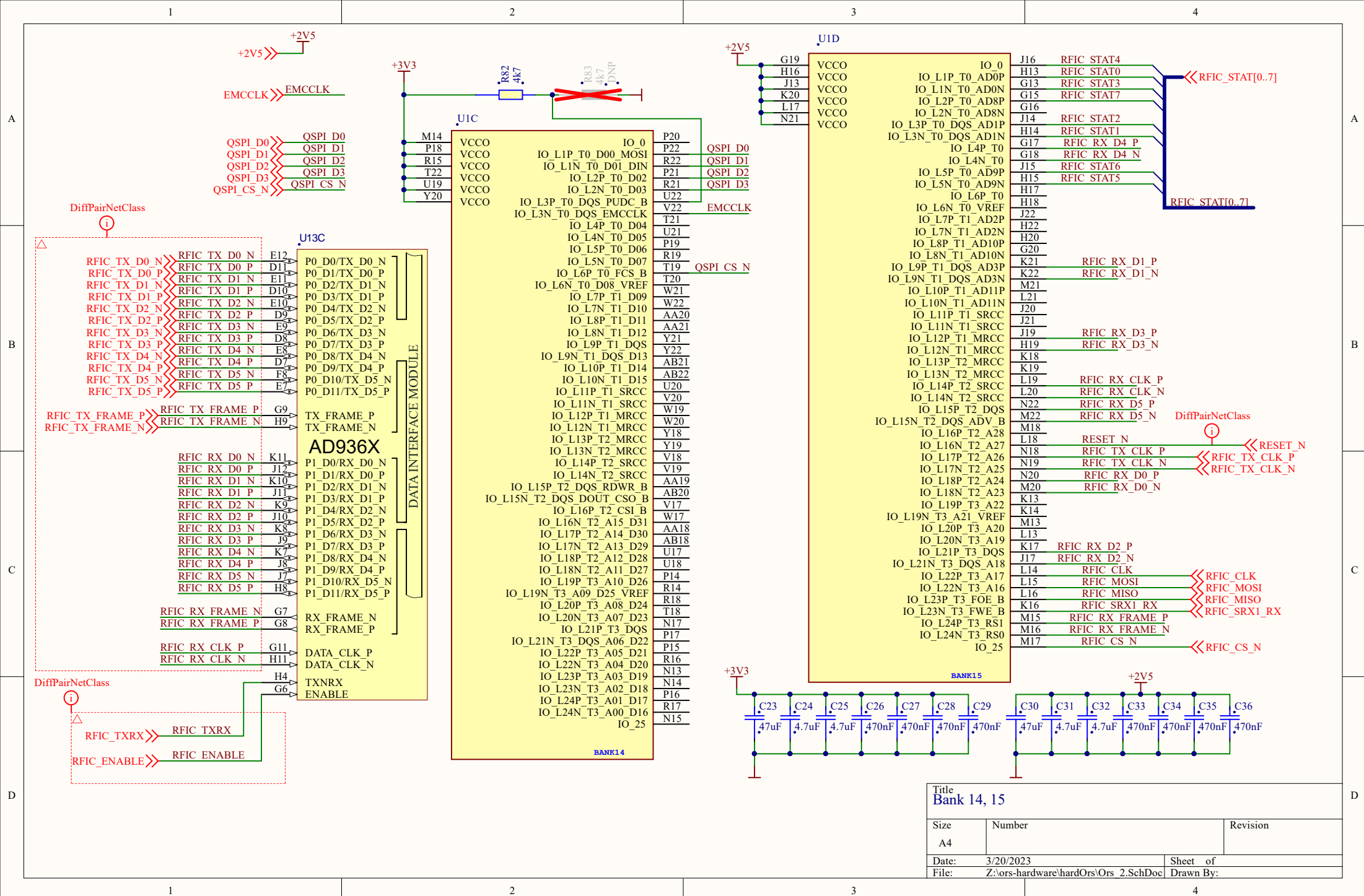
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Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_1.SchDoc	Drawn By:

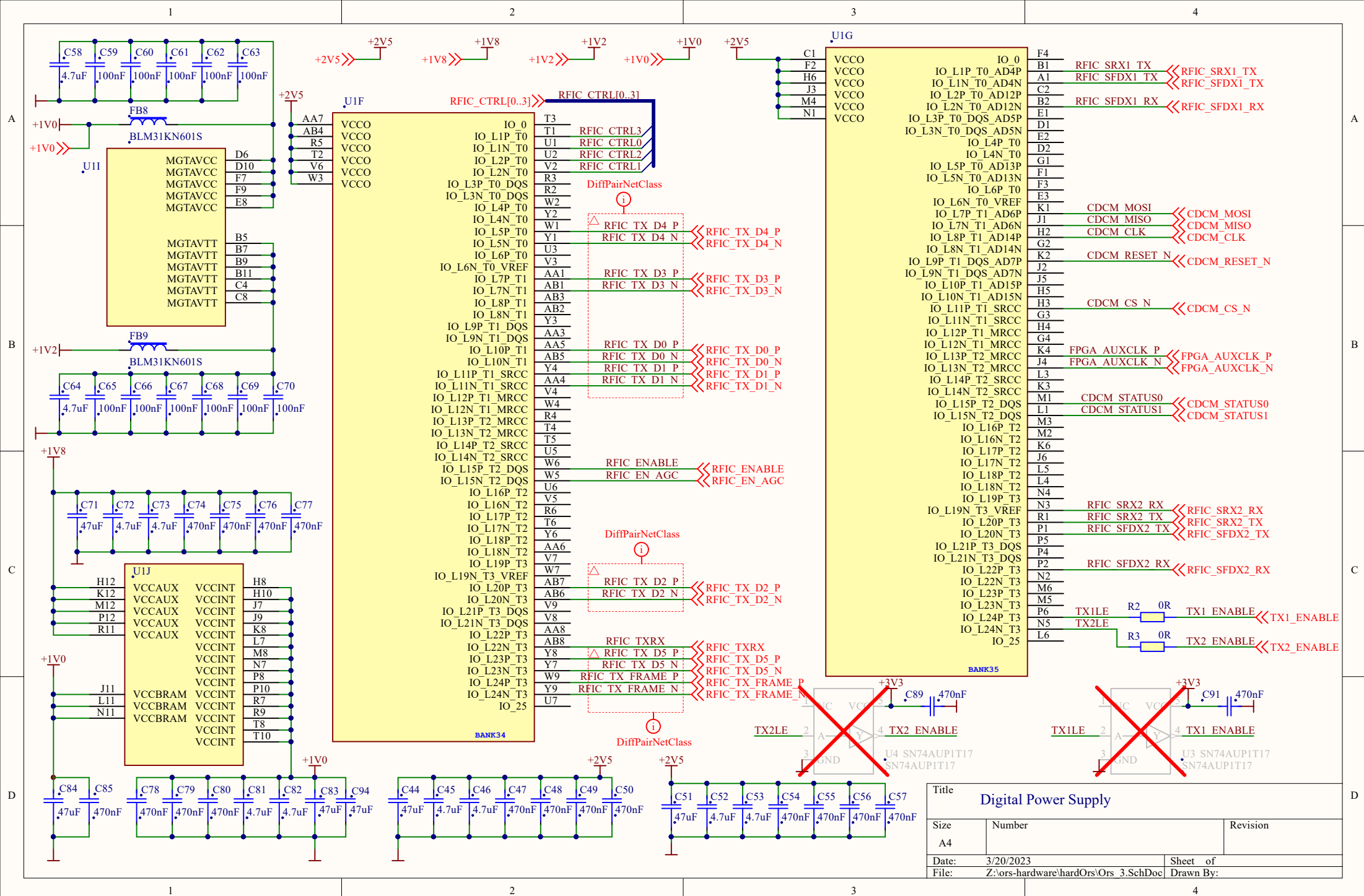


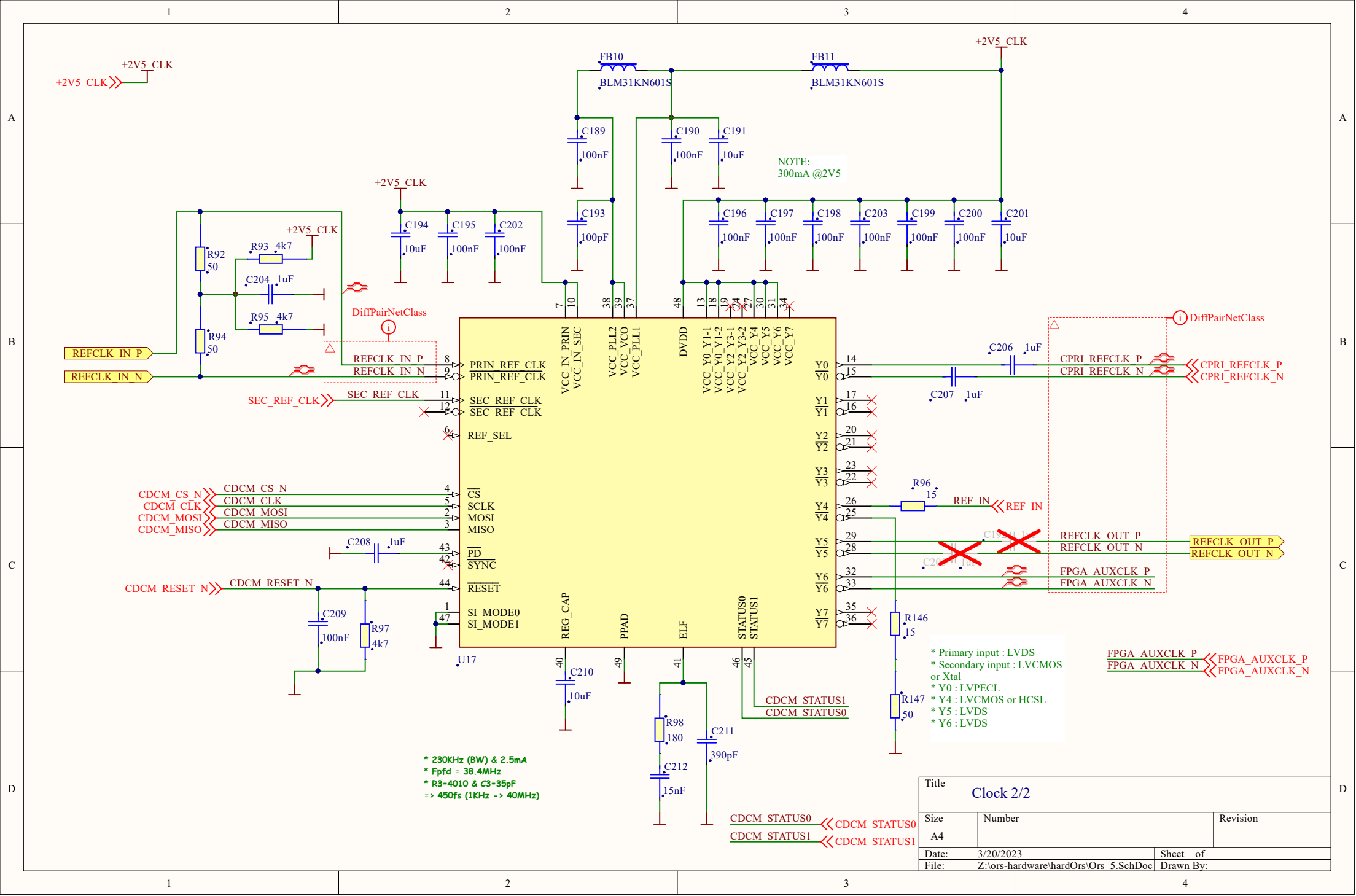
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Date:	3/20/2023	Sheet of
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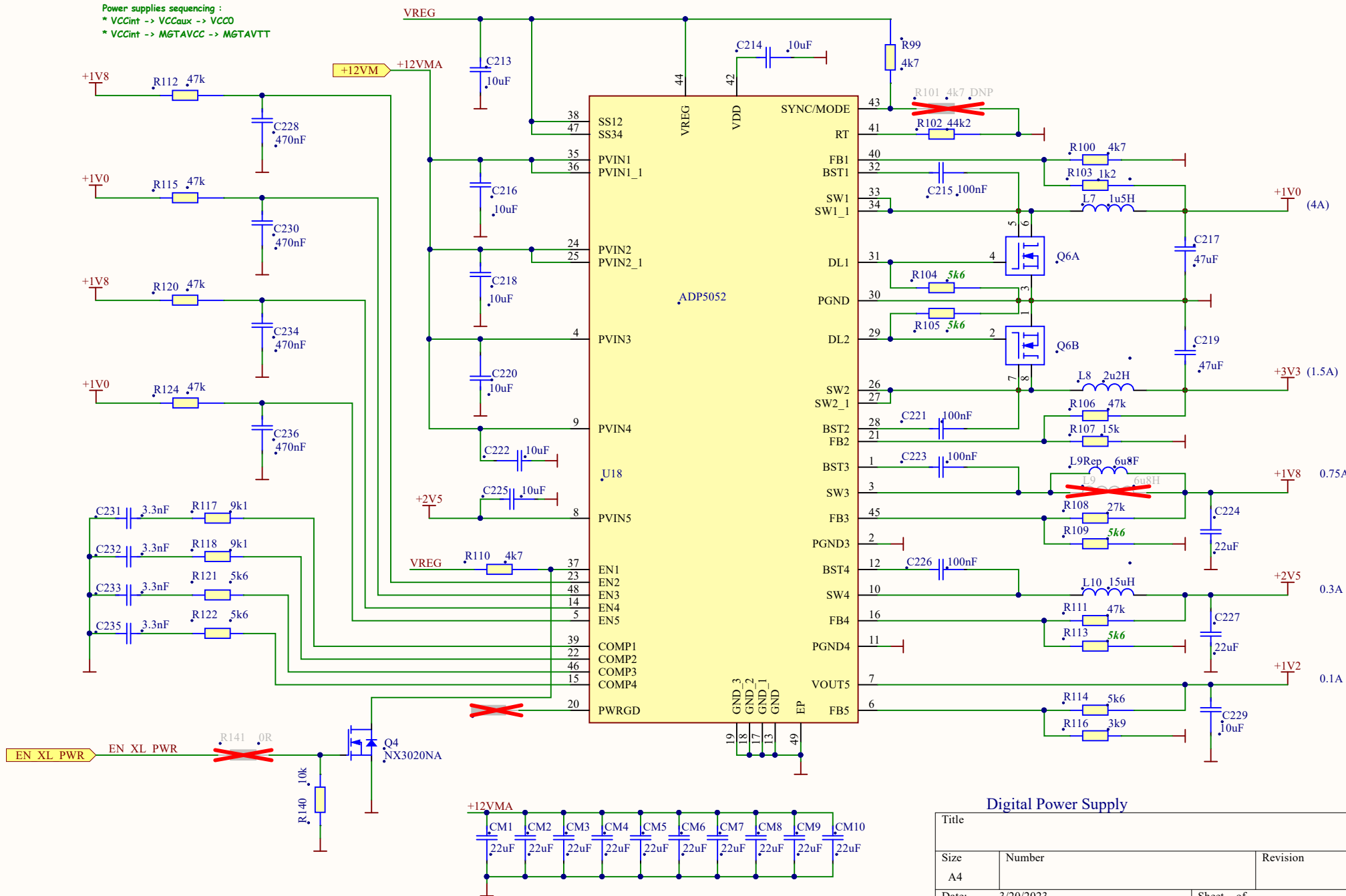






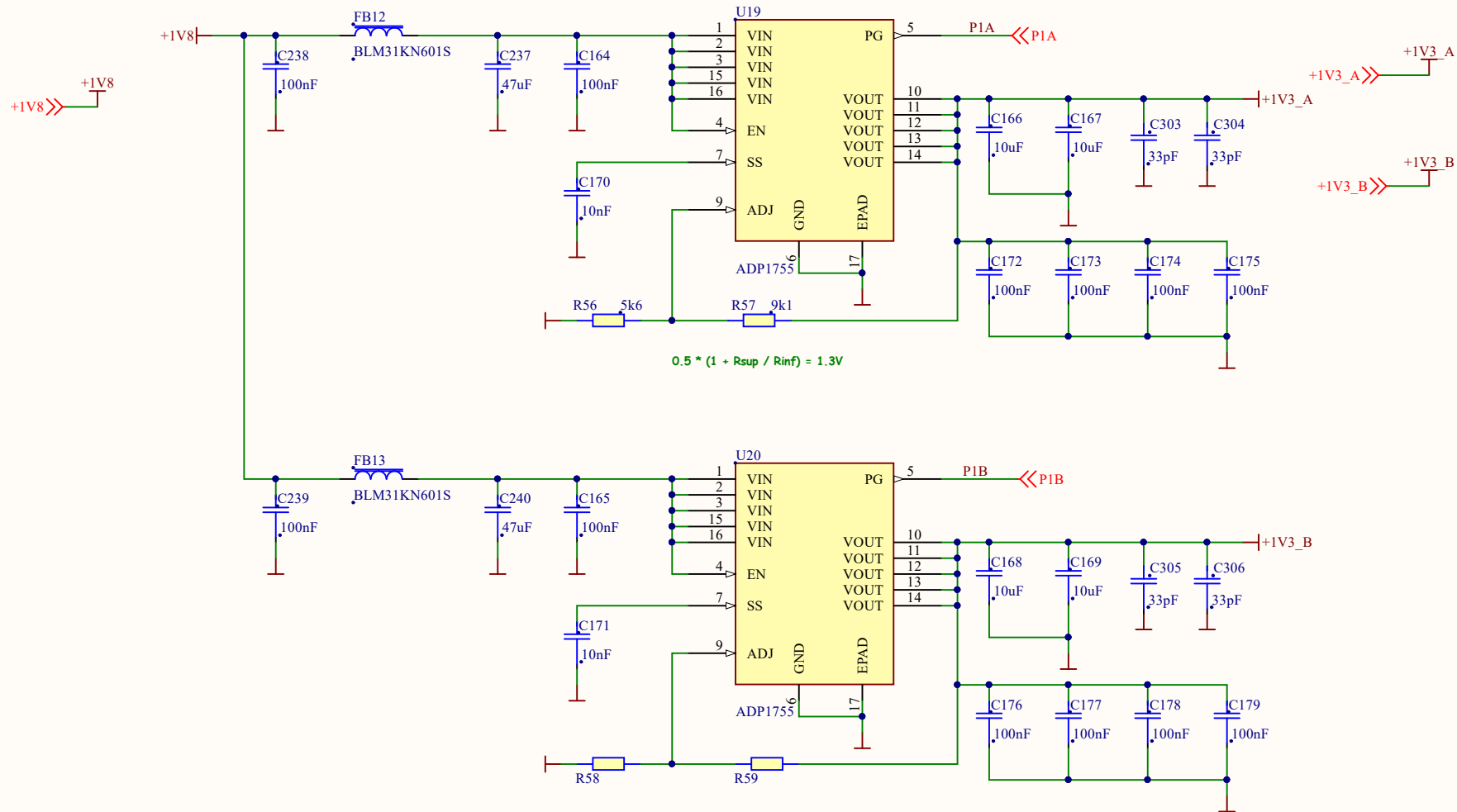


Power supplies sequencing :
* VCCint -> VCCaux -> VCCO
* VCCint -> MGTAVCC -> MGTAVTT



Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_6.SchDoc	Drawn By:



Title RF Power Supply		
Size A4	Number	Revision
Date: 3/20/2023	Sheet of	
File: Z:\ors-hardware\hardOrs\Ors_7.SchDoc	Drawn By:	

A

B

C

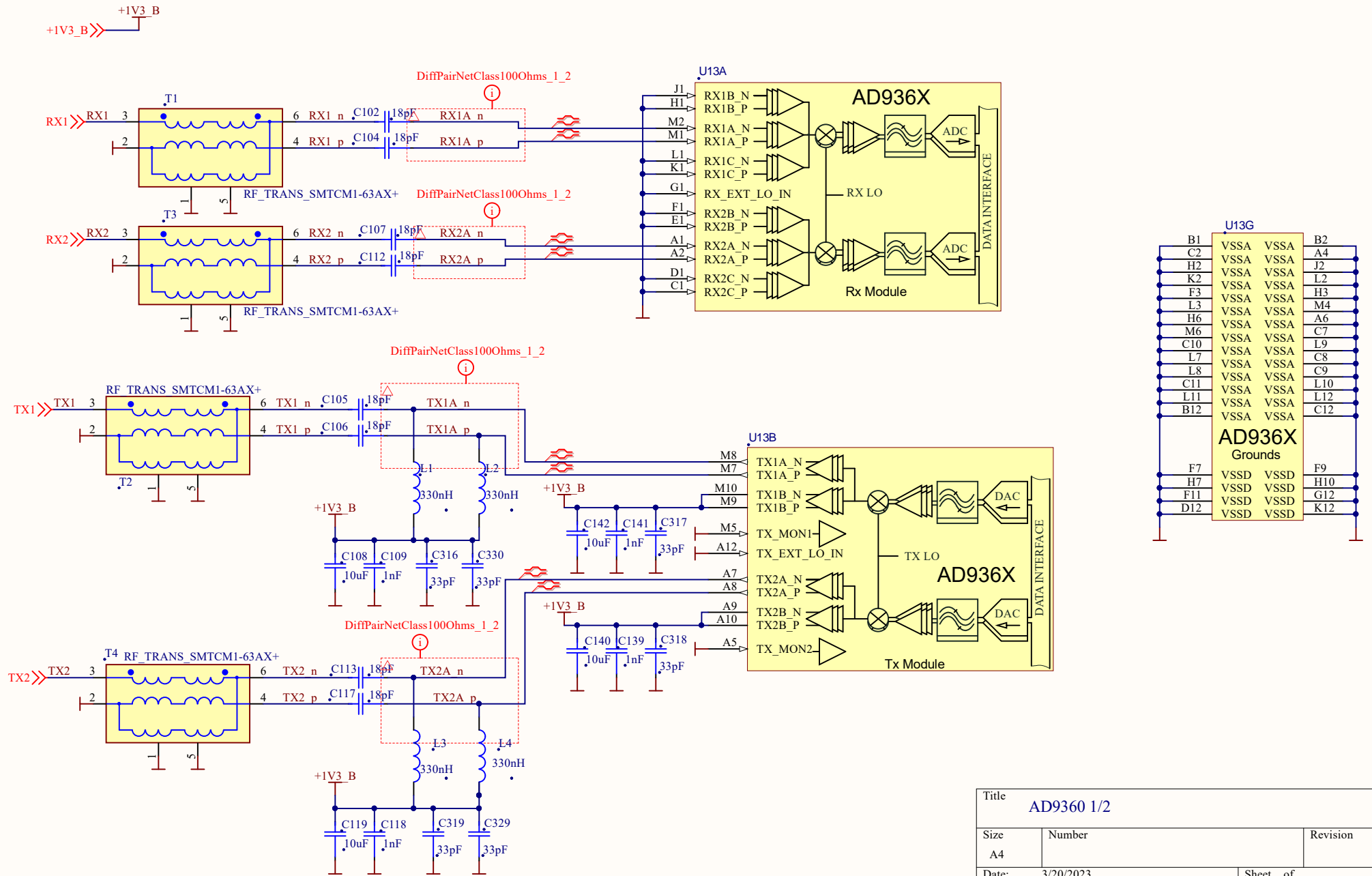
D

A

B

C

D



Title		
AD9360 1/2		
Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_8.SchDoc	Drawn By:

A

A

B

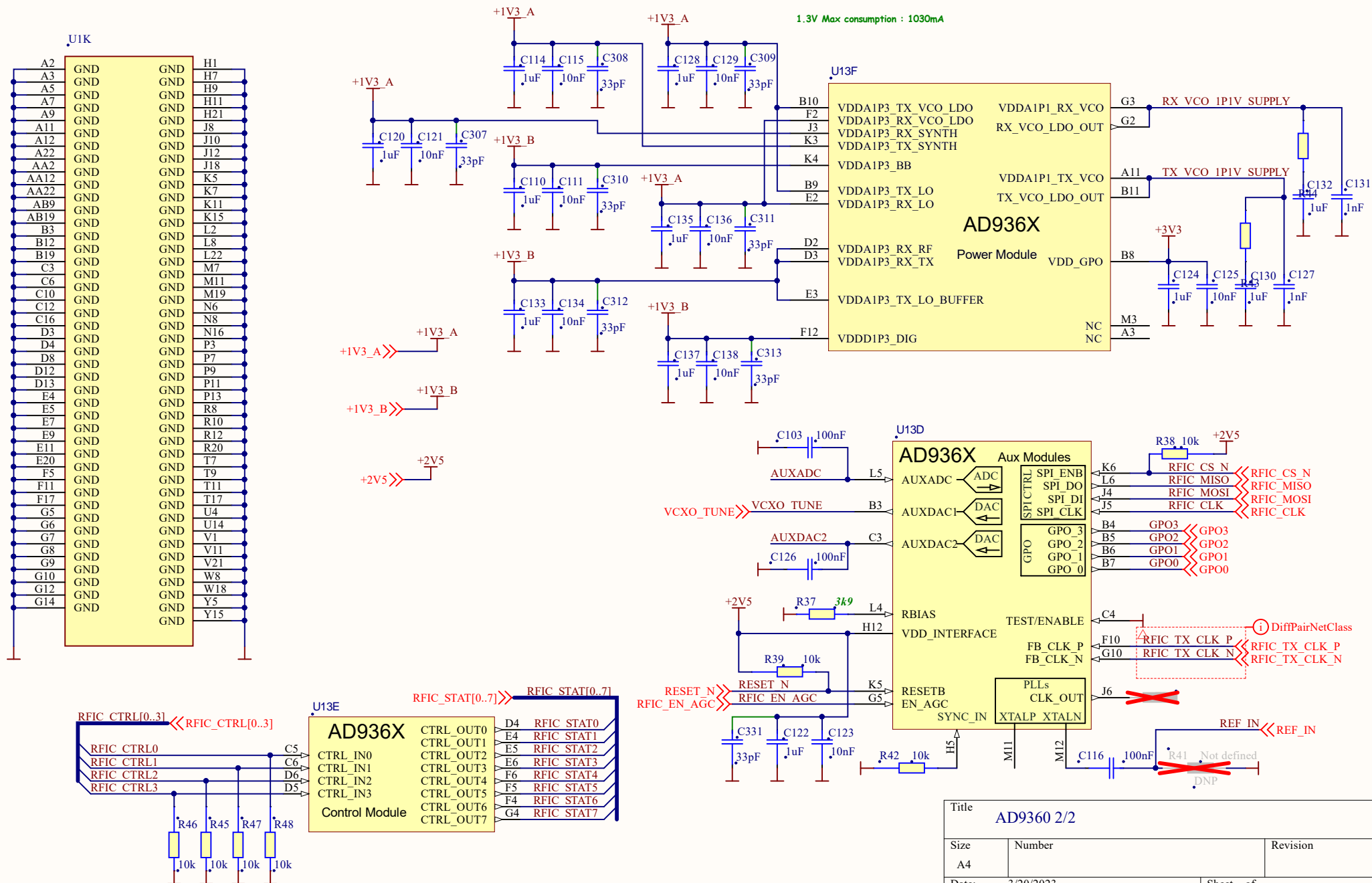
B

C

C

D

D



Title		
AD9360 2/2		
Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
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A

B

C

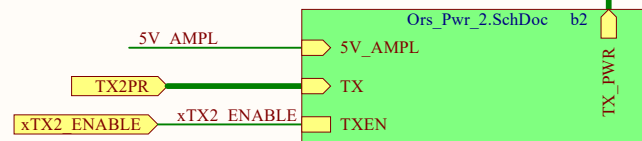
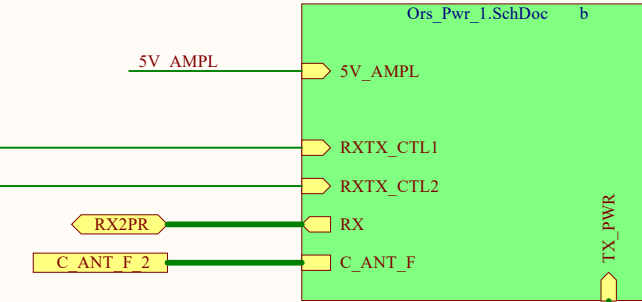
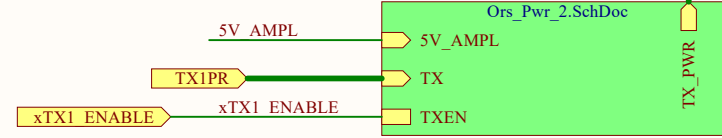
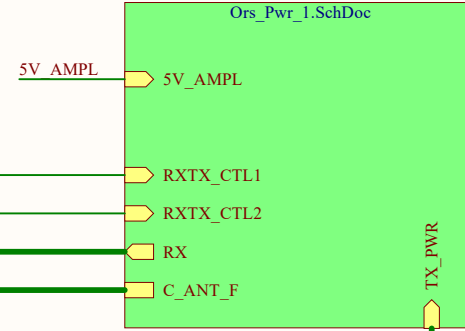
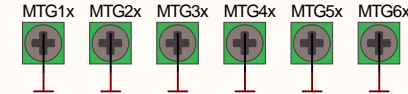
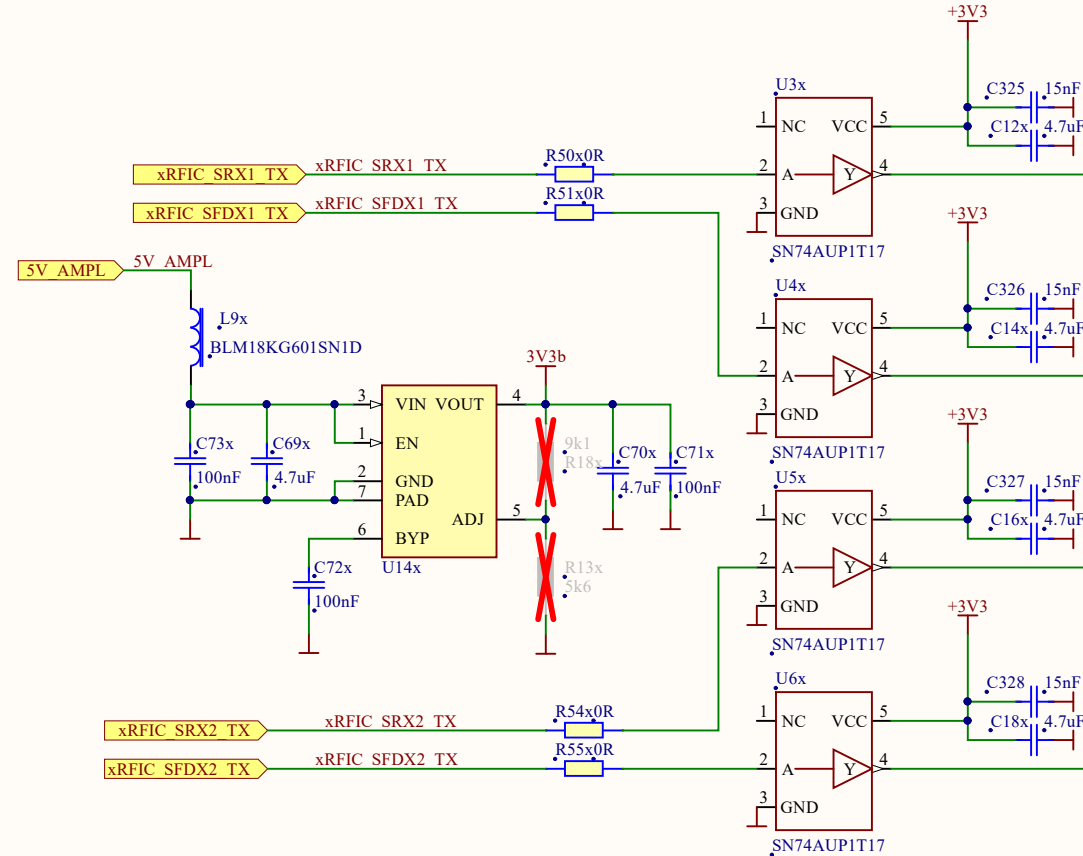
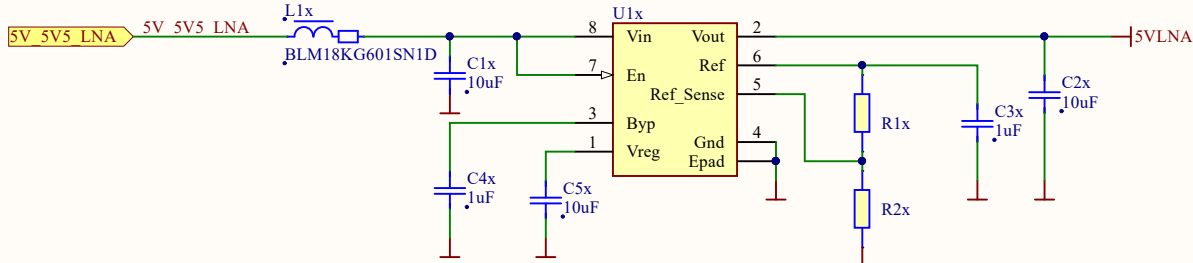
D

A

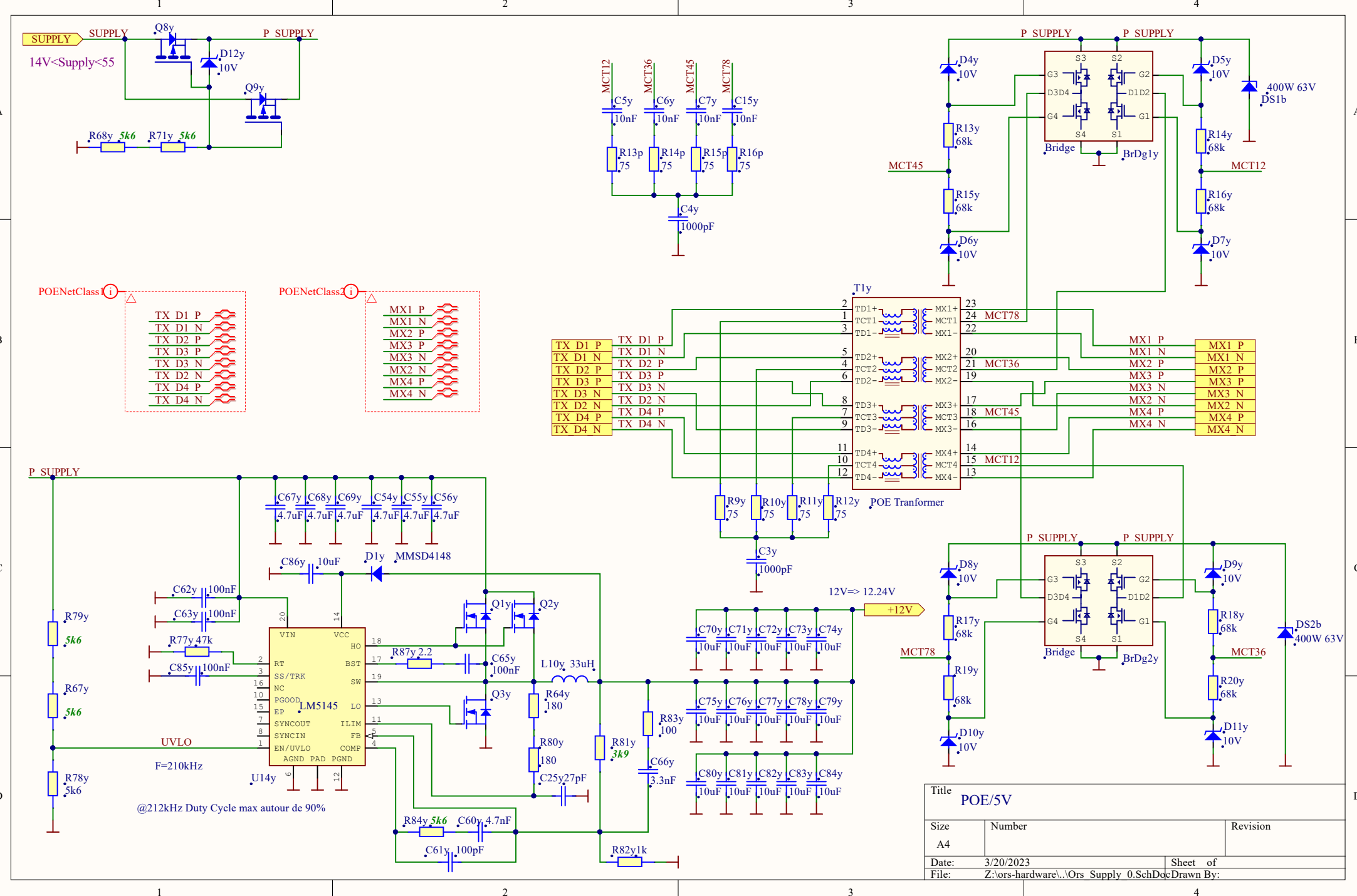
B

C

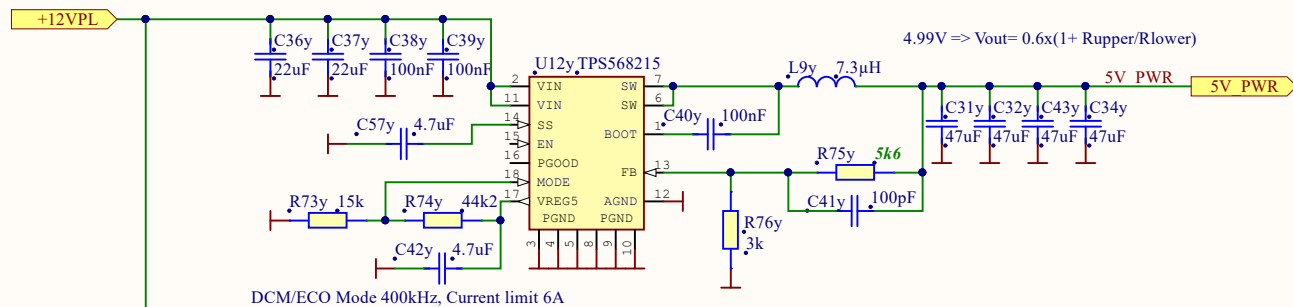
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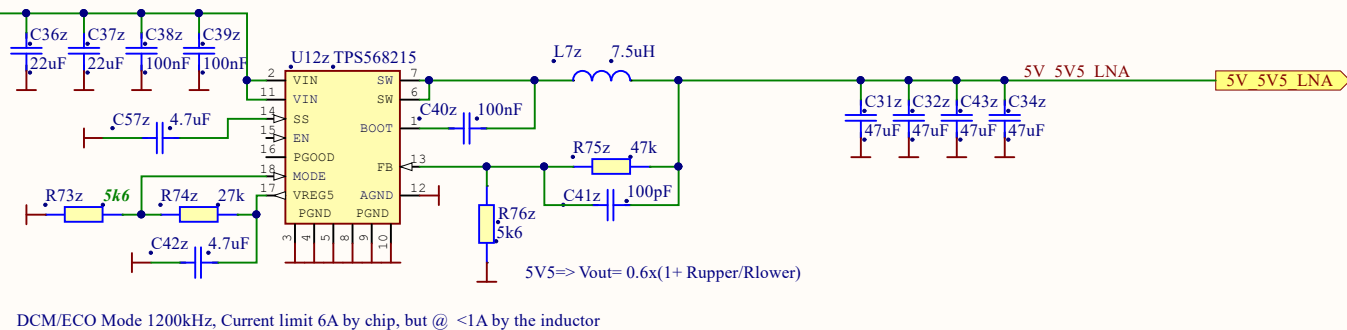
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Size	Number	Revision
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Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_0.SchDoc	Drawn By:



Title		
POE/5V		
Size	Number	Revision
A4		
Date:	3/20/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Supply_0.SchDoc	Drawn By:



Soft Start
 $TSS(seconde) = CSS \times 0.6/6e-6 \Rightarrow 0.47s$



Title Main Connectors		
Size A4	Number	Revision
Date: 3/20/2023	Sheet of	
File: Z:\ors-hardware\..Ors Supply 1.SchDoc	Drawn By:	

