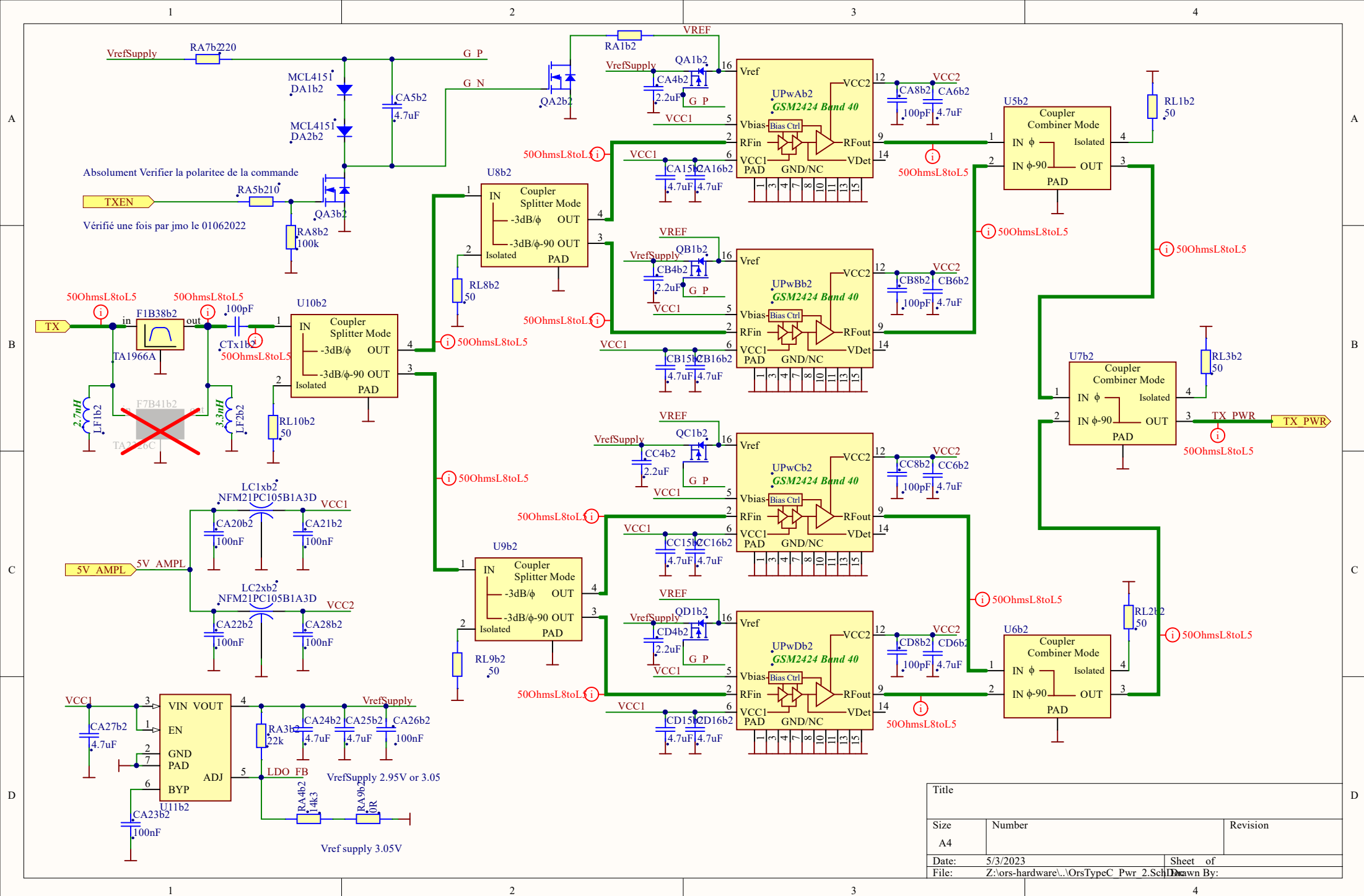
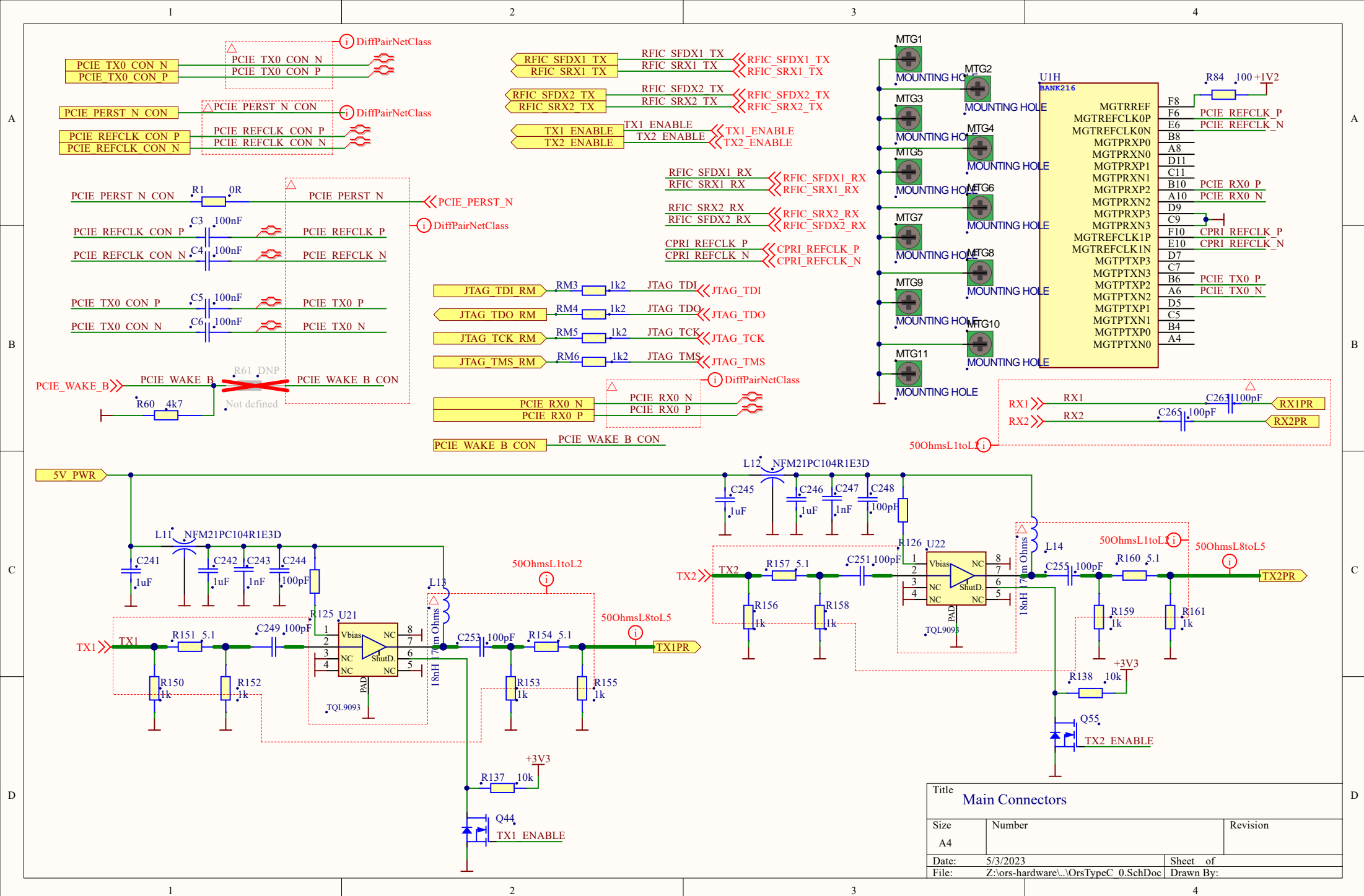
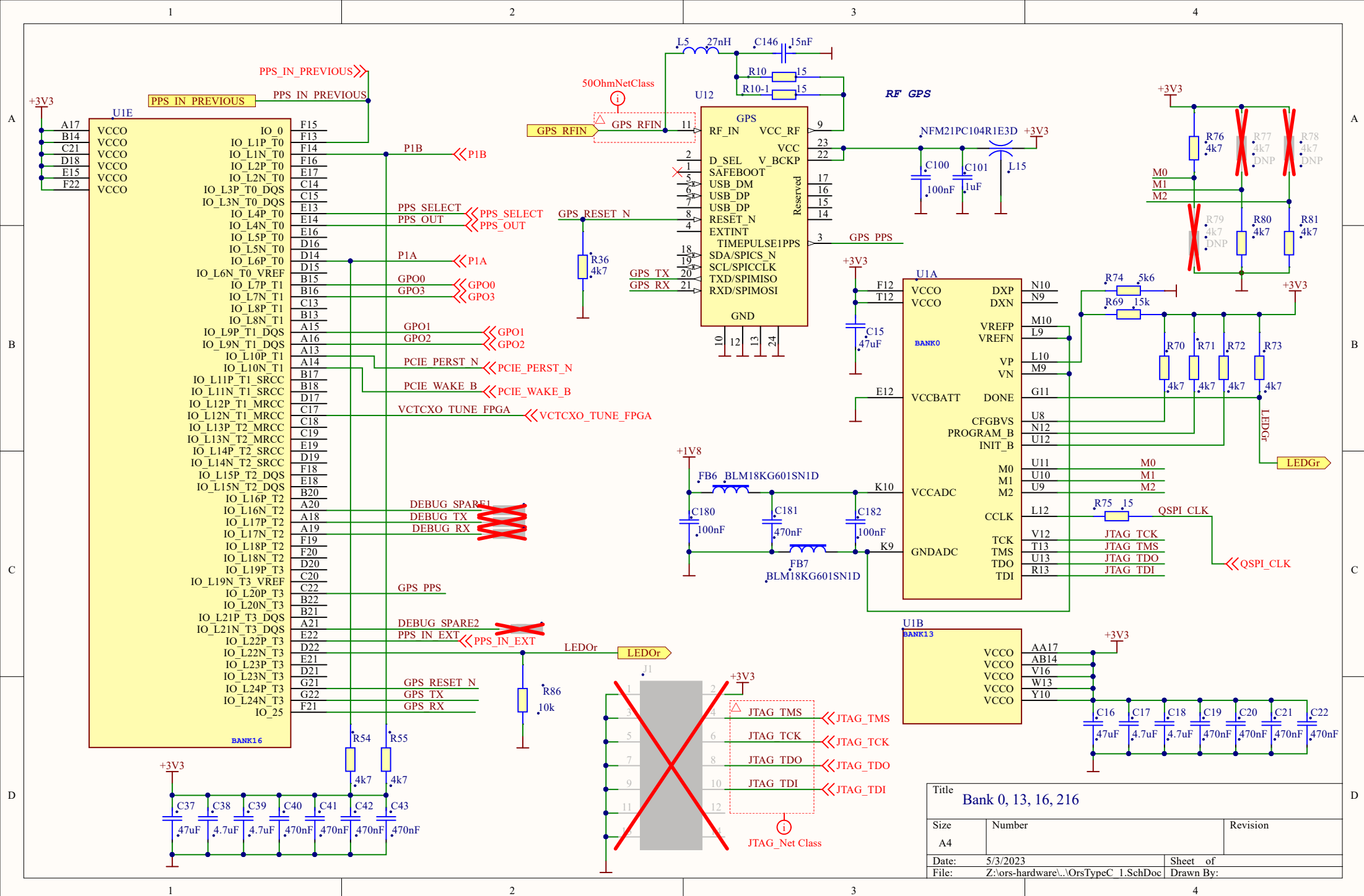


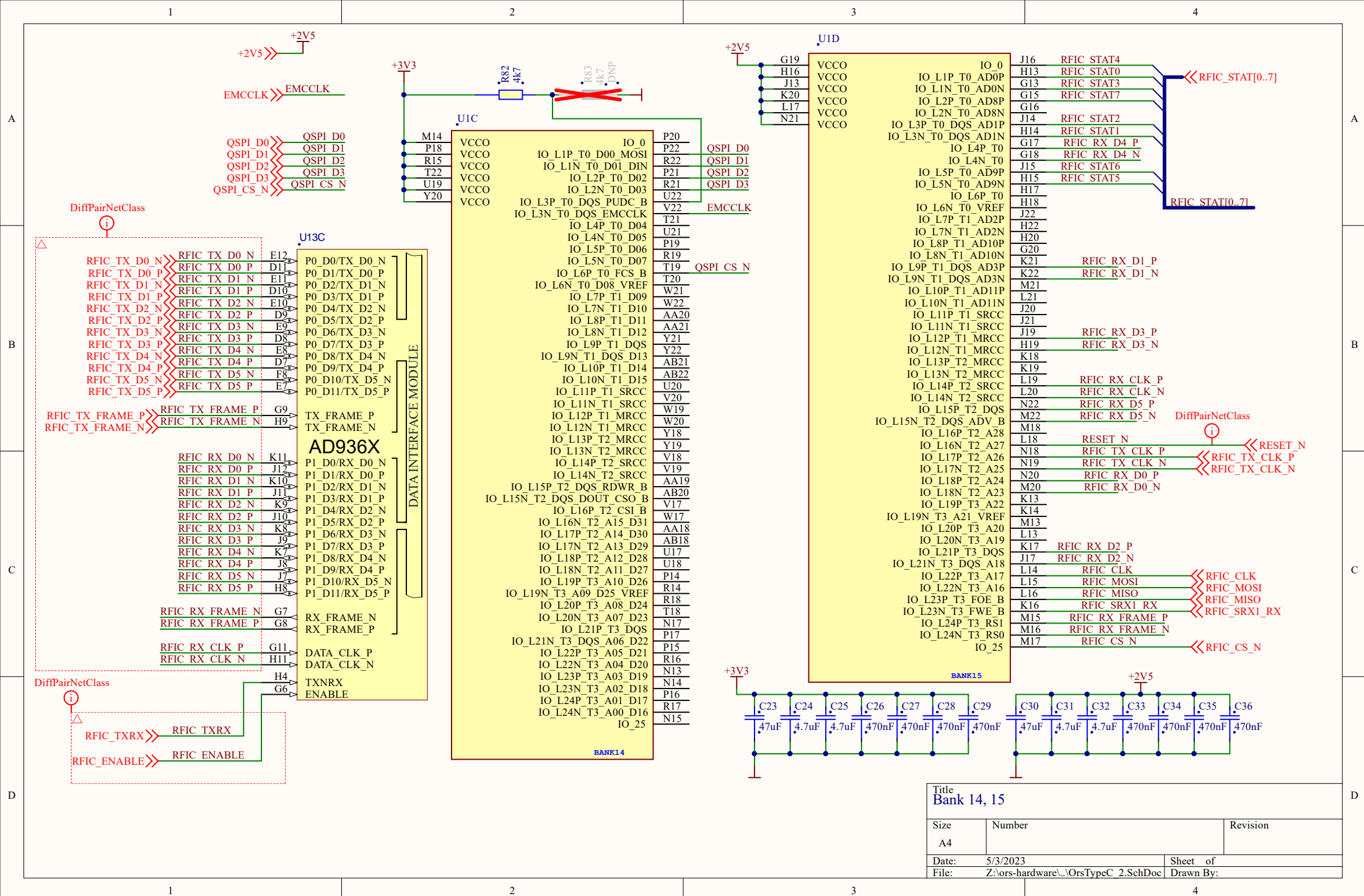
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Date:	5/3/2023	Sheet of
File:	Z:\ors-hardware\...\OrsTypeC Pwr 1.Sch	Drawn By:

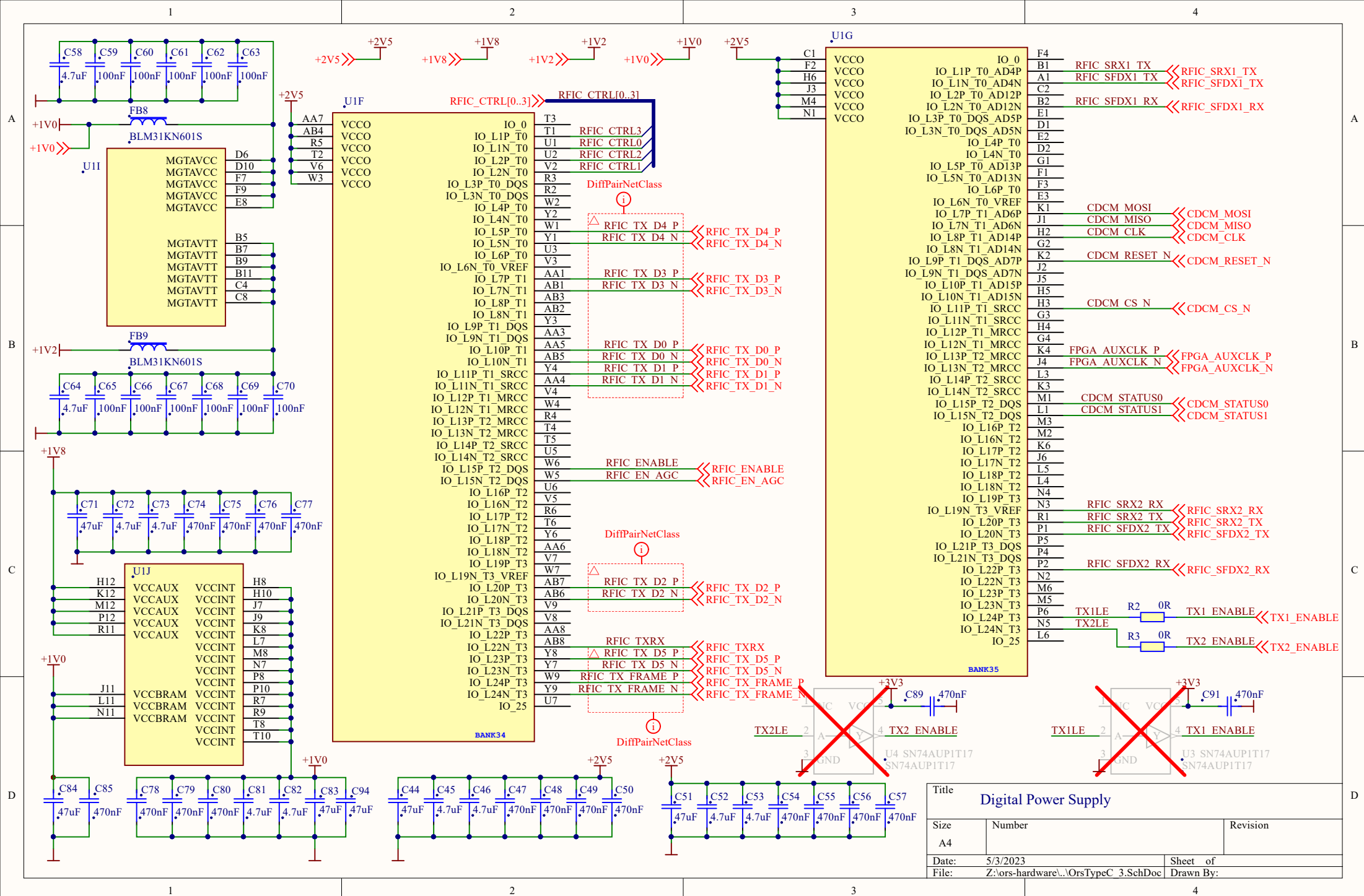








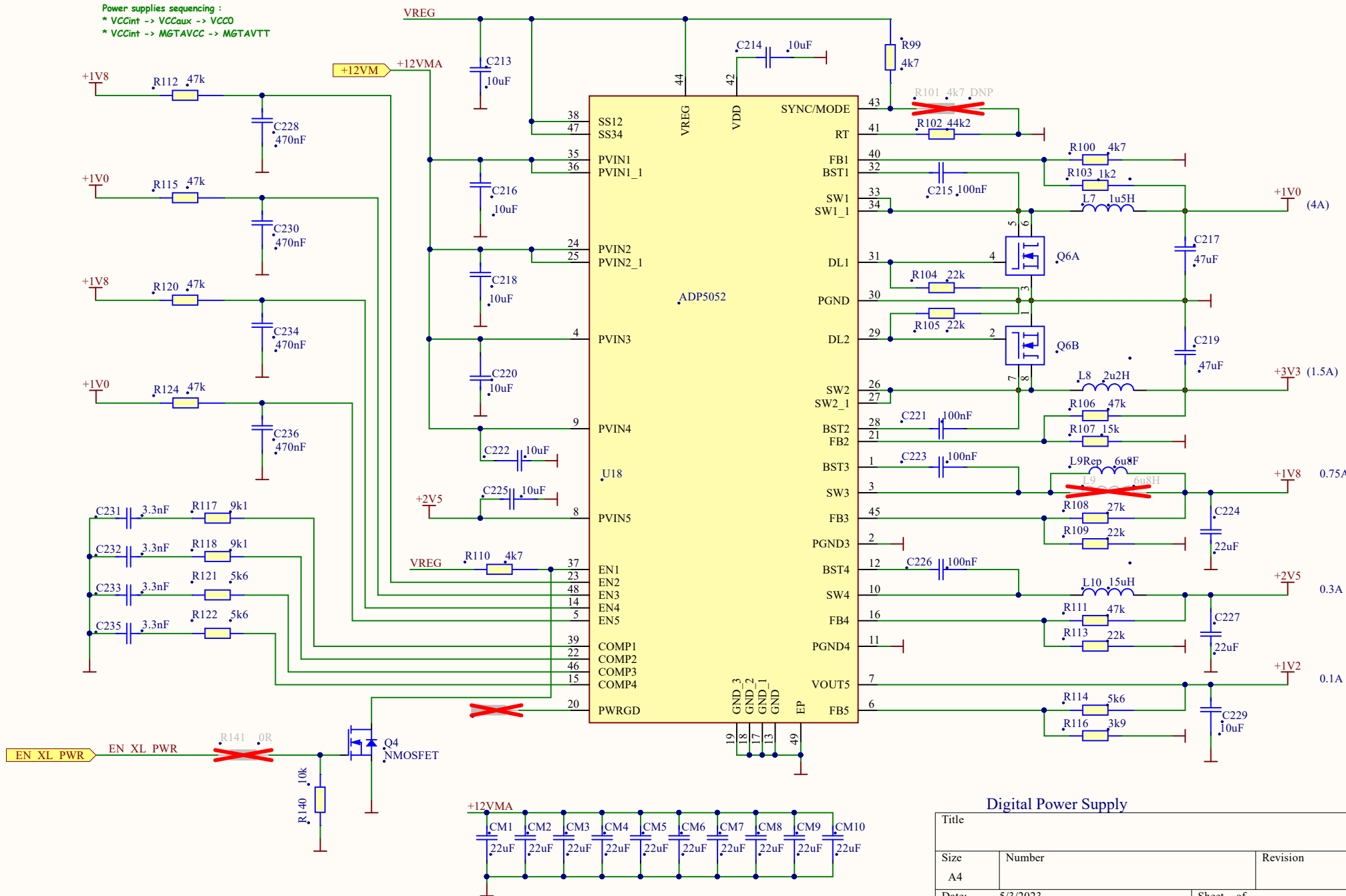






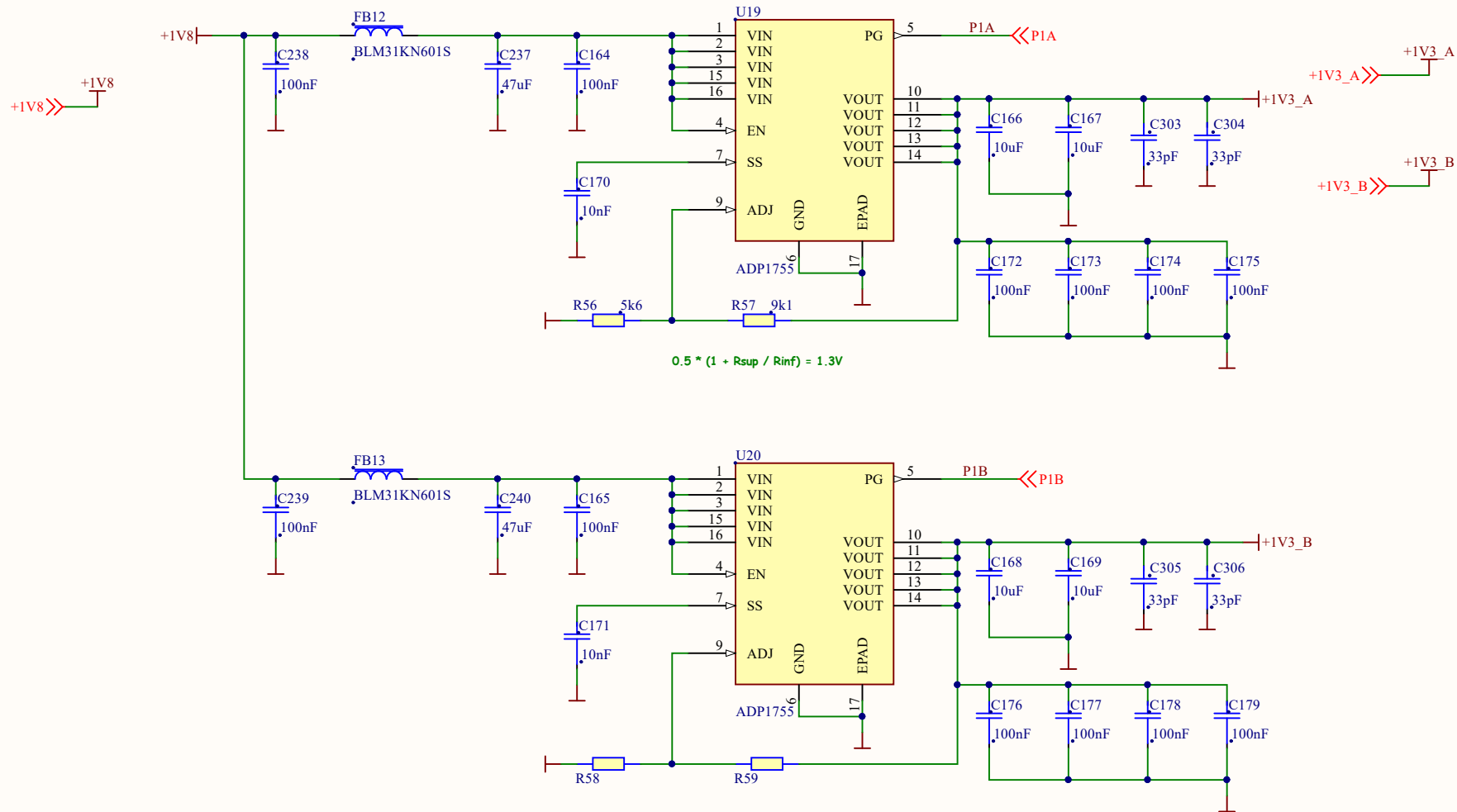


Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT



### Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	5/3/2023	Sheet of
File:	Z:\ors-hardware\...\OrsTypeC 6.SchDoc	Drawn By:



Title		
RF Power Supply		
Size	Number	Revision
A4		
Date:	5/3/2023	Sheet of
File:	Z:\ors-hardware\...\OrsTypeC 7.SchDoc	Drawn By:

A

B

C

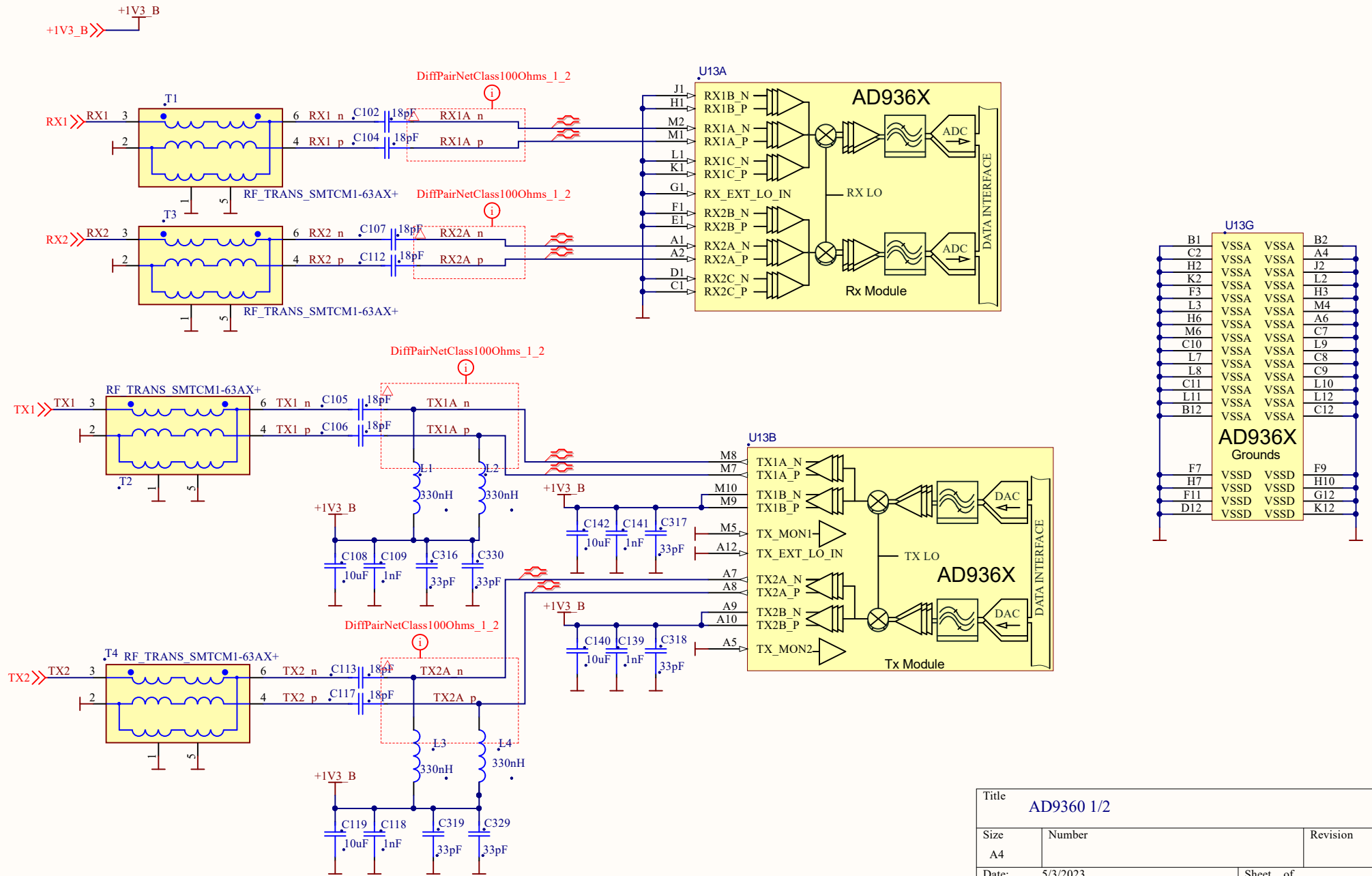
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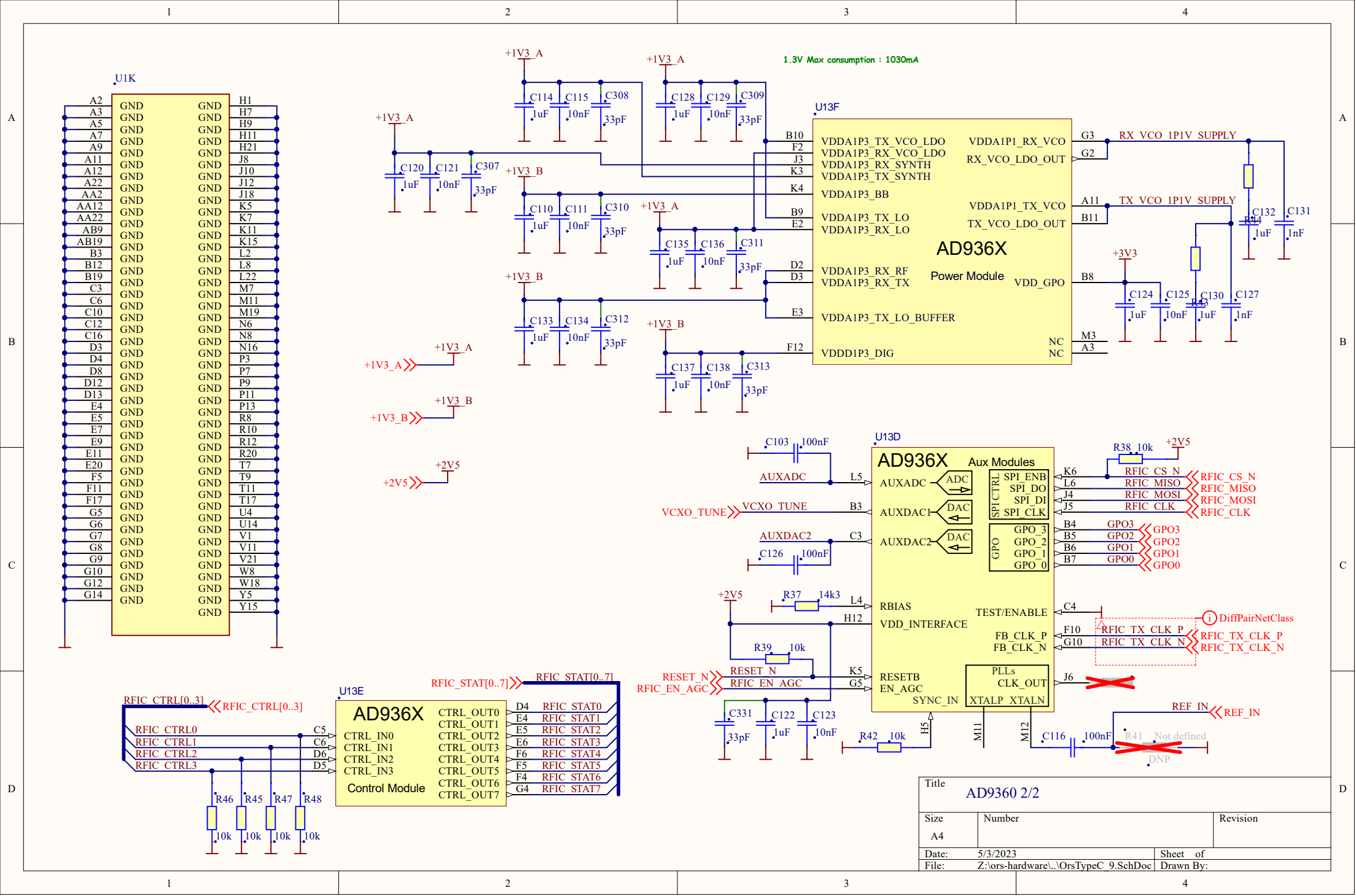
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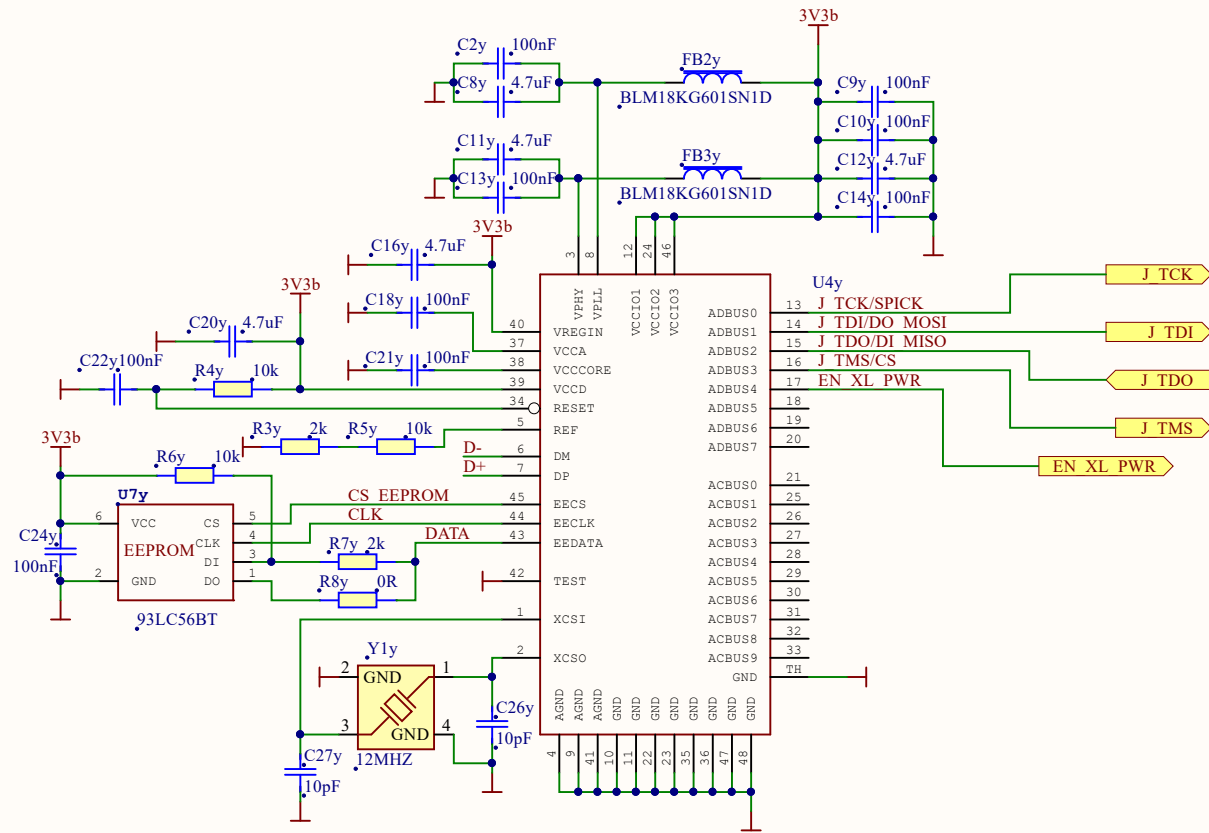
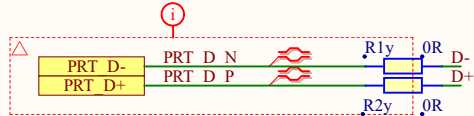
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Title		
AD9360 1/2		
Size	Number	Revision
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Date:	5/3/2023	Sheet of
File:	Z:\ors-hardware\...\OrsTypeC 8.SchDoc	Drawn By:



DiffPairNetClass



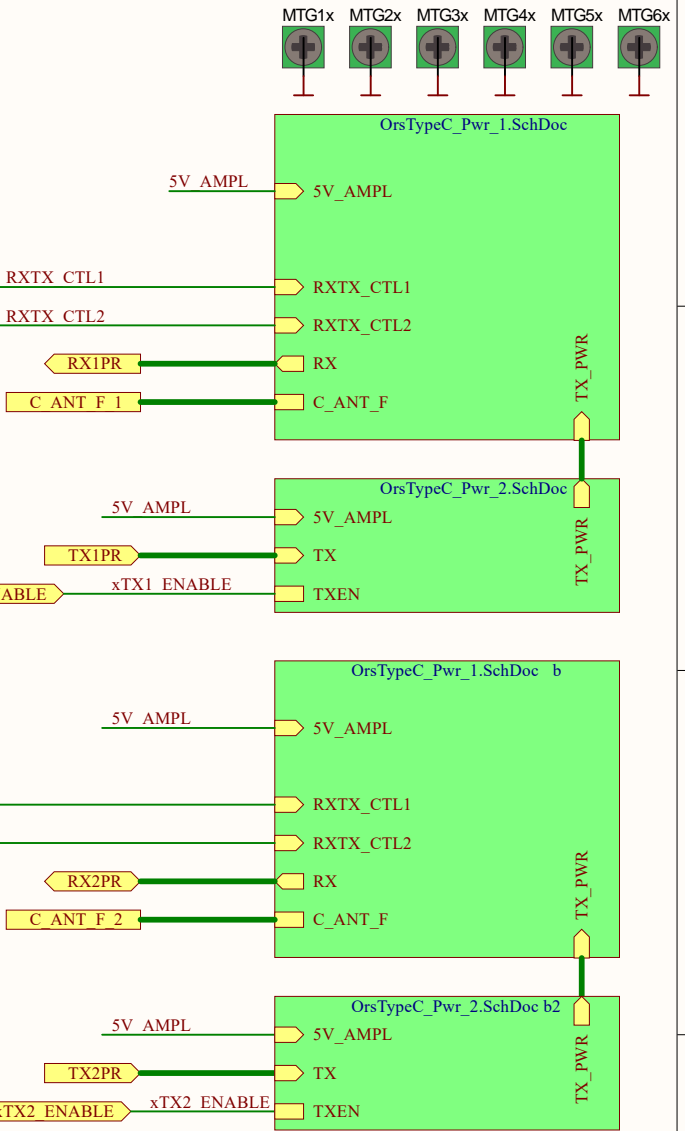
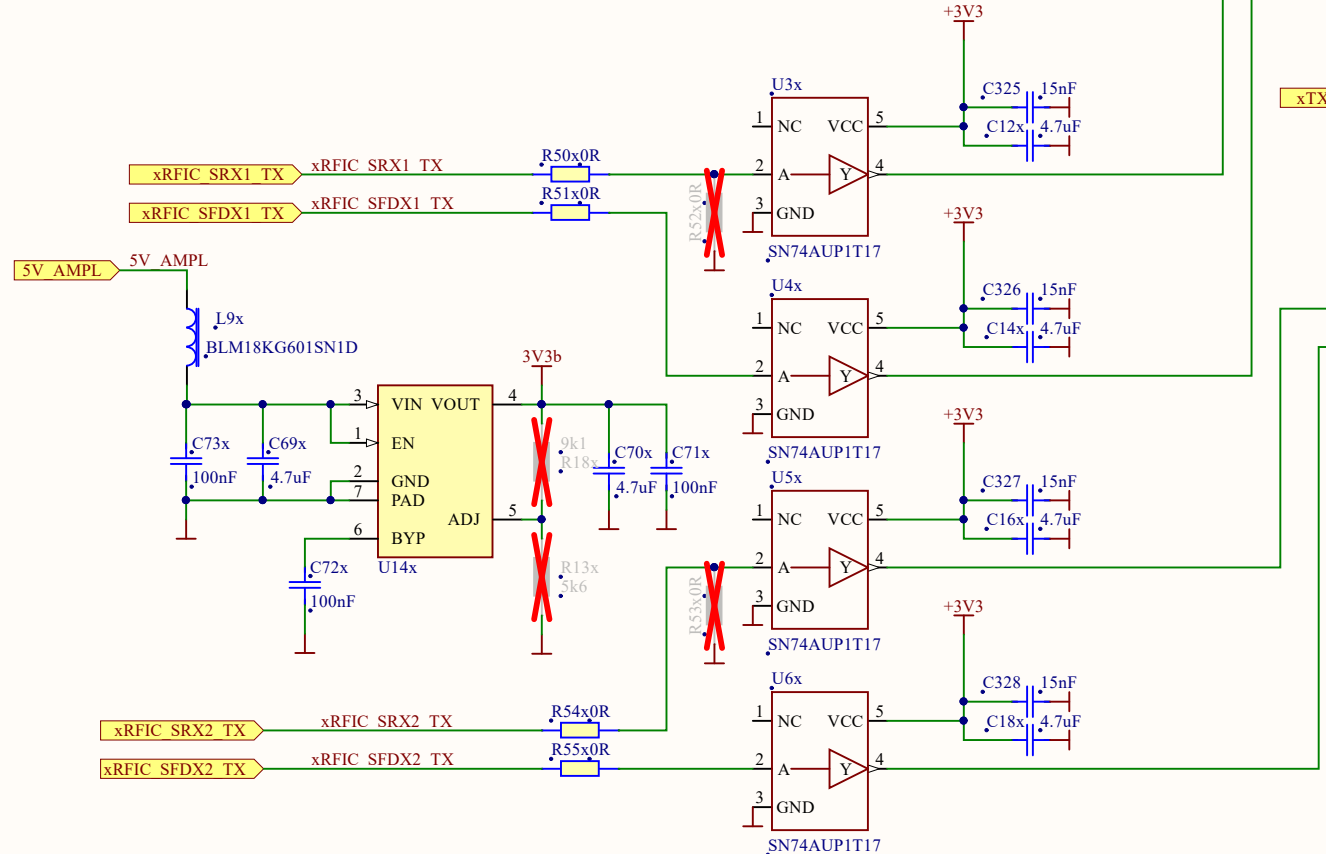
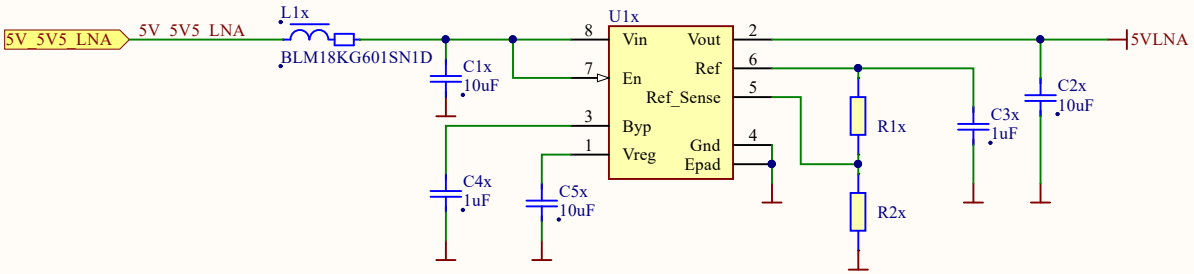
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File:	Z:\ors-hardware\...\OrsTypeC Prog.SchDoc	Drawn By:

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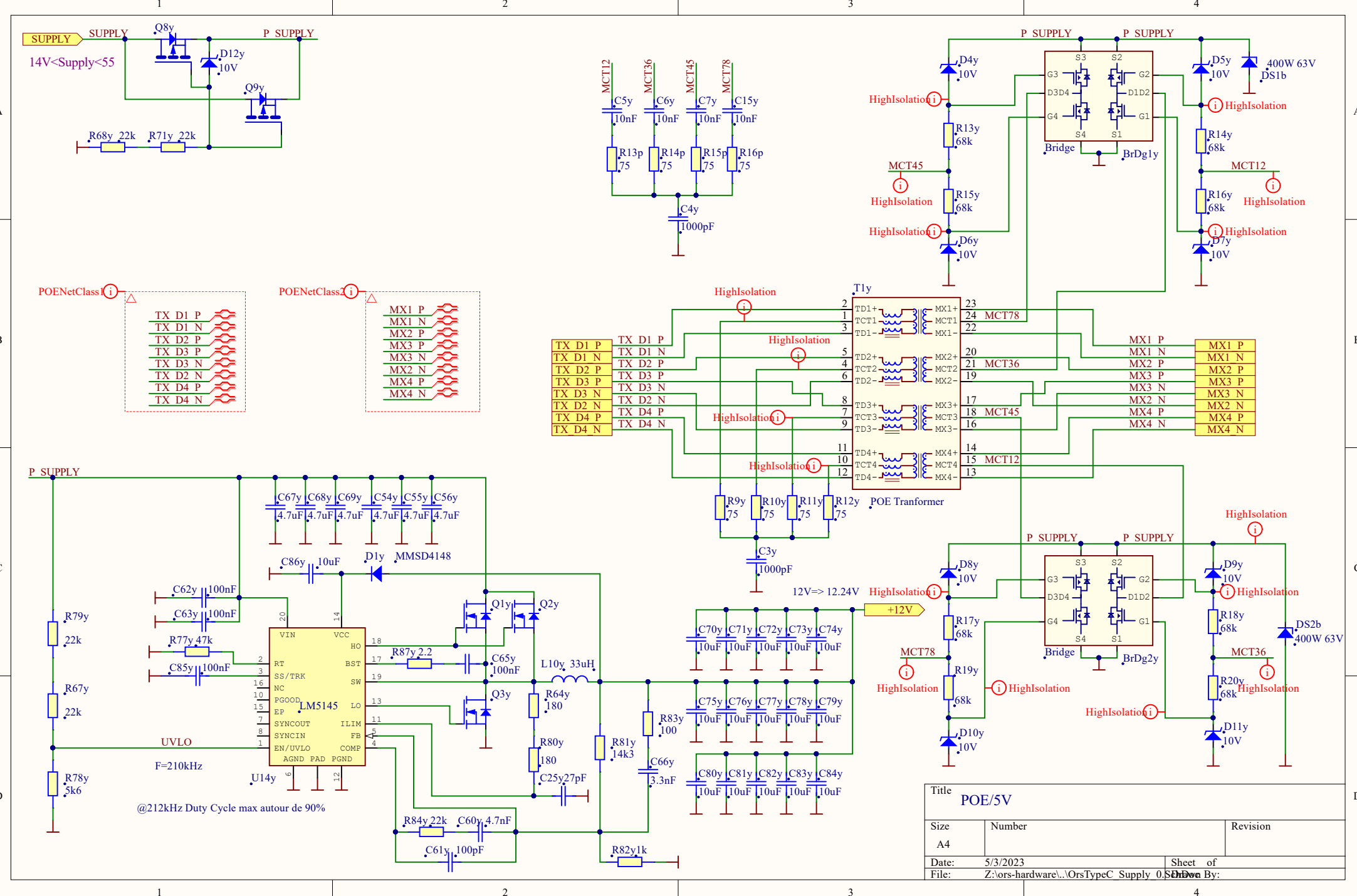
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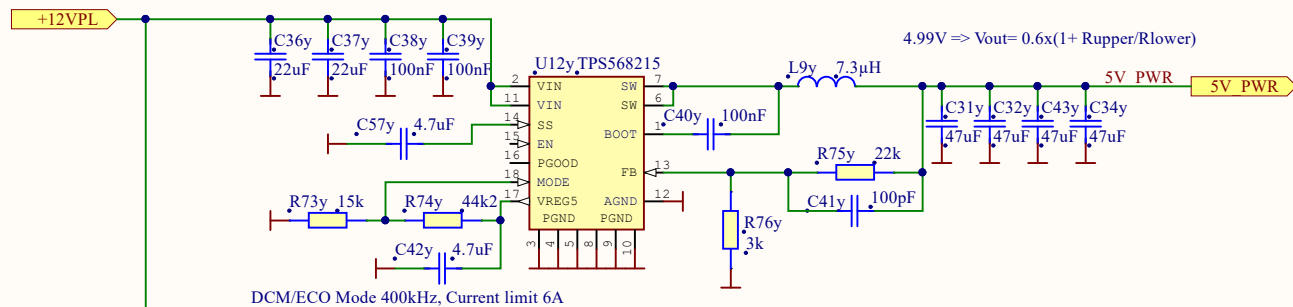
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D

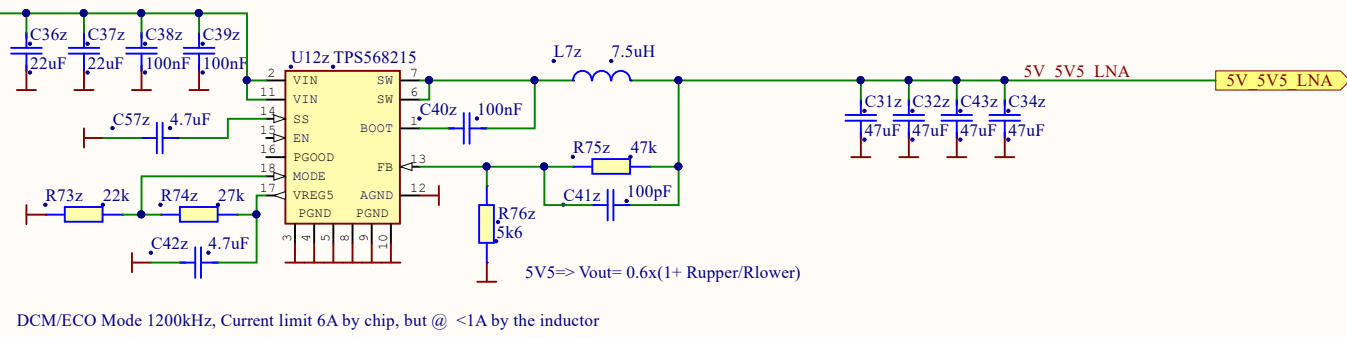


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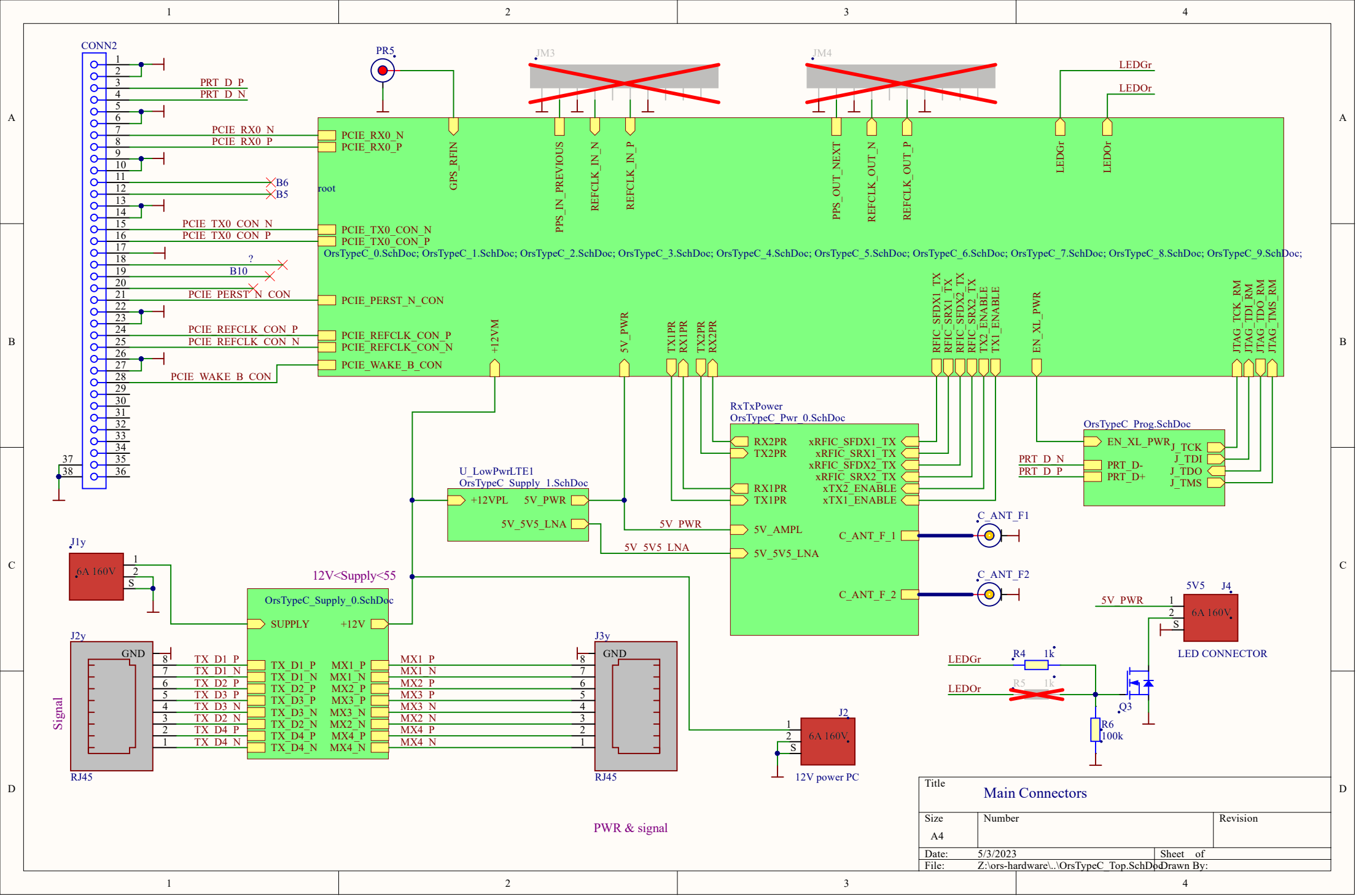




Soft Start  
 $TSS(seconde) = CSSx0.6/6e-6 \Rightarrow 0.47s$



Title Main Connectors		
Size A4	Number	Revision
Date: 5/3/2023	Sheet of	
File: Z:\ors-hardware\...\OrsTypeC Supply 1.SchDoc	Drawn By:	



Title		
Main Connectors		
Size	Number	Revision
A4		
Date:	5/3/2023	Sheet of
File:	Z:\ors-hardware\...\OrsTypeC_Top.SchDoc	Drawn By: