

A

A

B

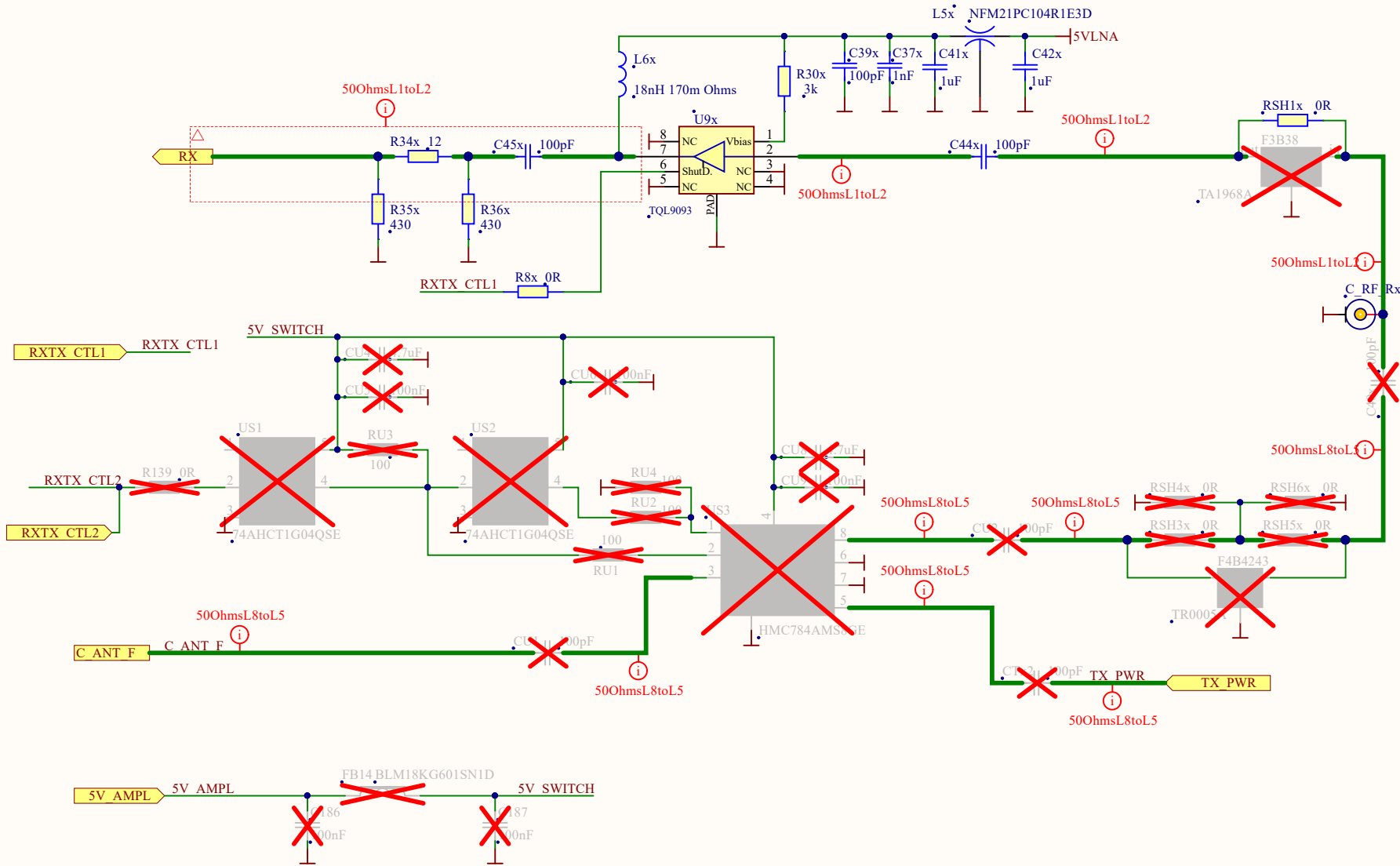
B

C

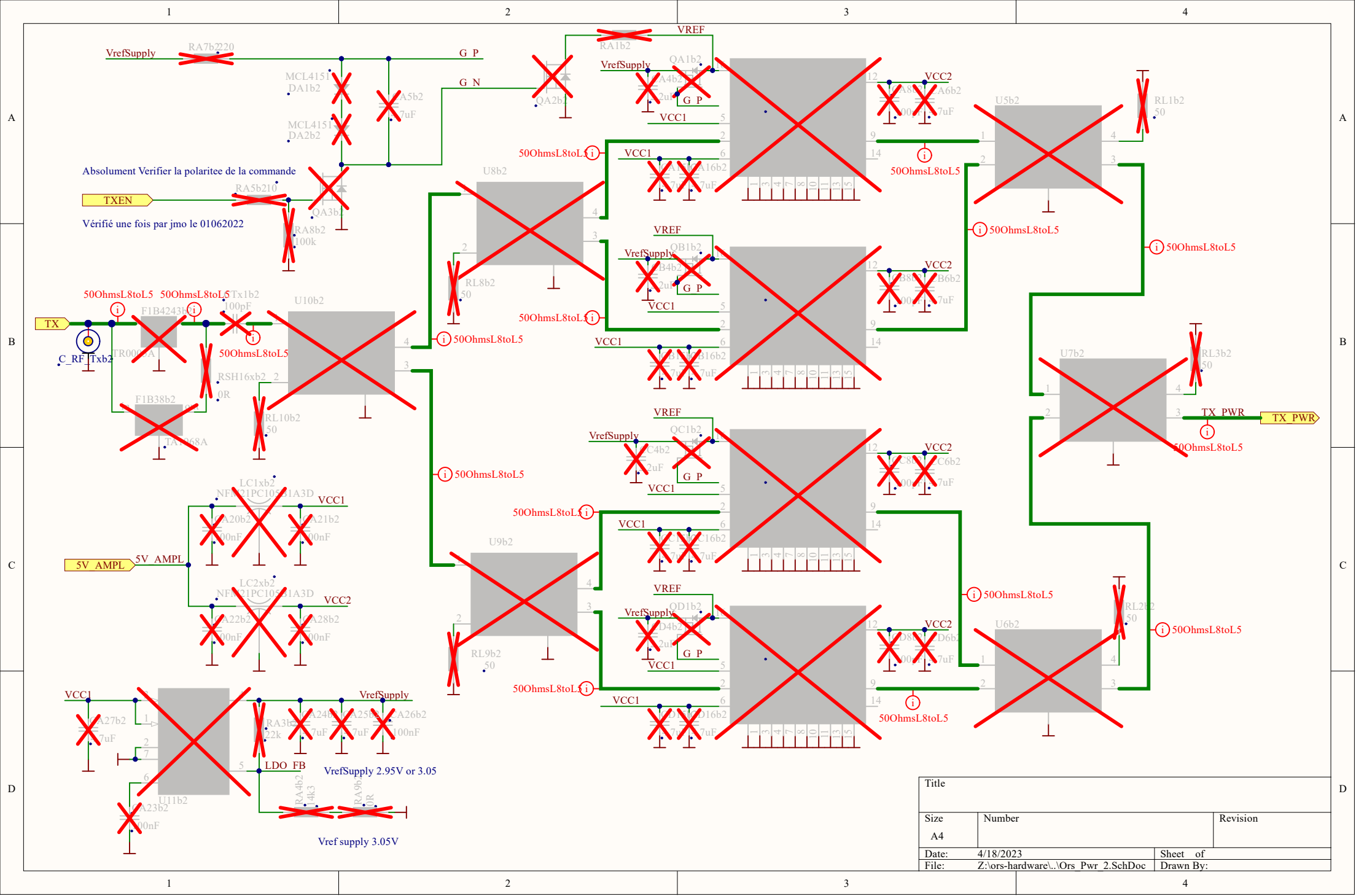
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D

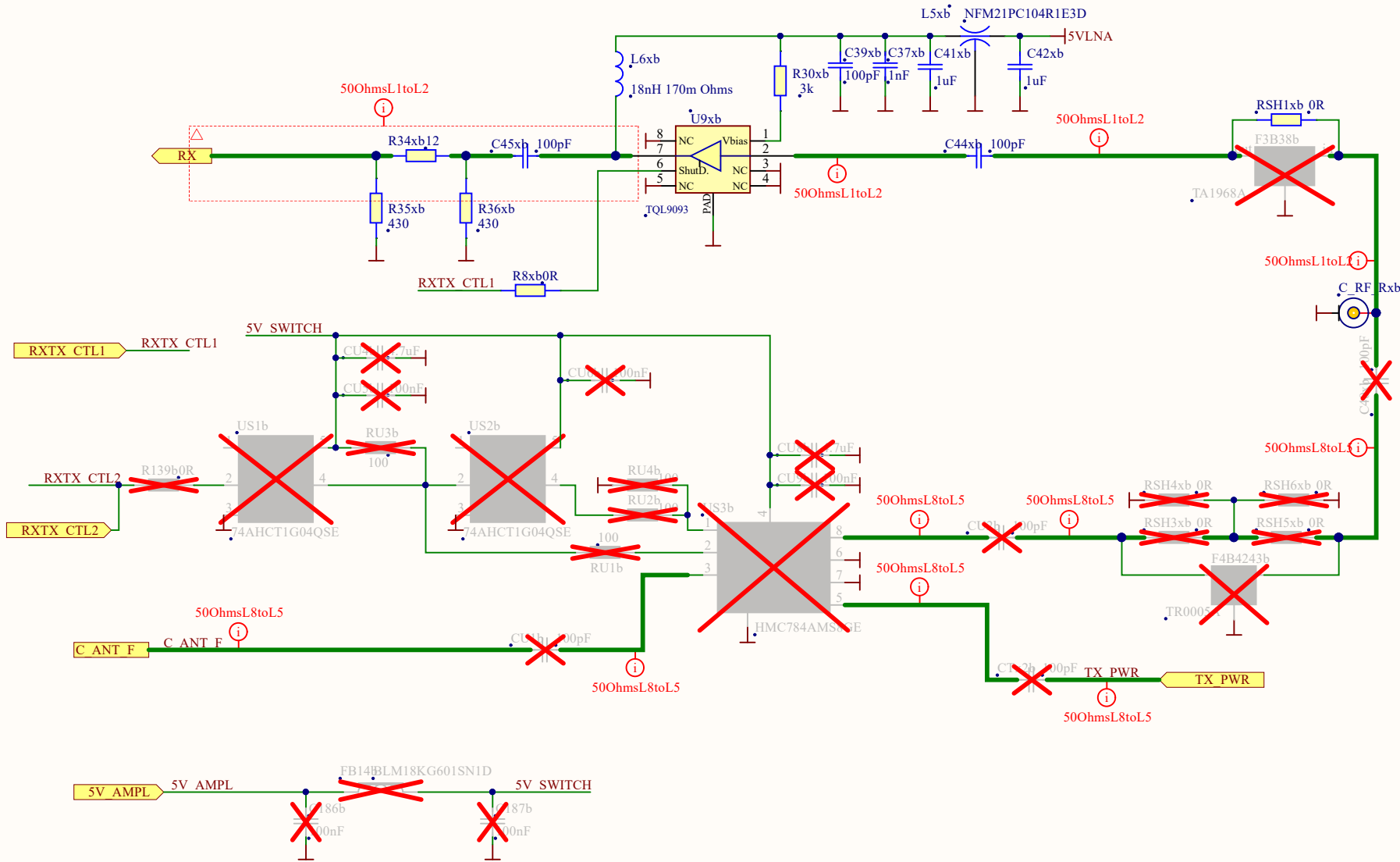
D



Title		
Size	Number	Revision
A4		
Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_1.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_2.SchDoc	Drawn By:



Title		
Size	Number	Revision
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Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Pwr 1.SchDoc	Drawn By:

A

A

B

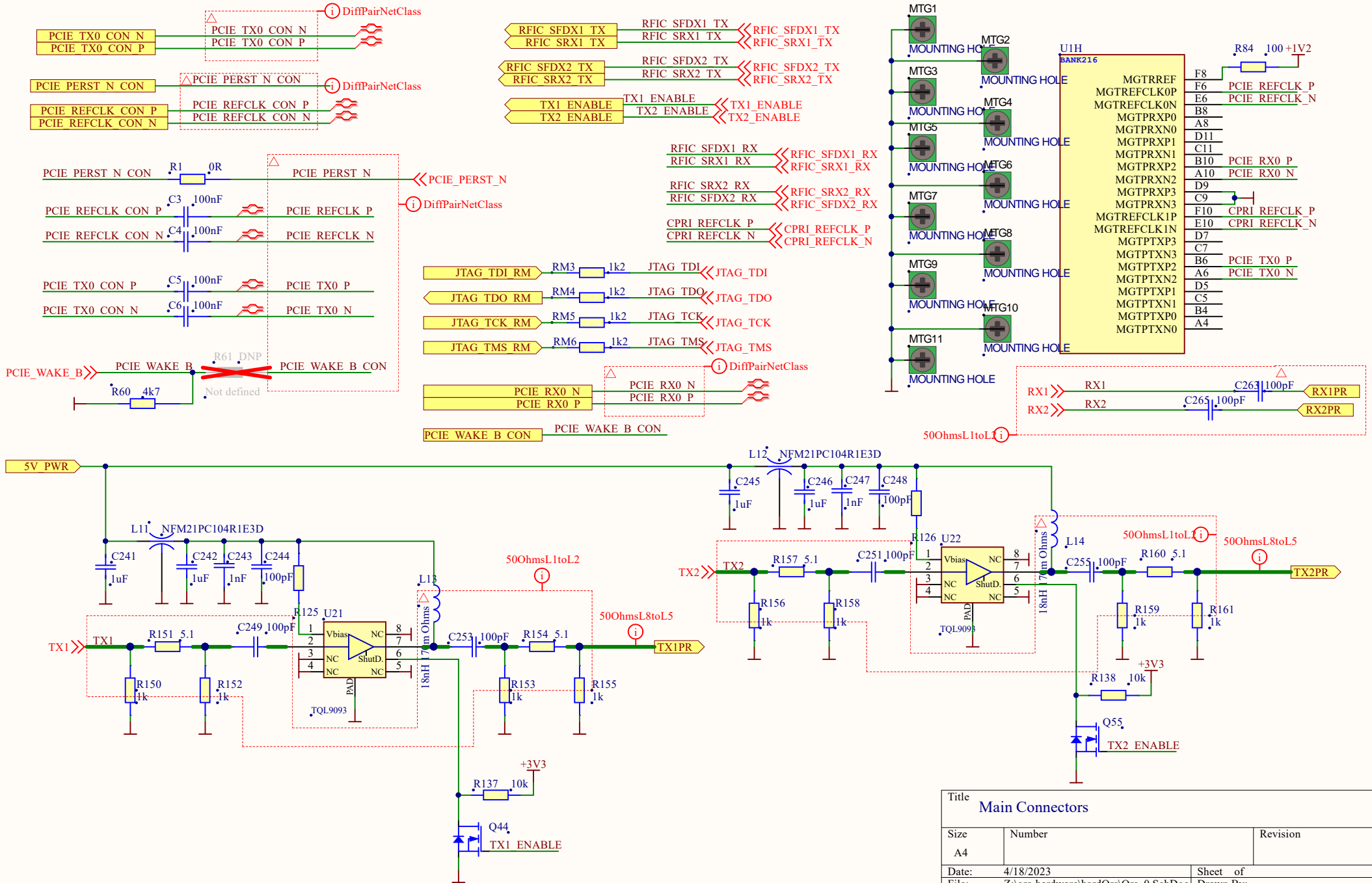
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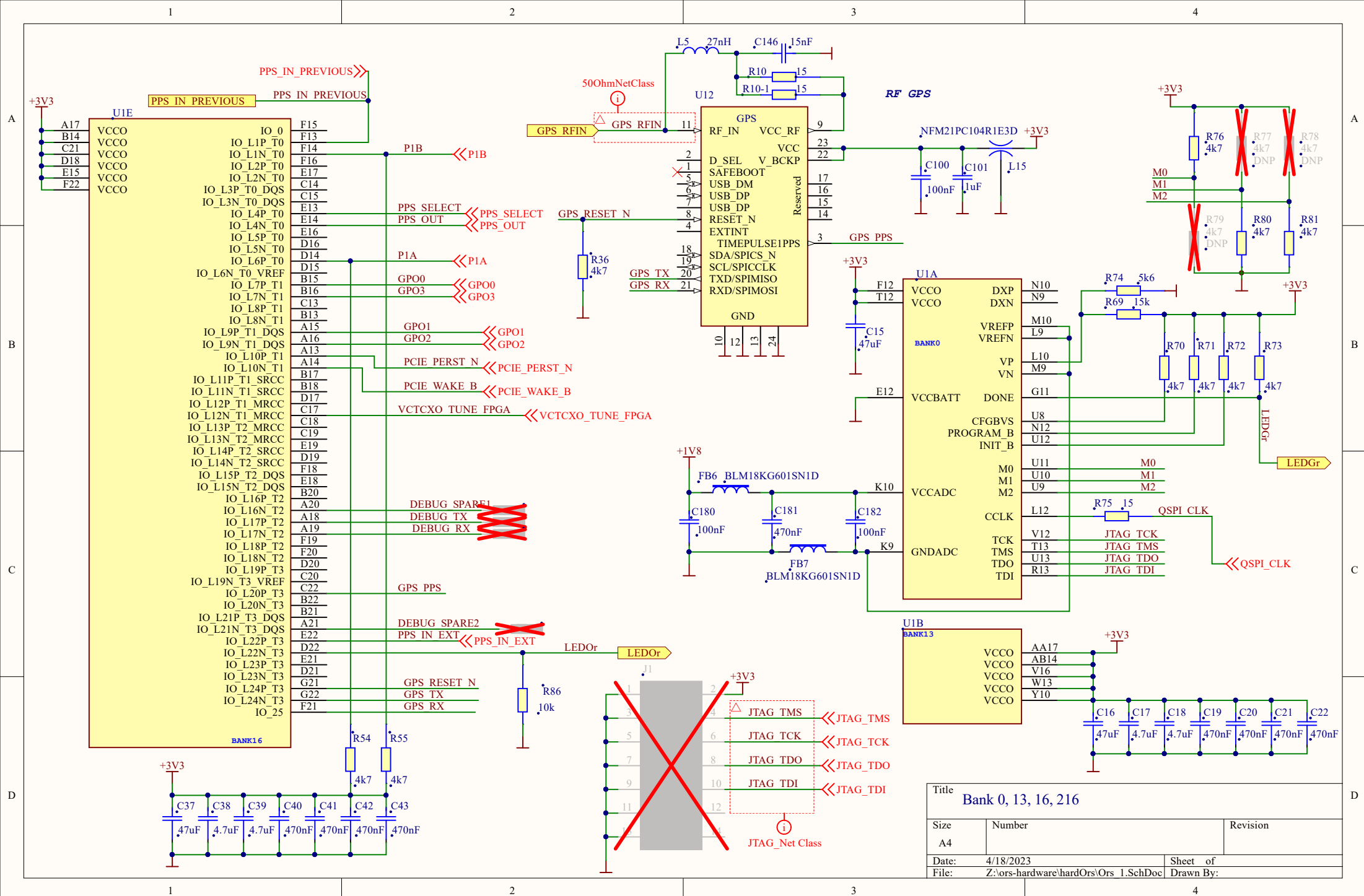
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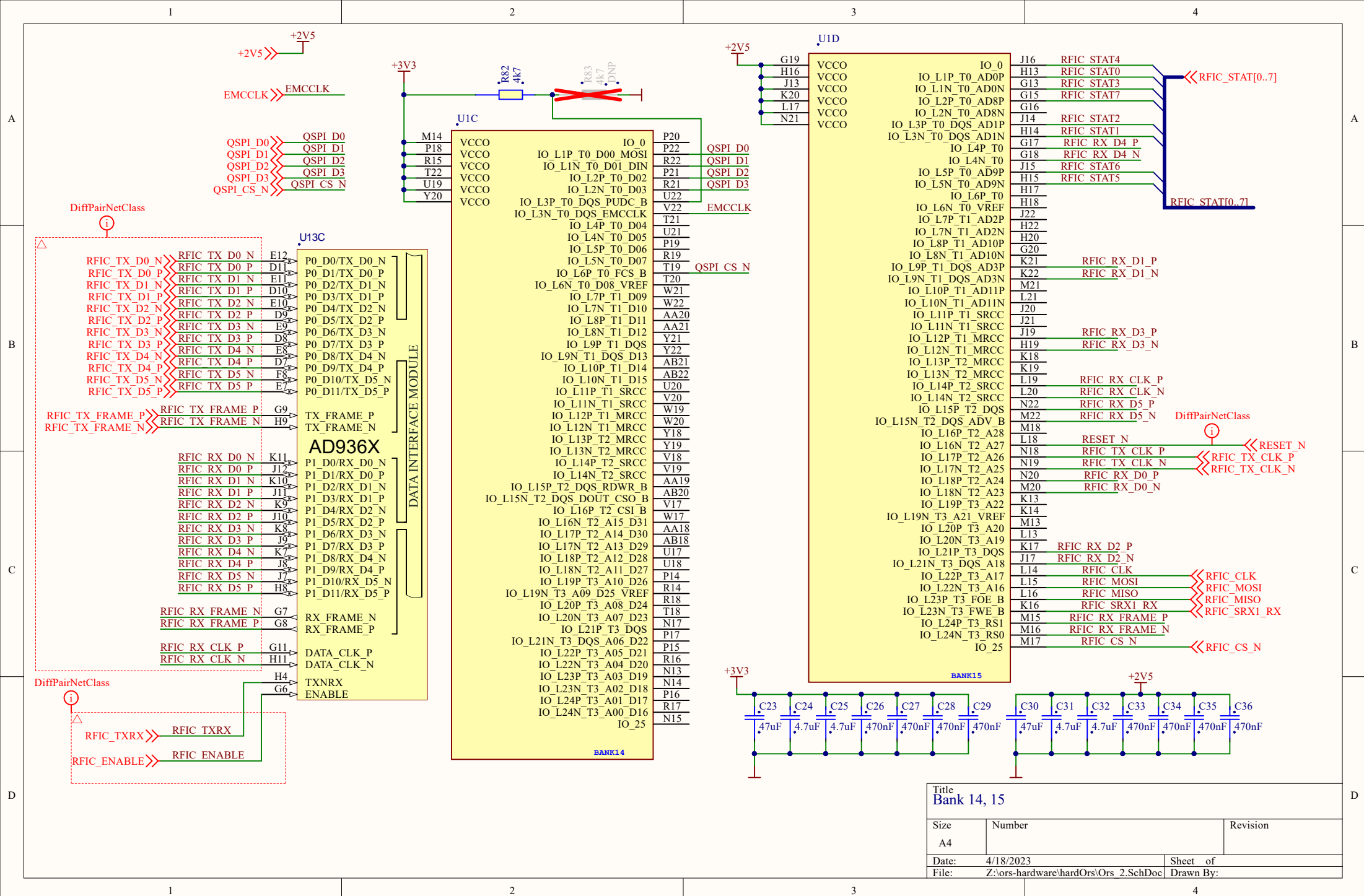
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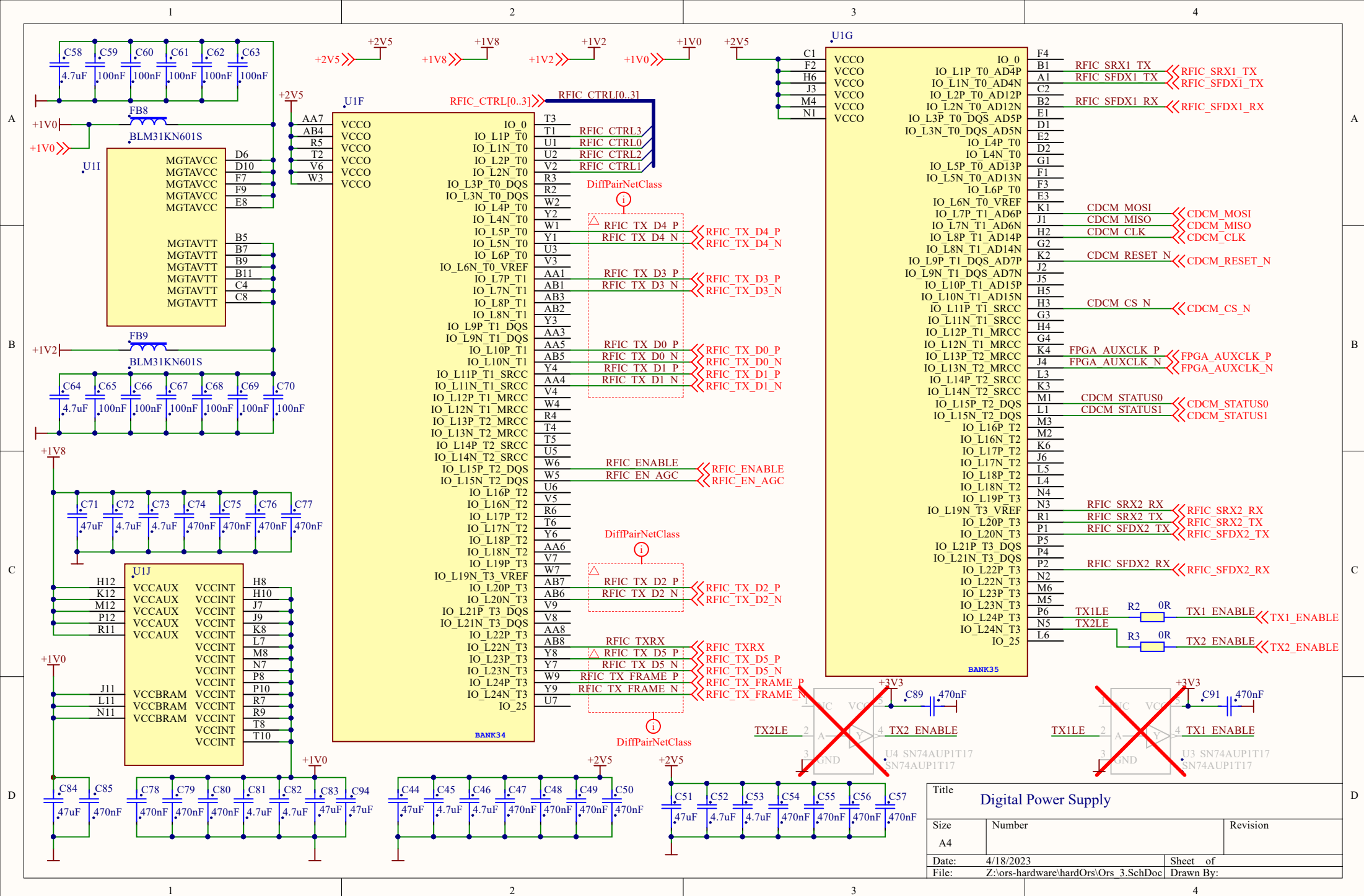
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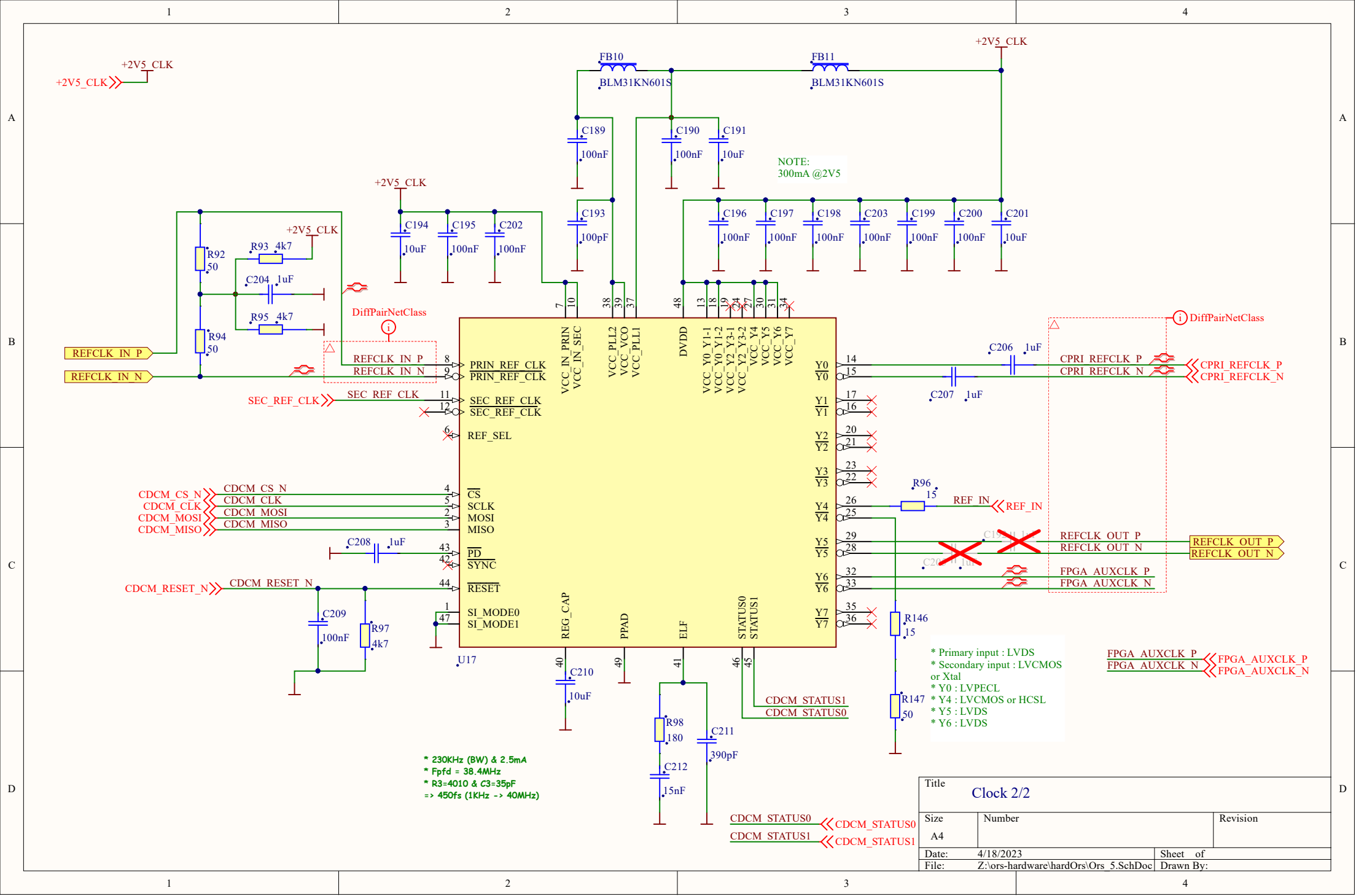




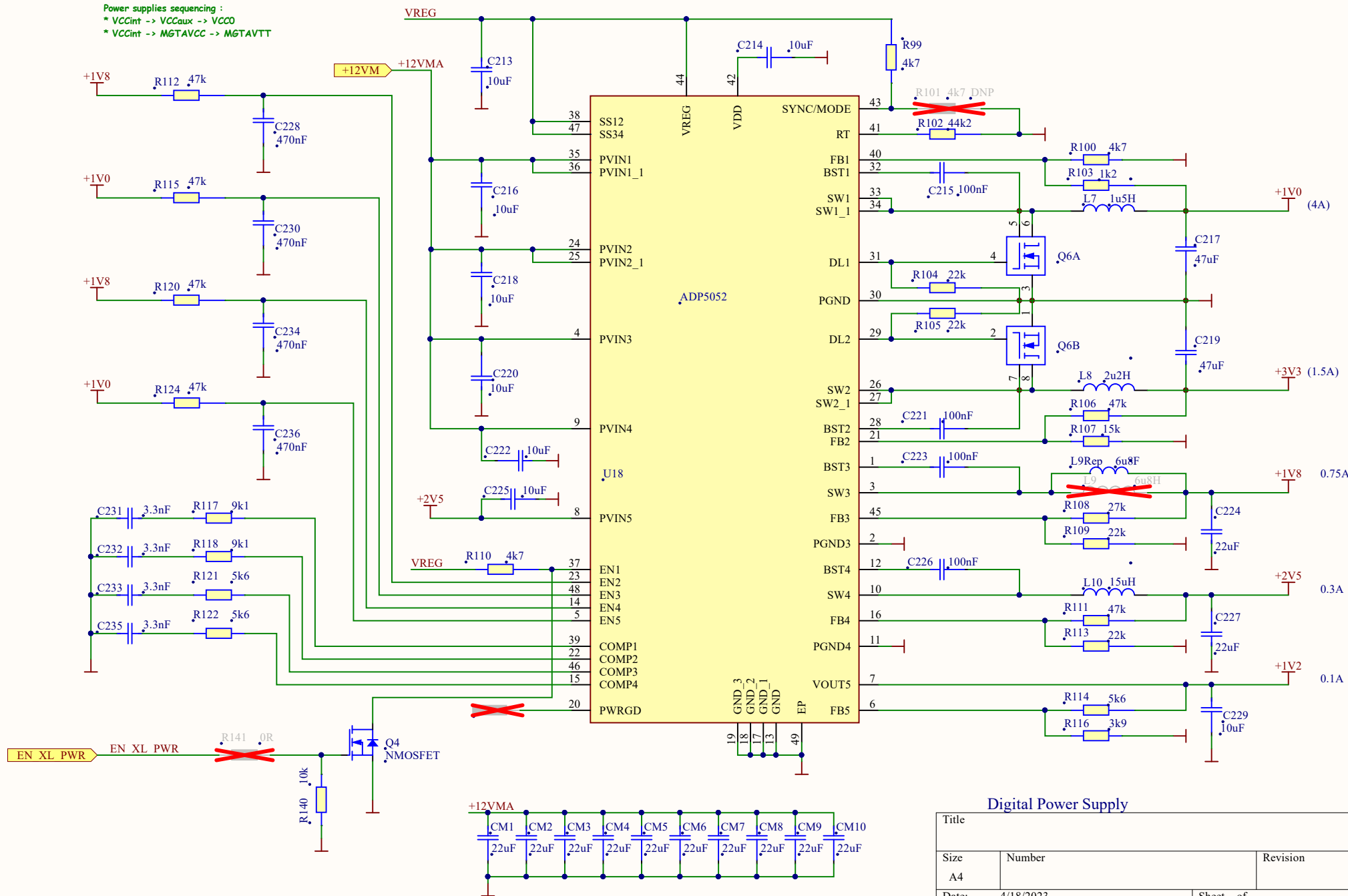






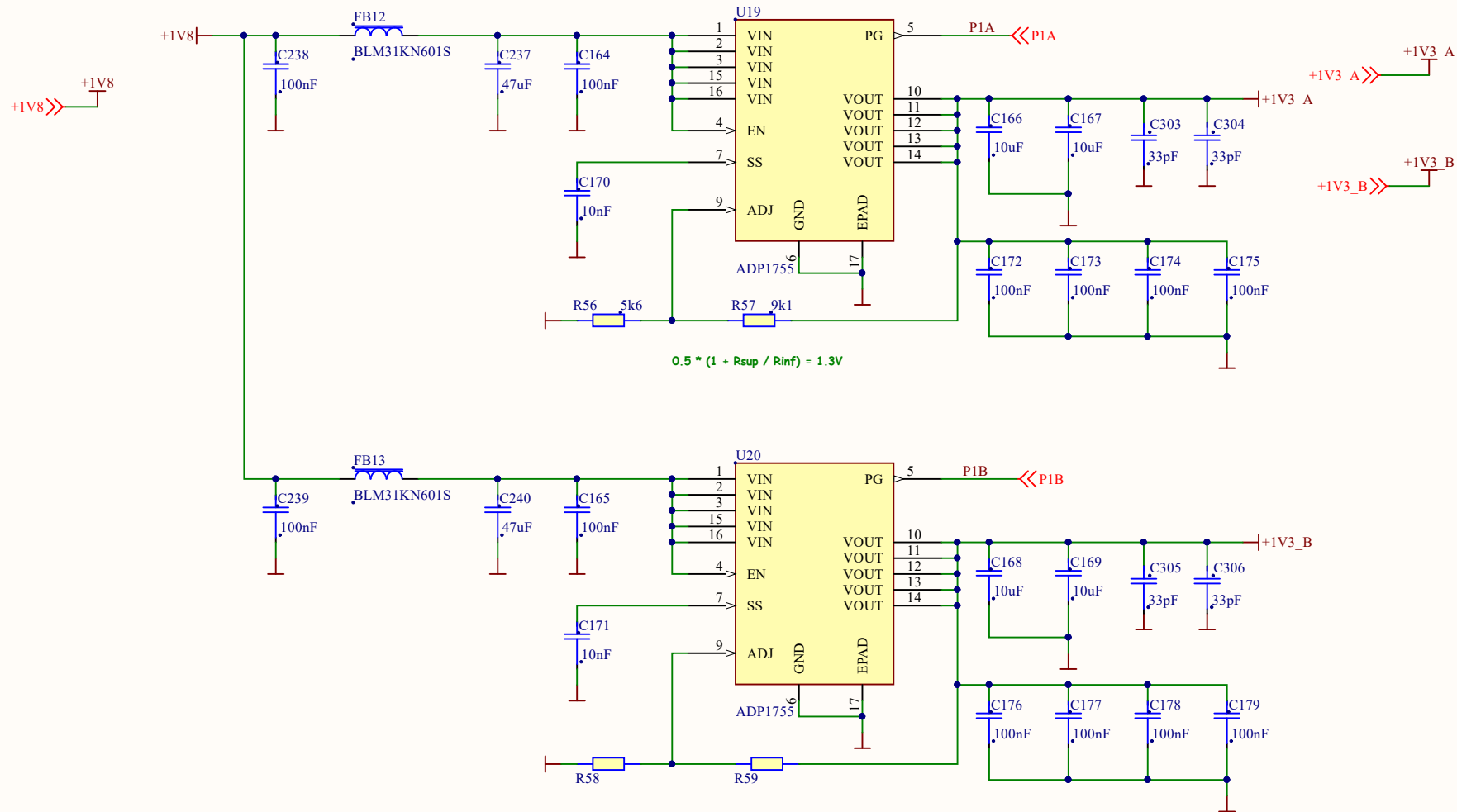


Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT



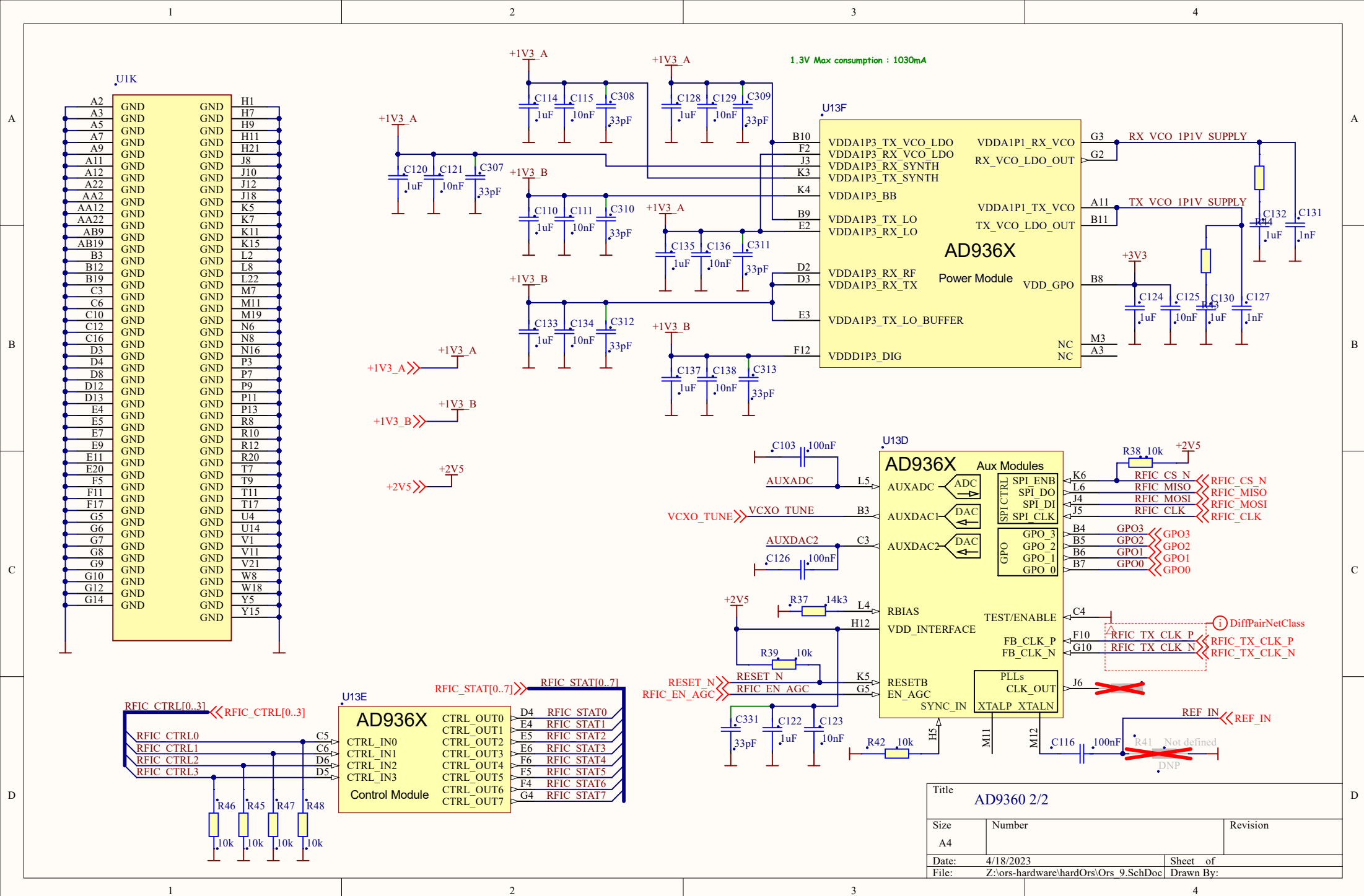
### Digital Power Supply

Title		
Size	Number	Revision
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Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_6.SchDoc	Drawn By:



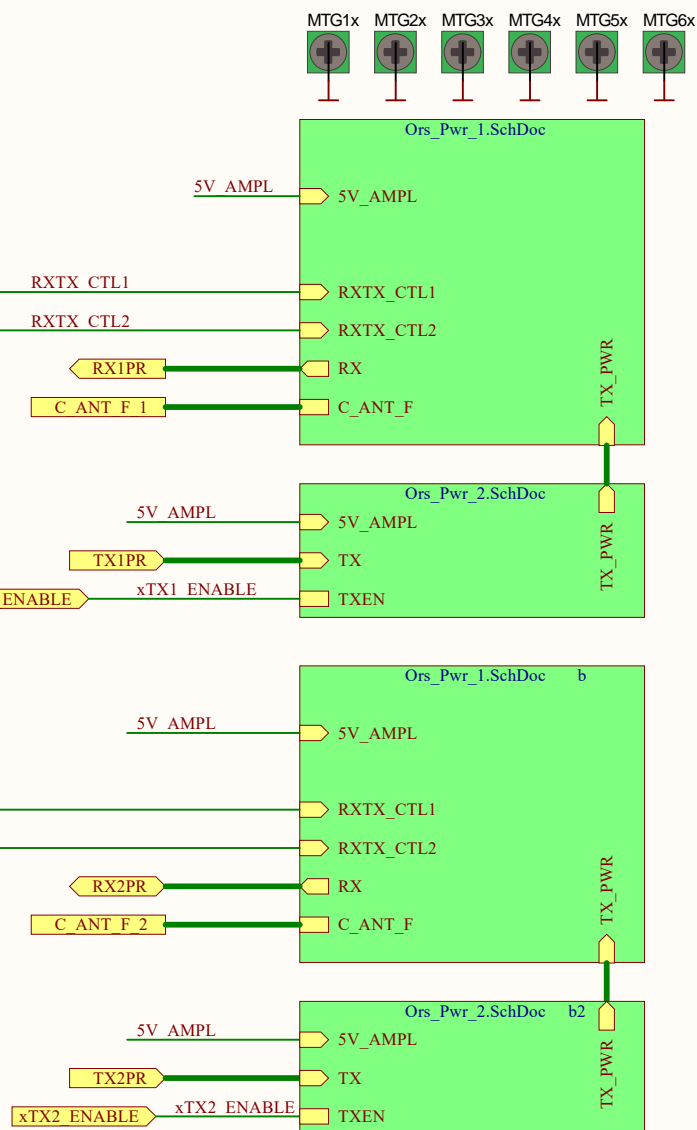
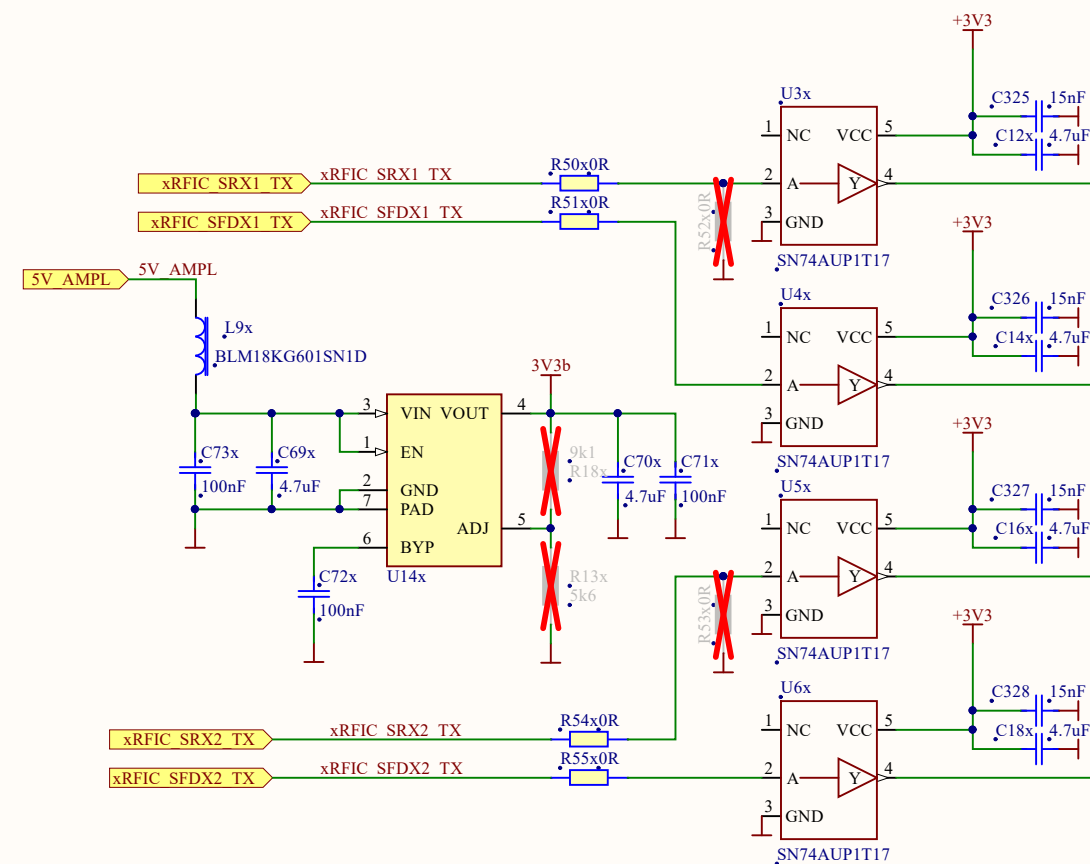
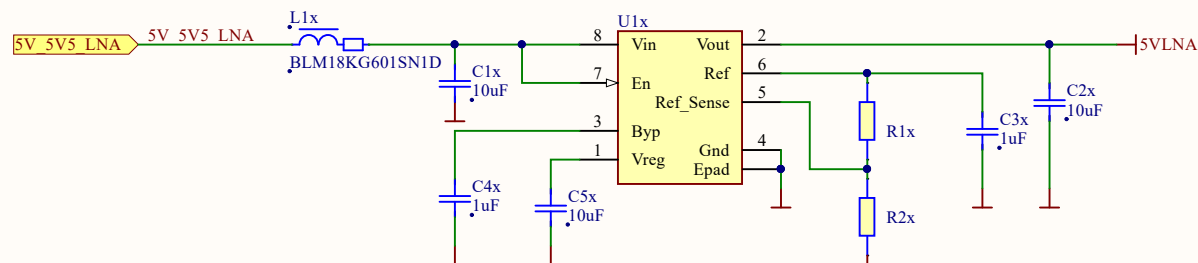
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Size A4	Number	Revision
Date: 4/18/2023	Sheet of	
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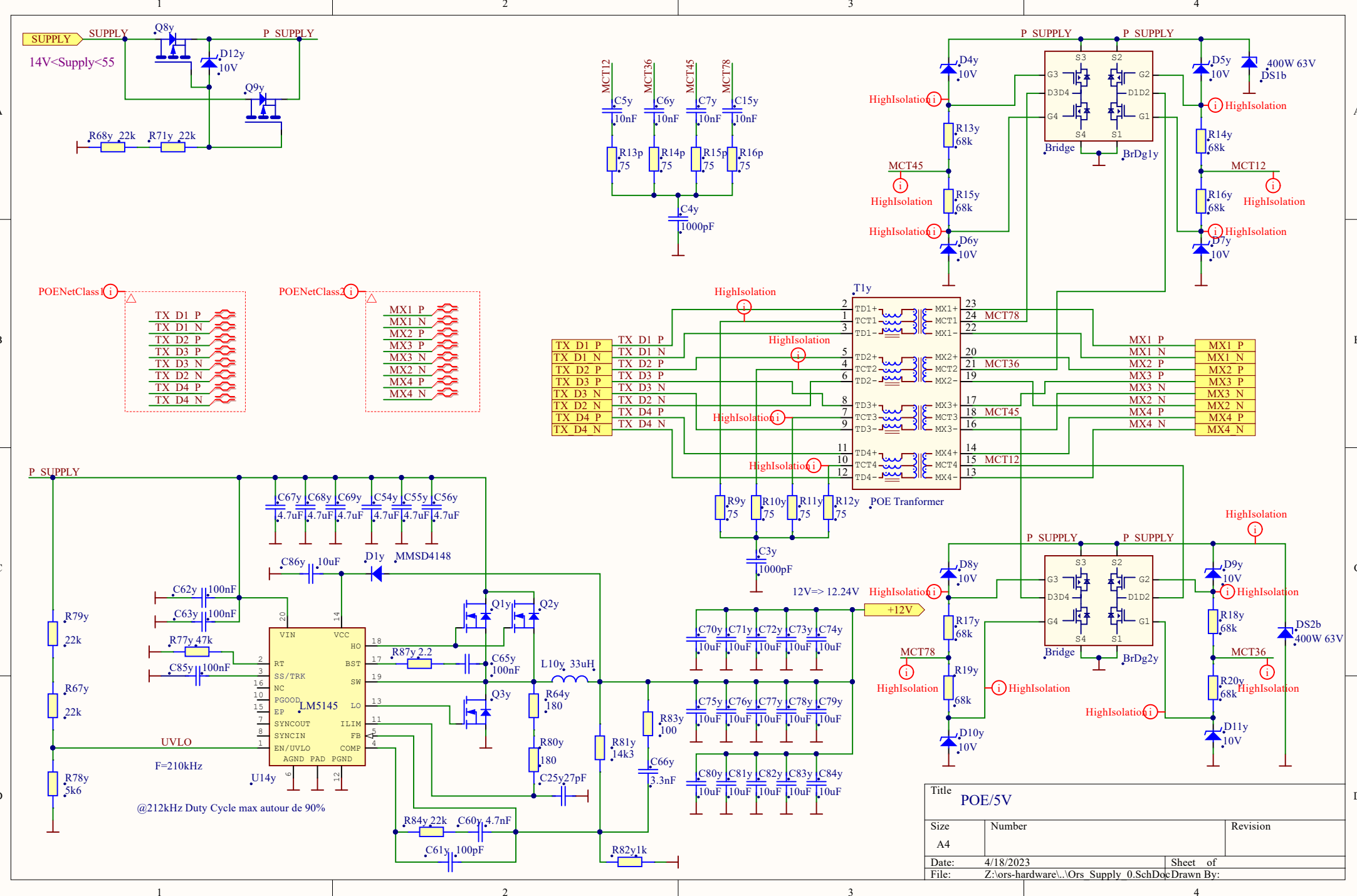


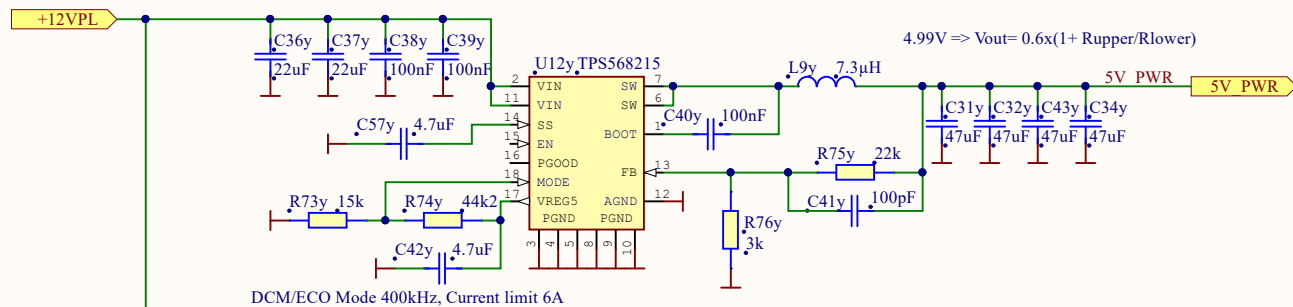






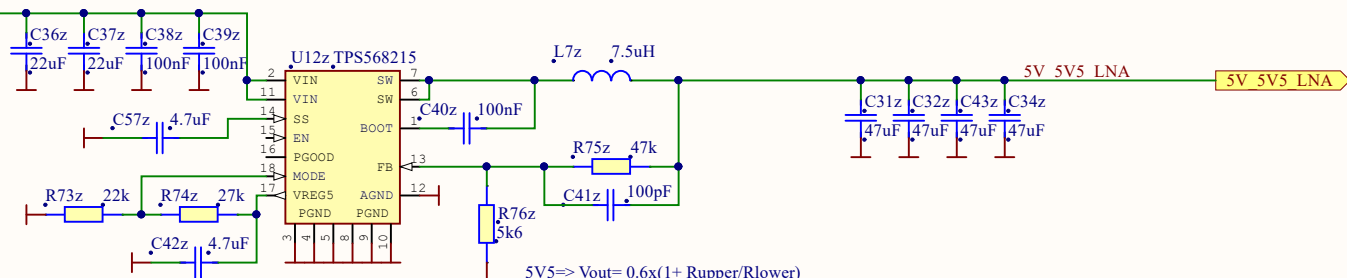
Title		
Size A4	Number	Revision
Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Pwr 0.SchDoc	Drawn By:





Soft Start  
TSS(seconde)=CSSx0.6/6e-6 => 0.47s

DCM/ECO Mode 400kHz, Current limit 6A



DCM/ECO Mode 1200kHz, Current limit 6A by chip, but @ <1A by the inductor

Title Main Connectors		
Size A4	Number	Revision
Date:	4/18/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Supply 1.SchDoc	Drawn By:

