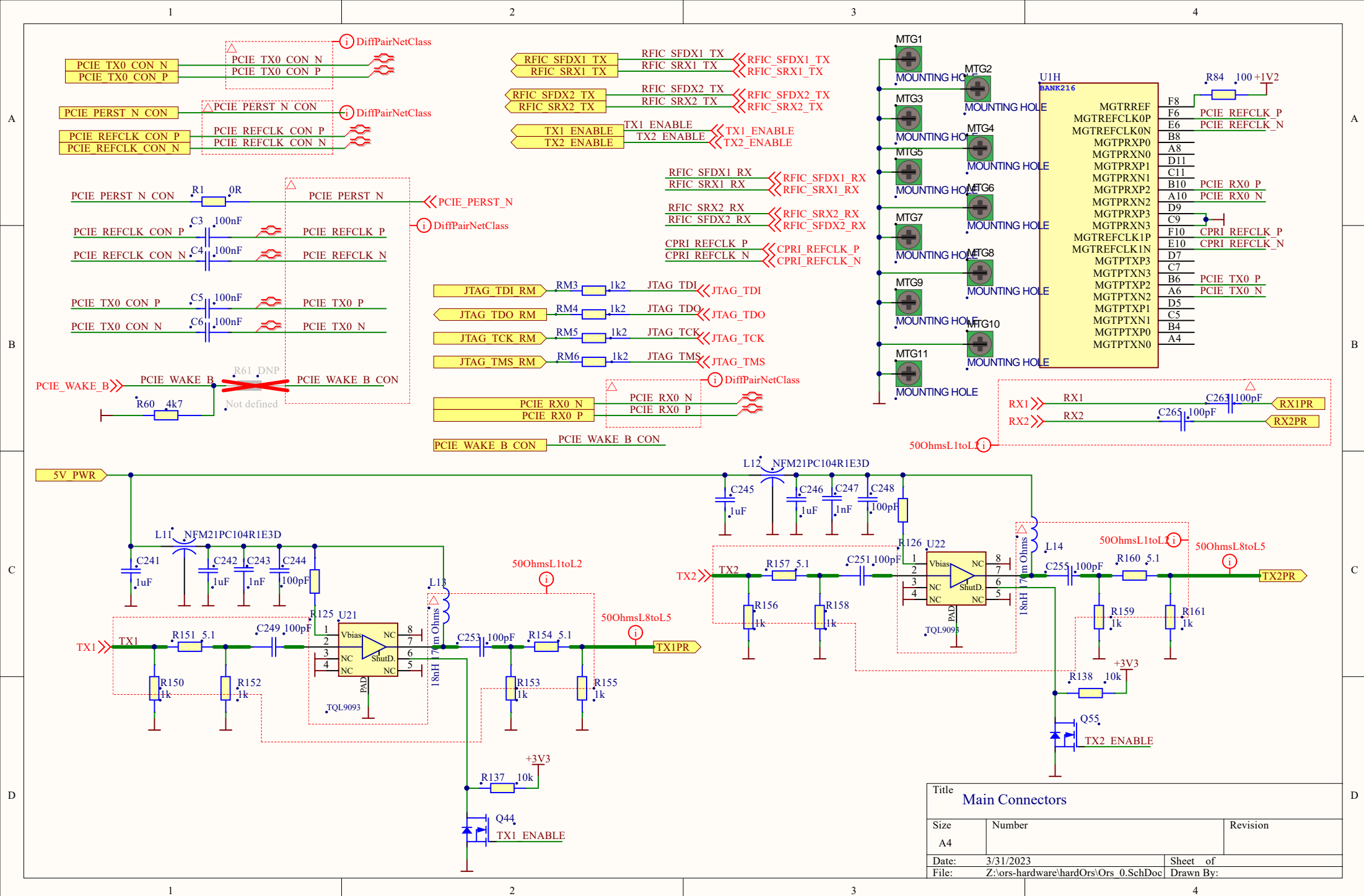
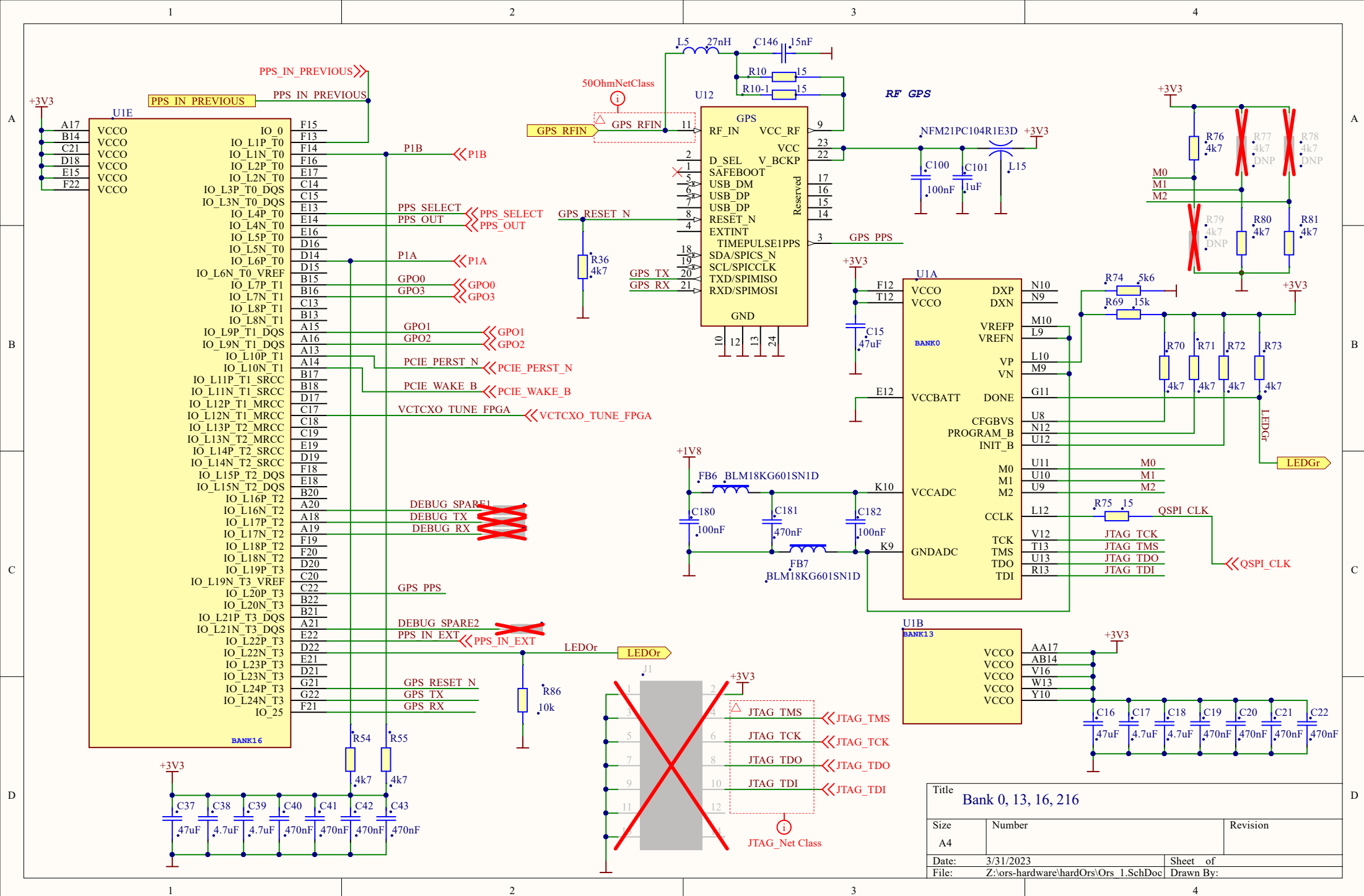


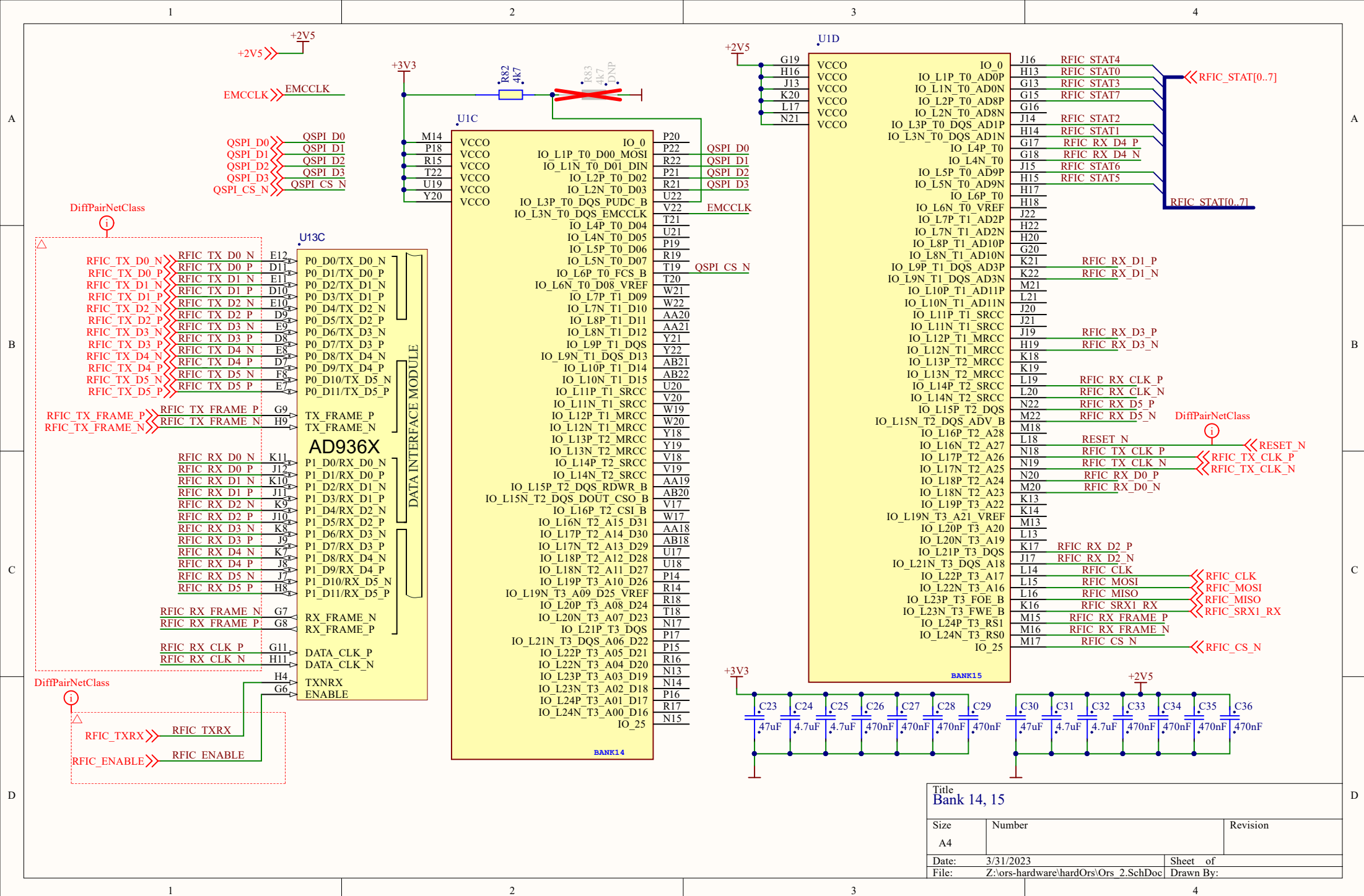
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Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Pwr 1.SchDoc	Drawn By:



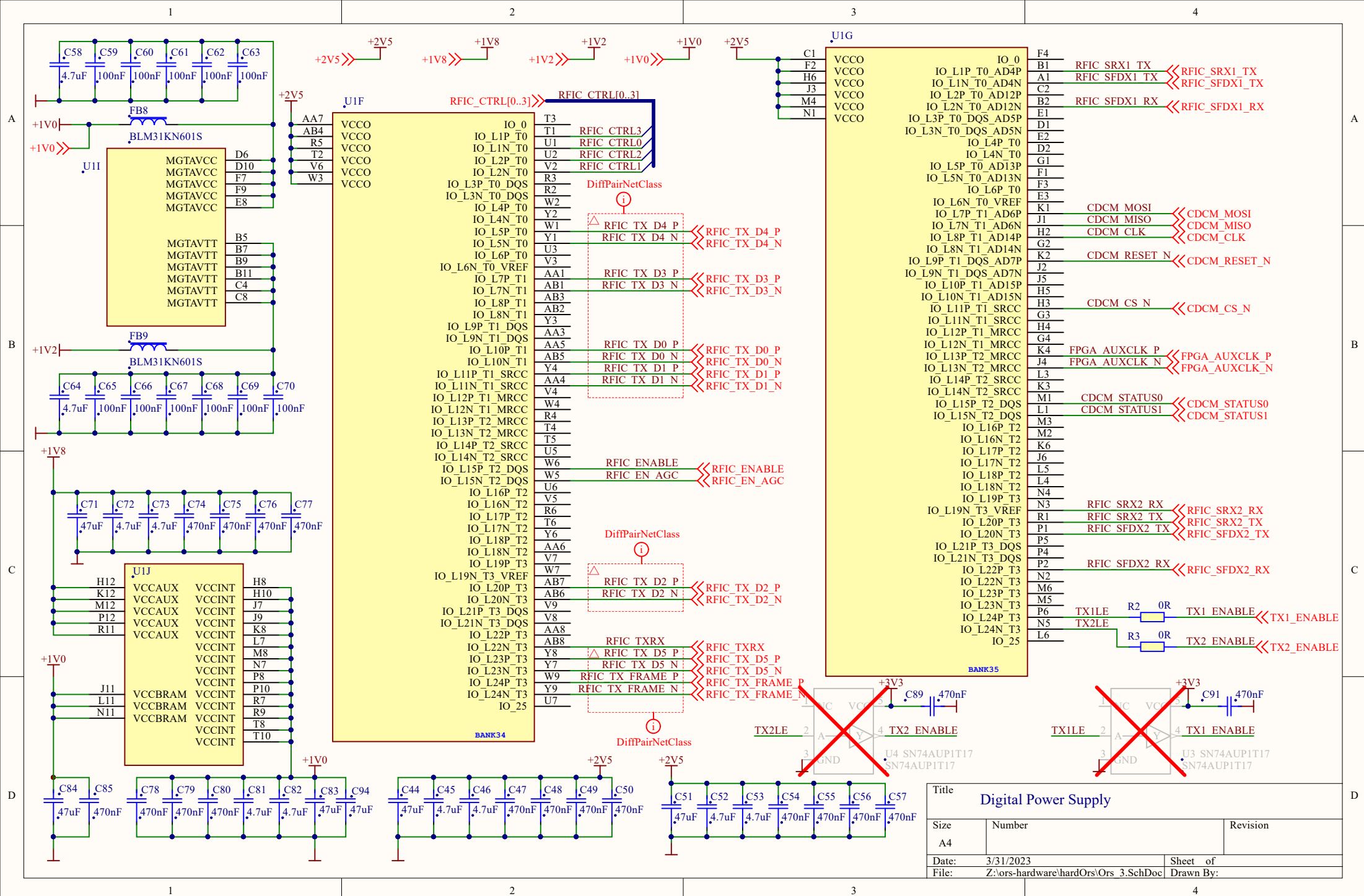










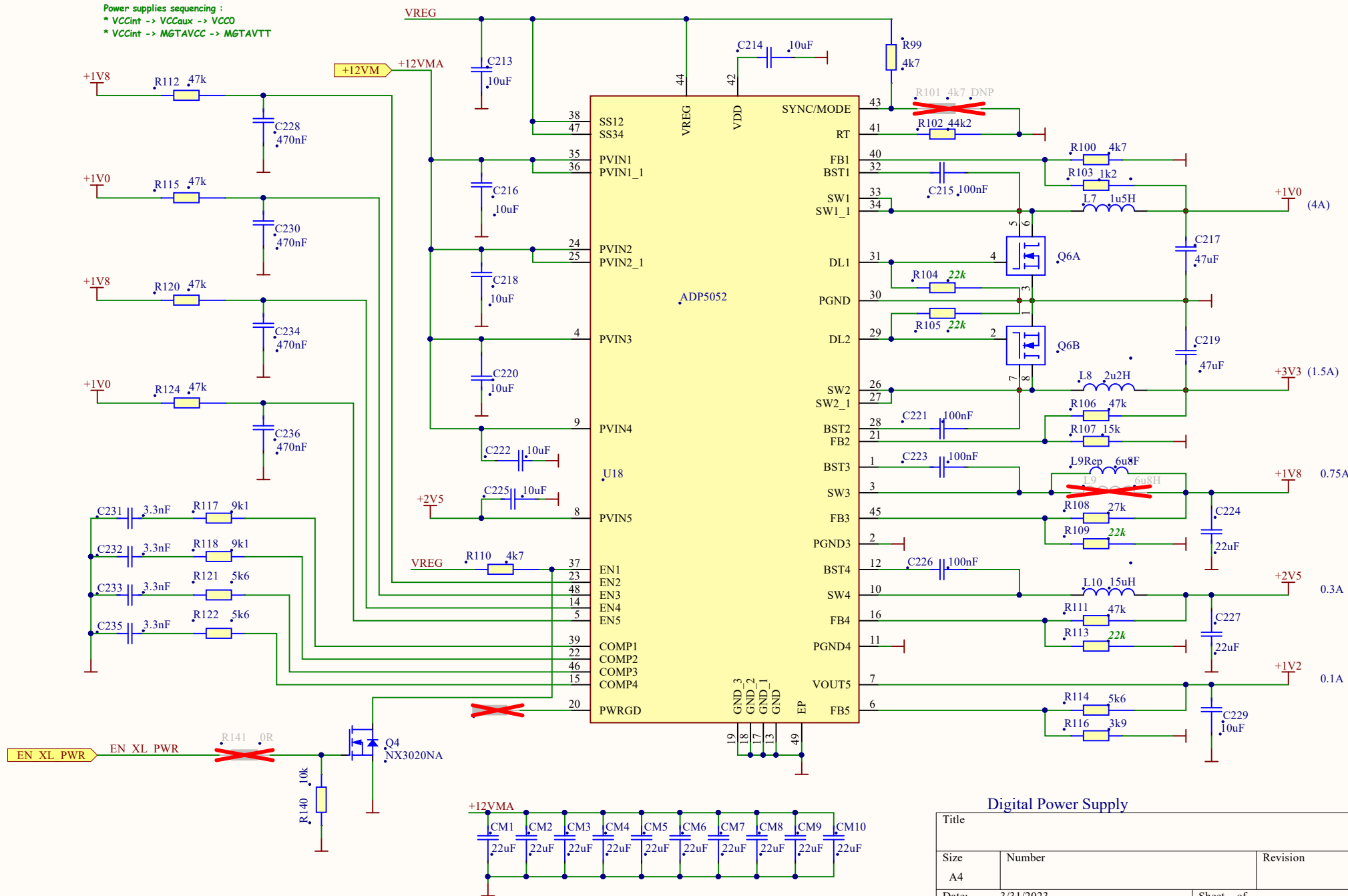






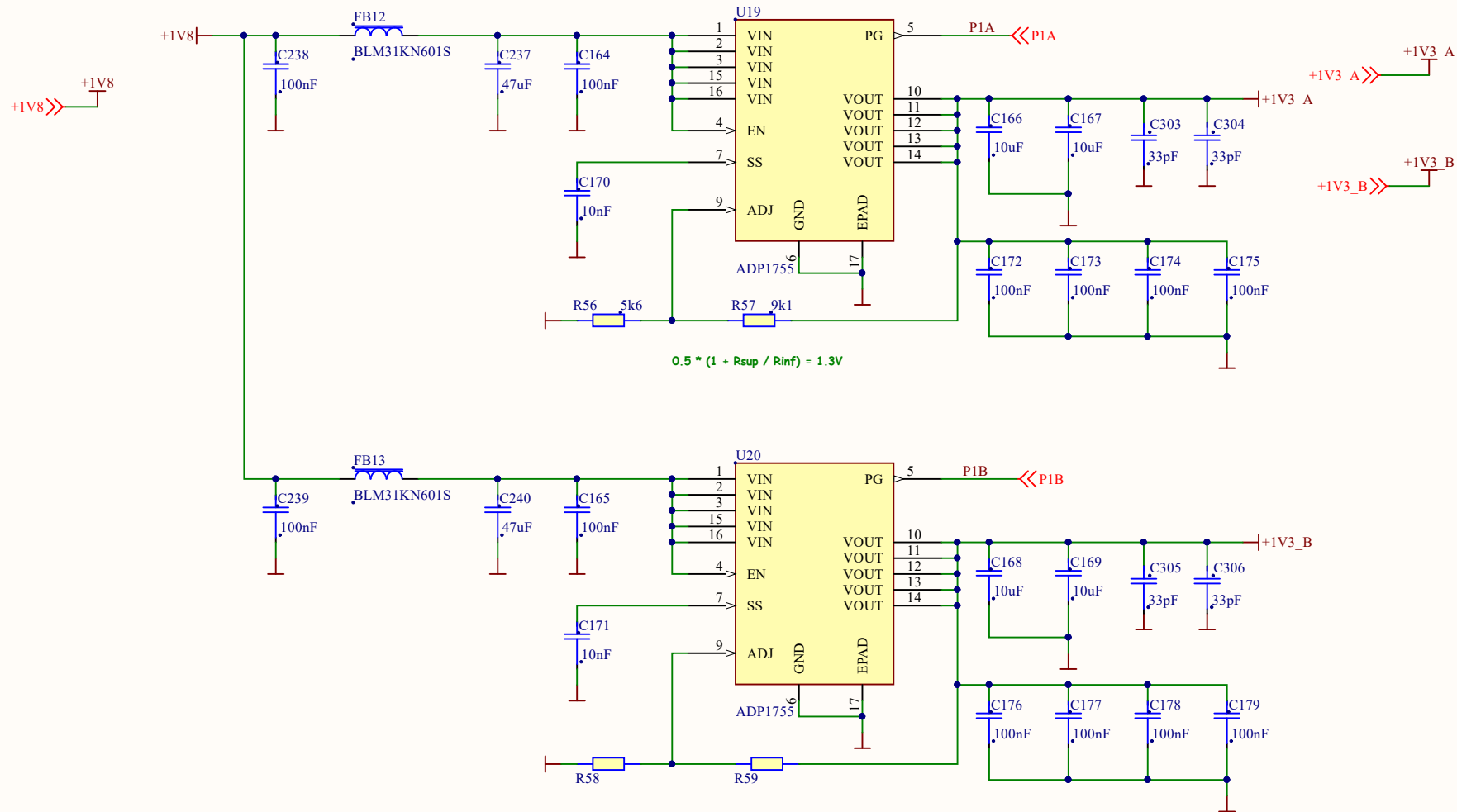


Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT



### Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_6.SchDoc	Drawn By:



Title RF Power Supply		
Size A4	Number	Revision
Date: 3/31/2023	Sheet of	
File: Z:\ors-hardware\hardOrs\Ors_7.SchDoc	Drawn By:	

A

B

C

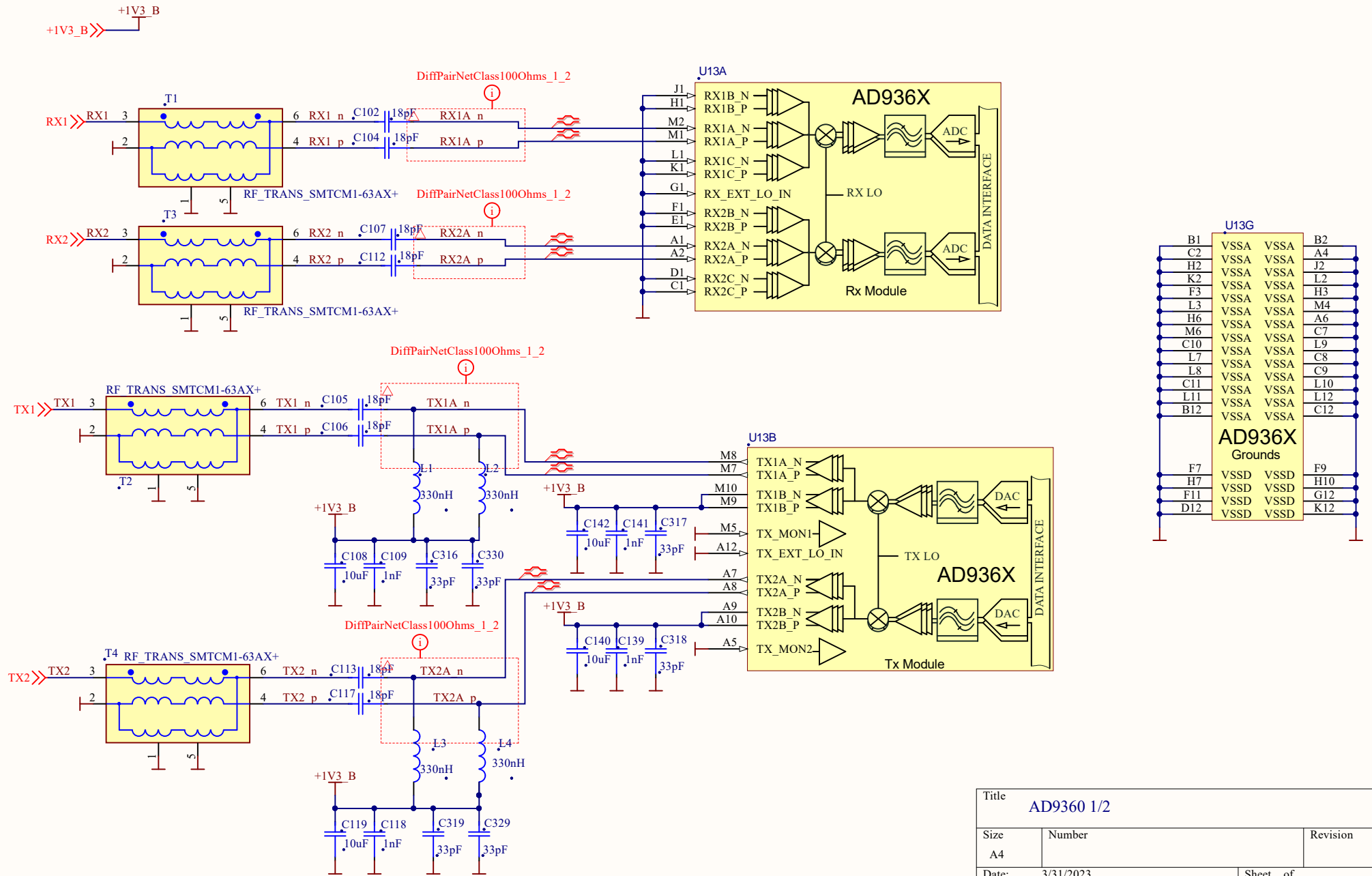
D

A

B

C

D



Title		
AD9360 1/2		
Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_8.SchDoc	Drawn By:

A

A

B

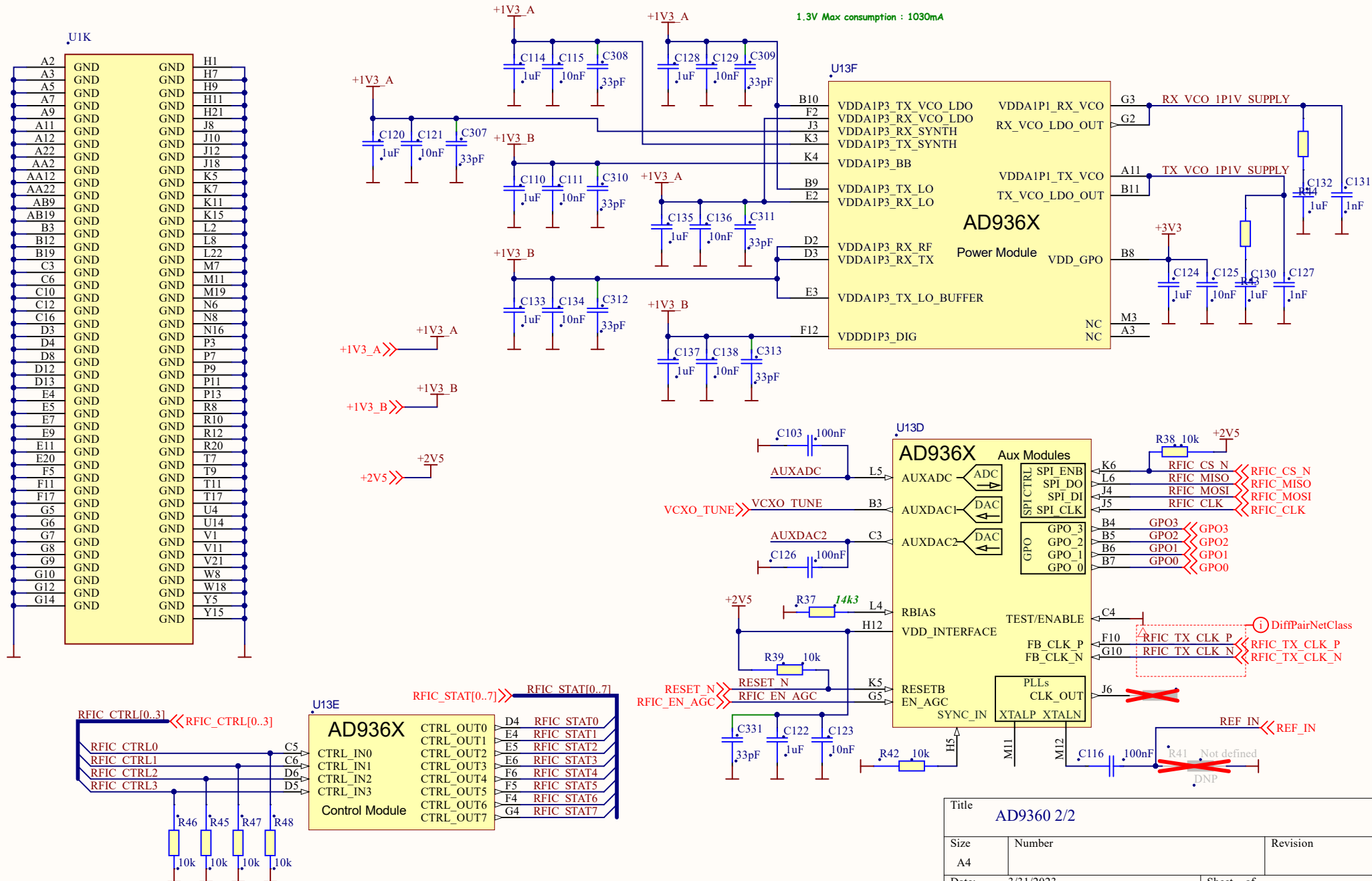
B

C

C

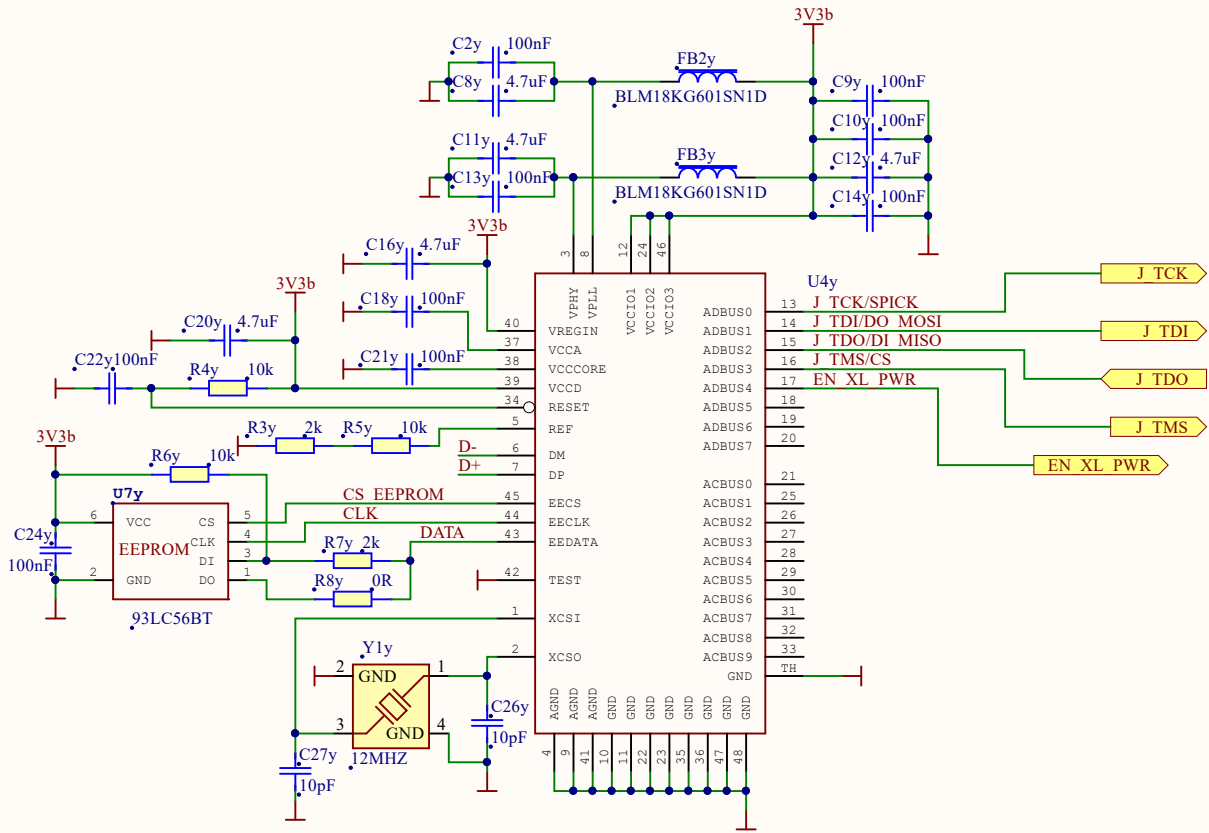
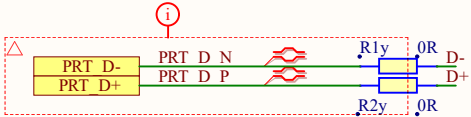
D

D



Title		
AD9360 2/2		
Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
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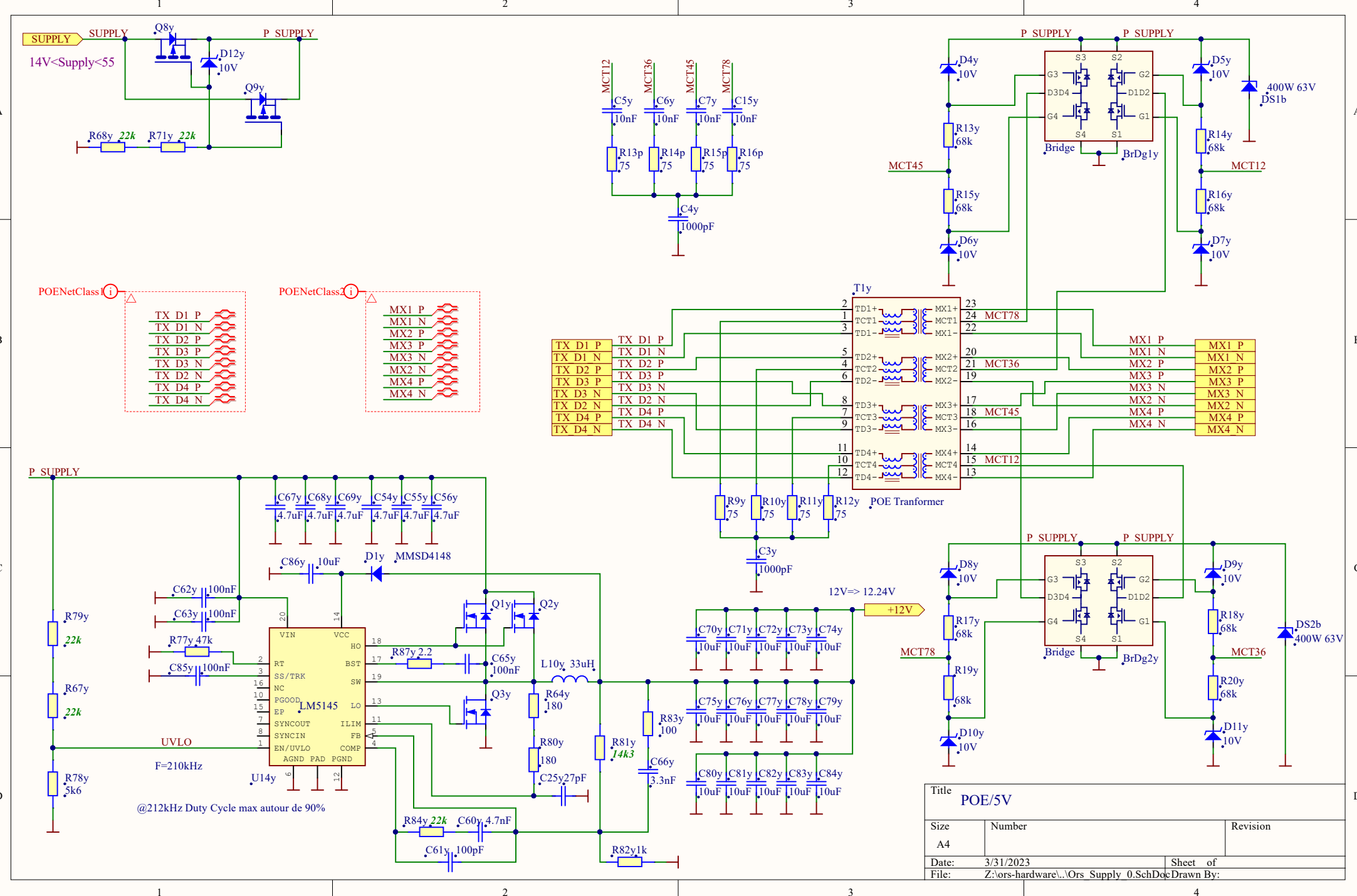
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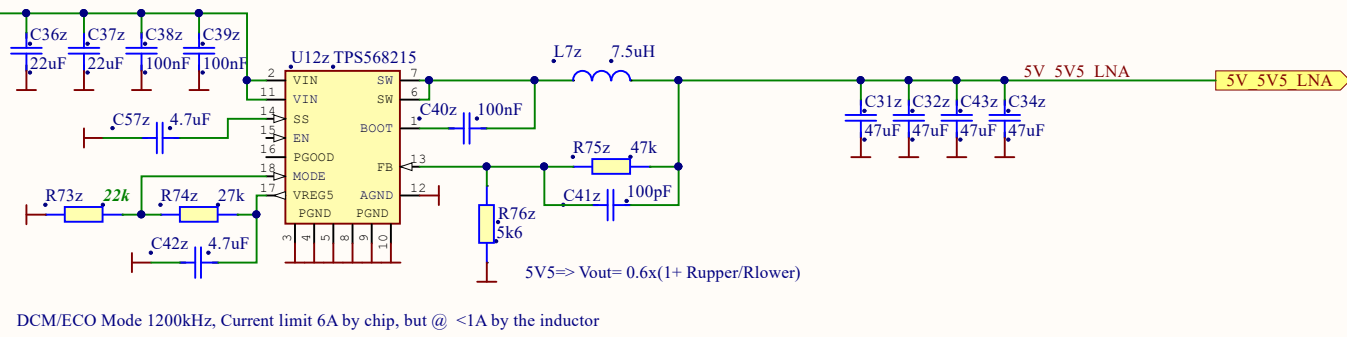
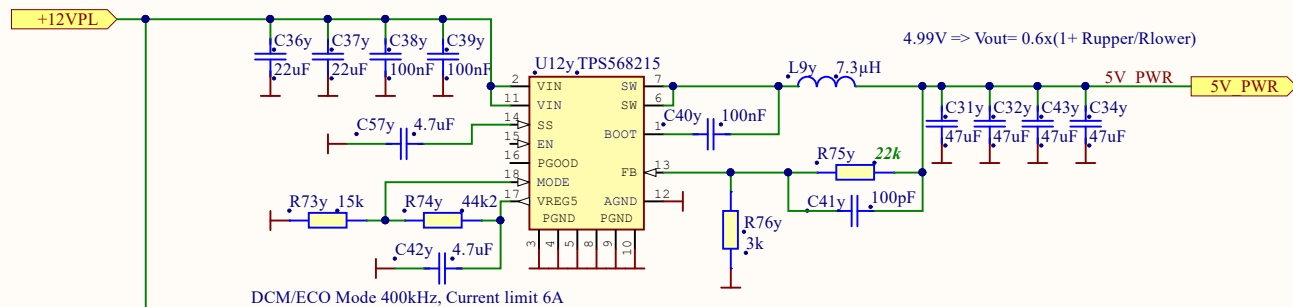
Title		
Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors Prog.SchDoc	Drawn By:







Title		
PoE/5V		
Size	Number	Revision
A4		
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Supply_0.SchDoc	Drawn By:



Title Main Connectors		
Size A4	Number	Revision
Date:	3/31/2023	Sheet of
File:	Z:\ors-hardware\...\Ors Supply 1.SchDoc Drawn By:	

