

A

B

C

D

E

F

G

H

A

B

C

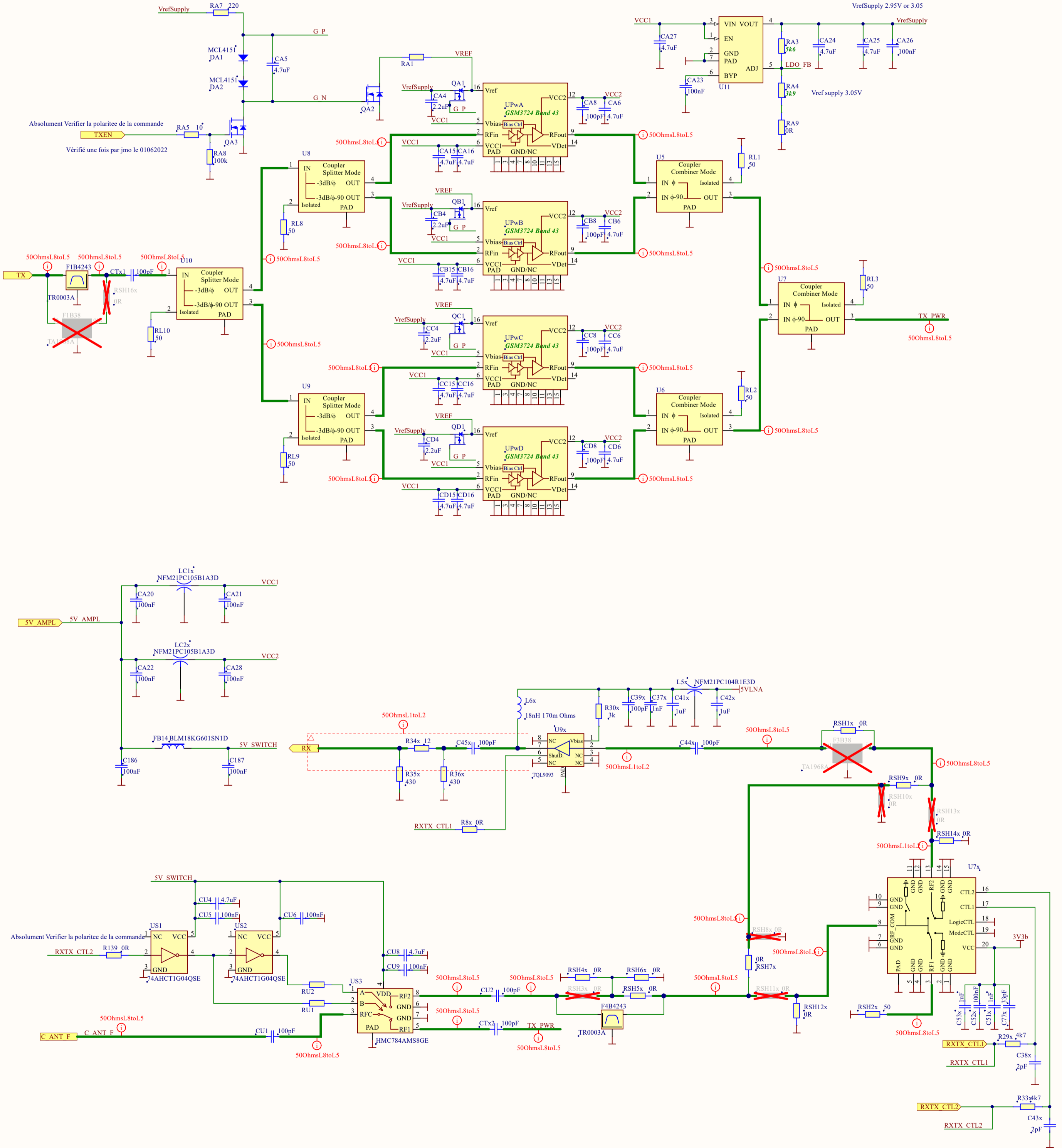
D

E

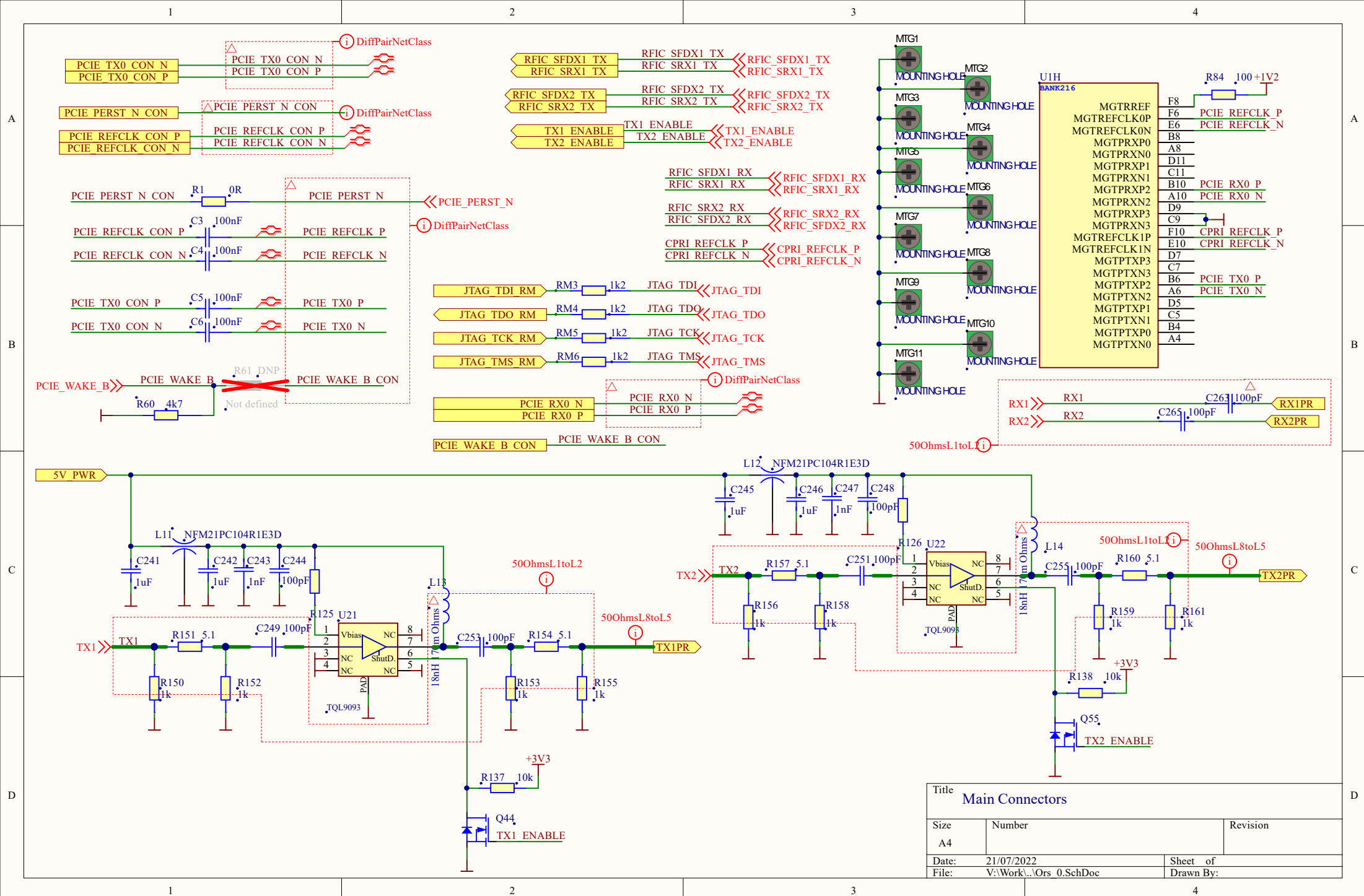
F

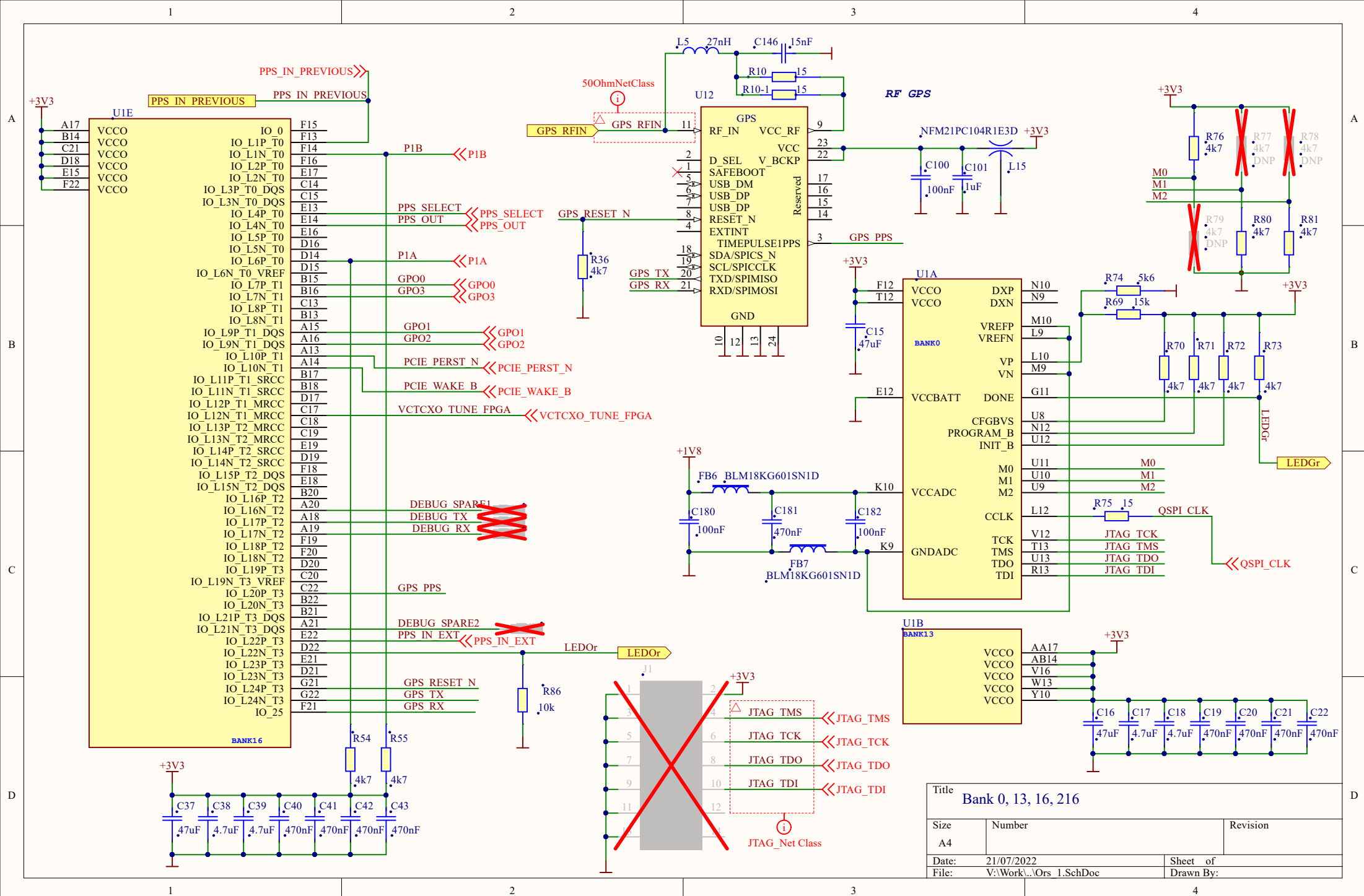
G

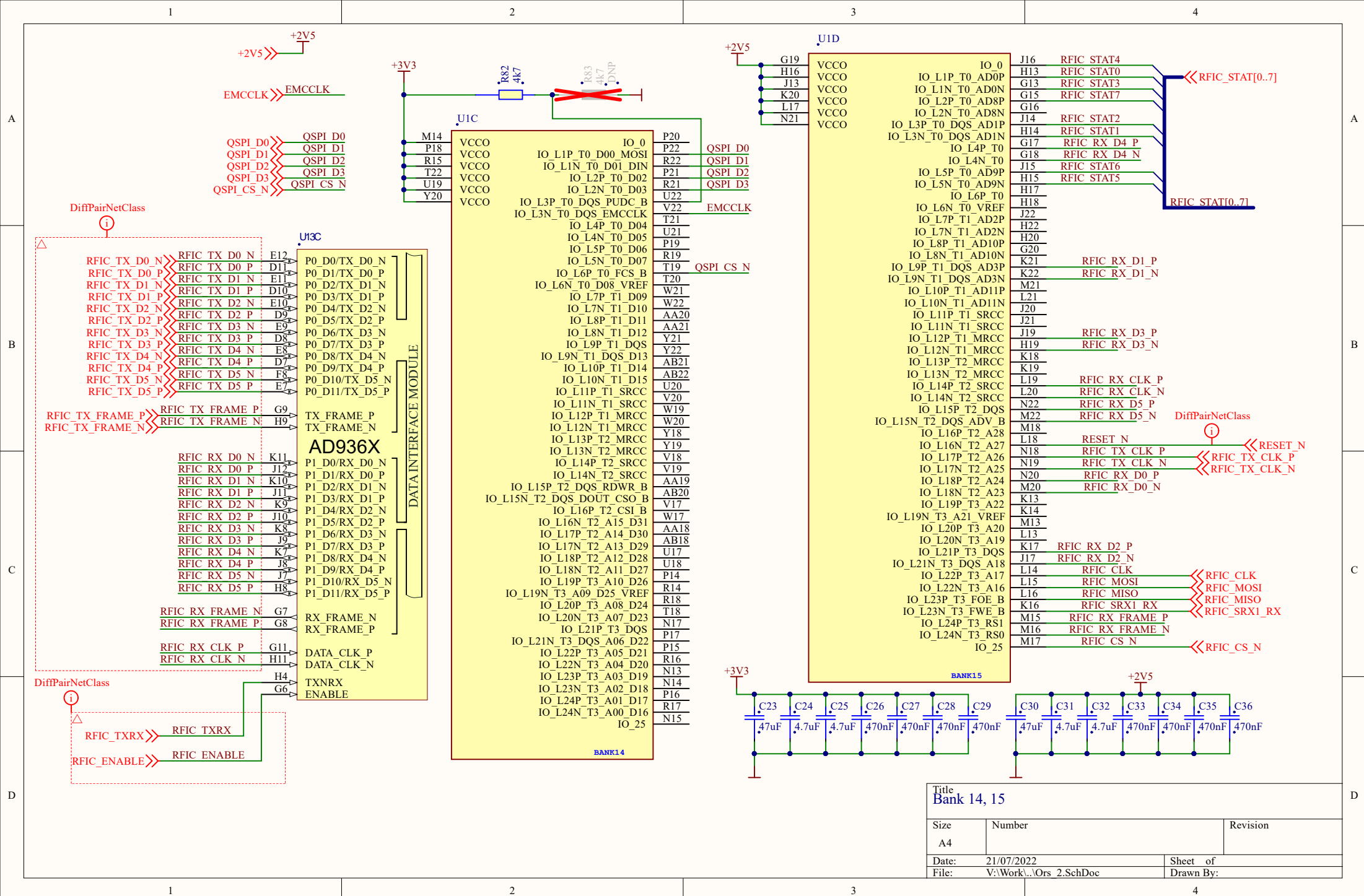
H

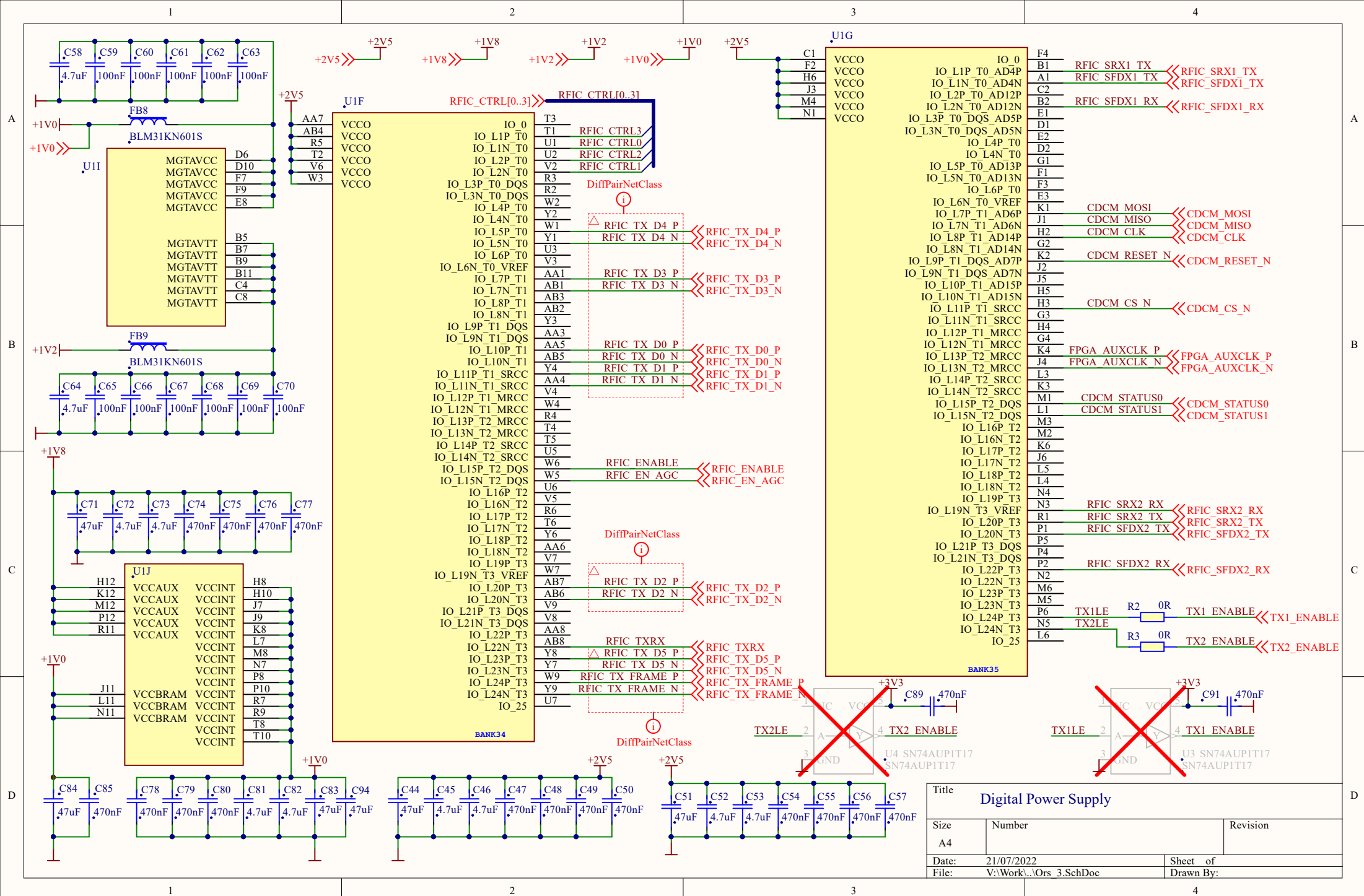


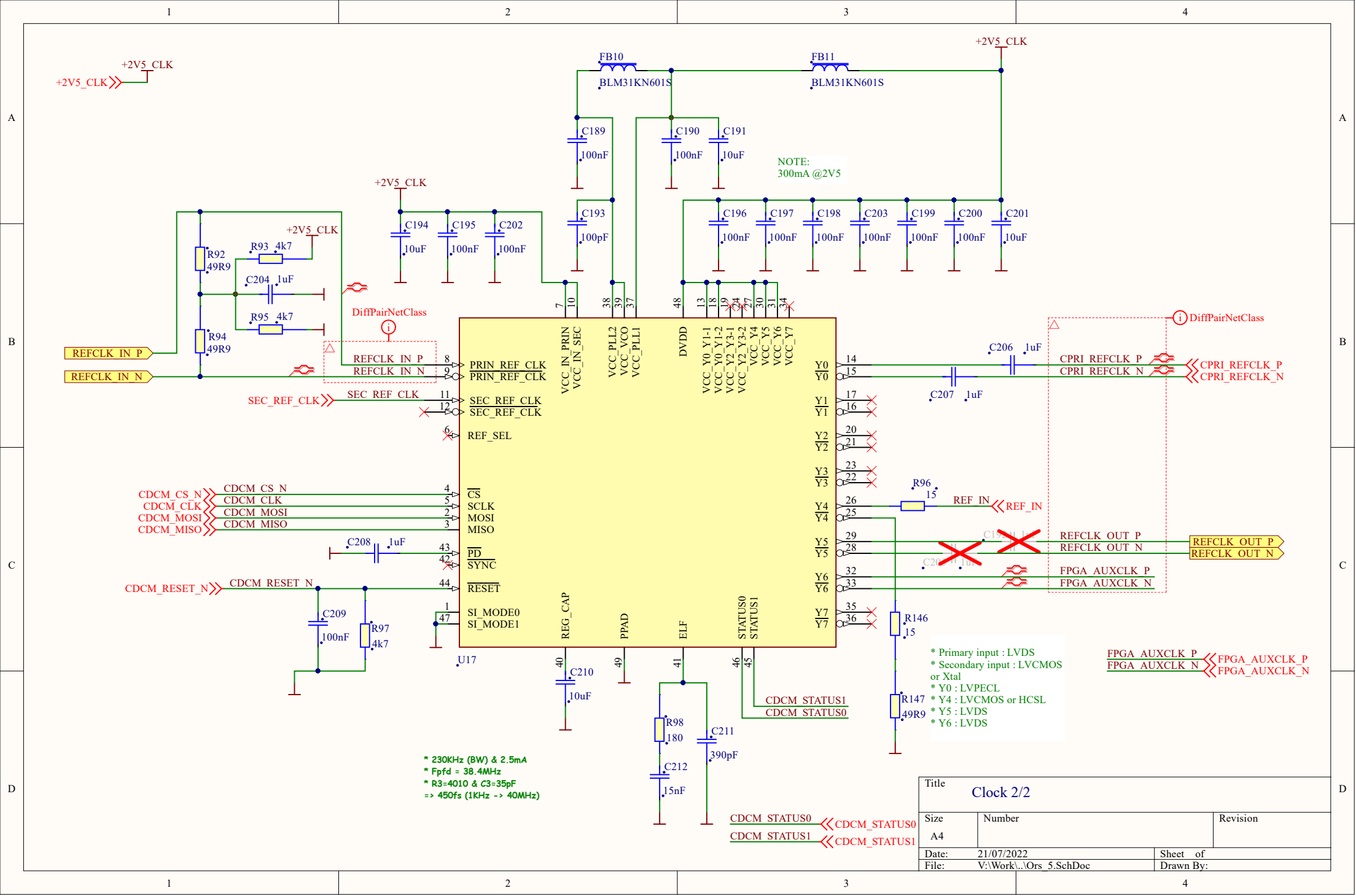
Title		
Size	Number	Revision
A2		
Date:	21/07/2022	Sheet of
File:	V:\Work\U\Ors Pwr_1.SchDoc	Drawn By:



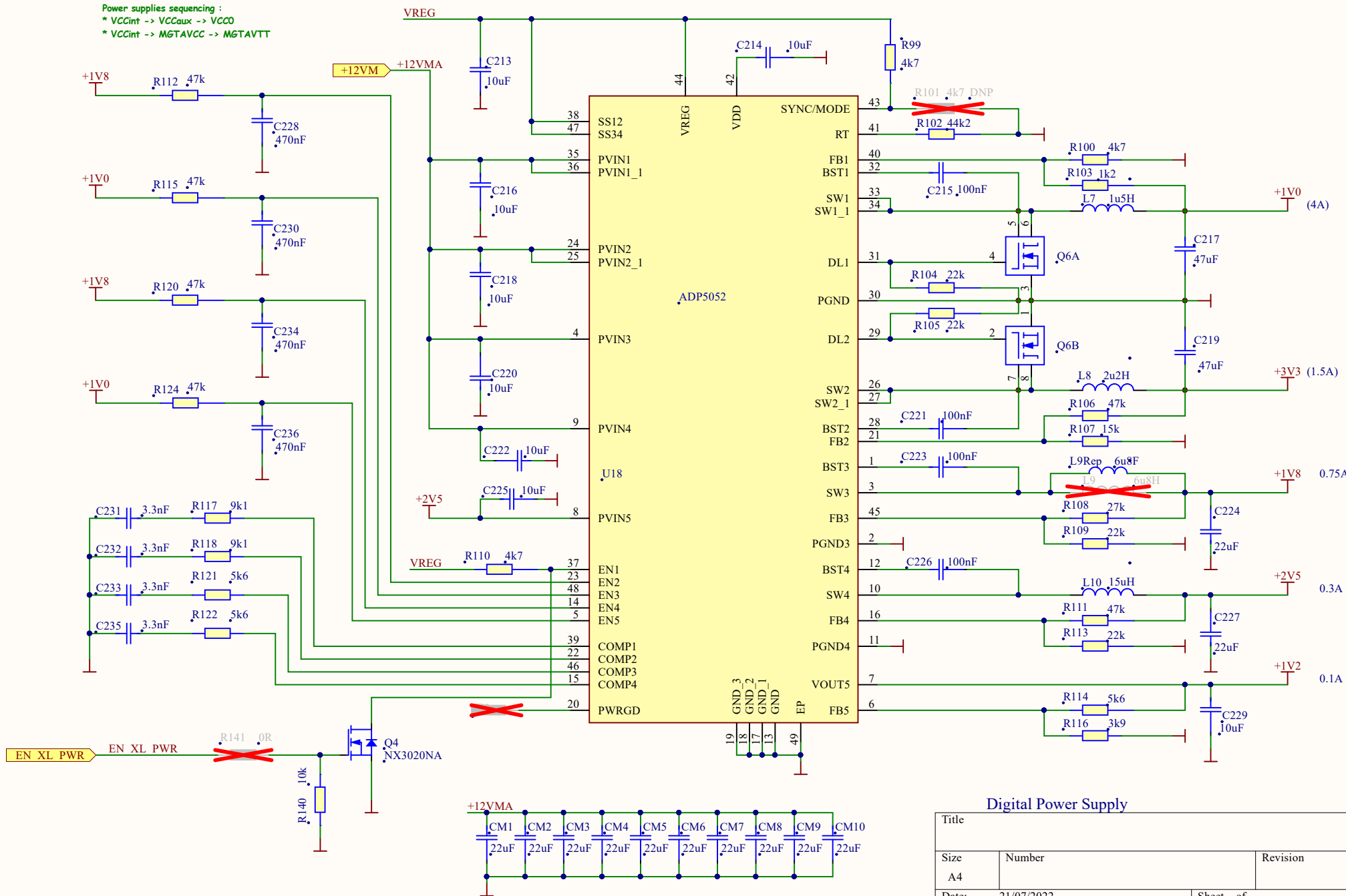








Power supplies sequencing :
* VCCint -> VCCaux -> VCCO
* VCCint -> MGTAVCC -> MGTAVTT



Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	21/07/2022	Sheet of
File:	V:\Work\...\Ors 6.SchDoc	Drawn By:

A

B

C

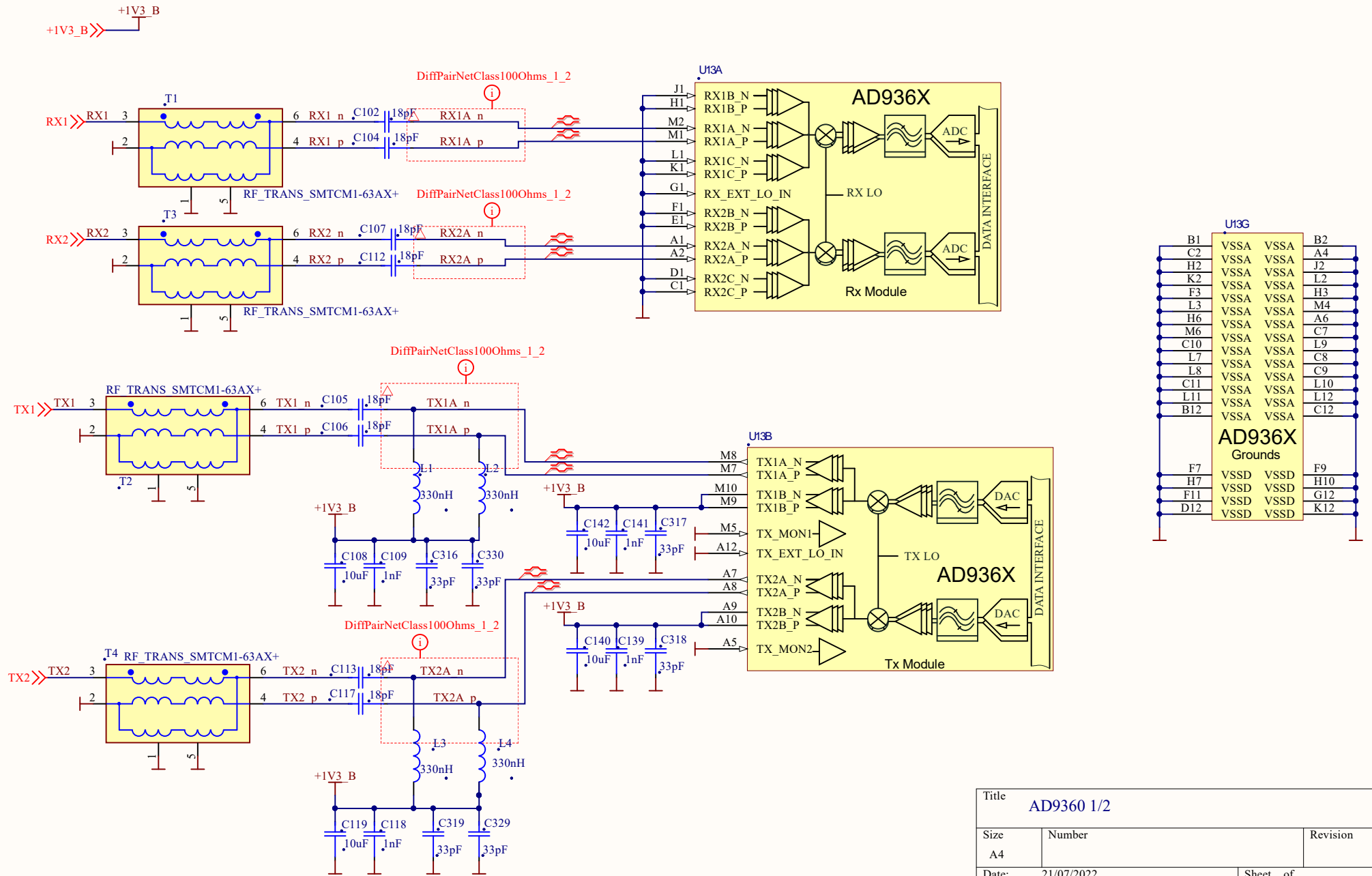
D

A

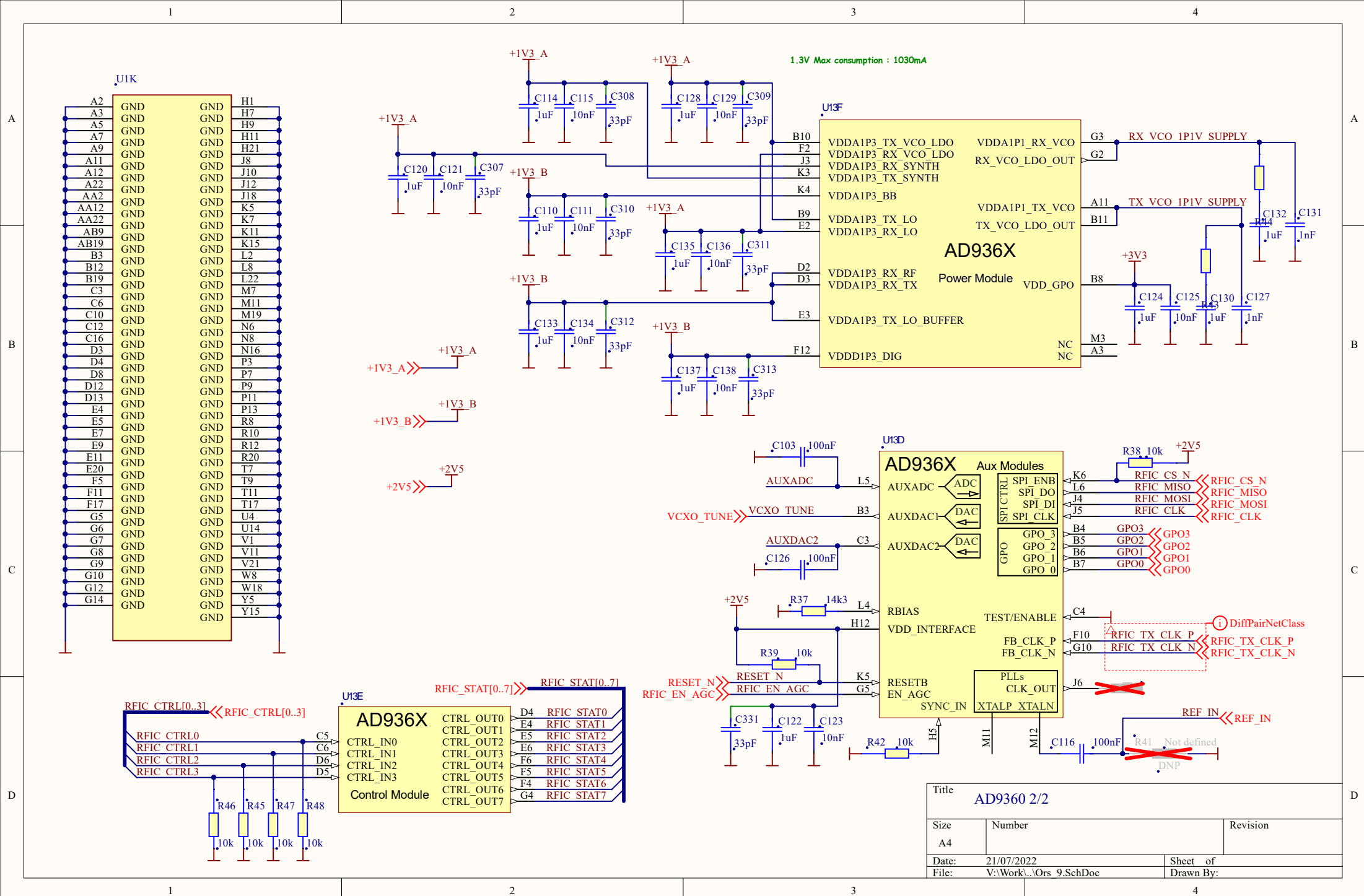
B

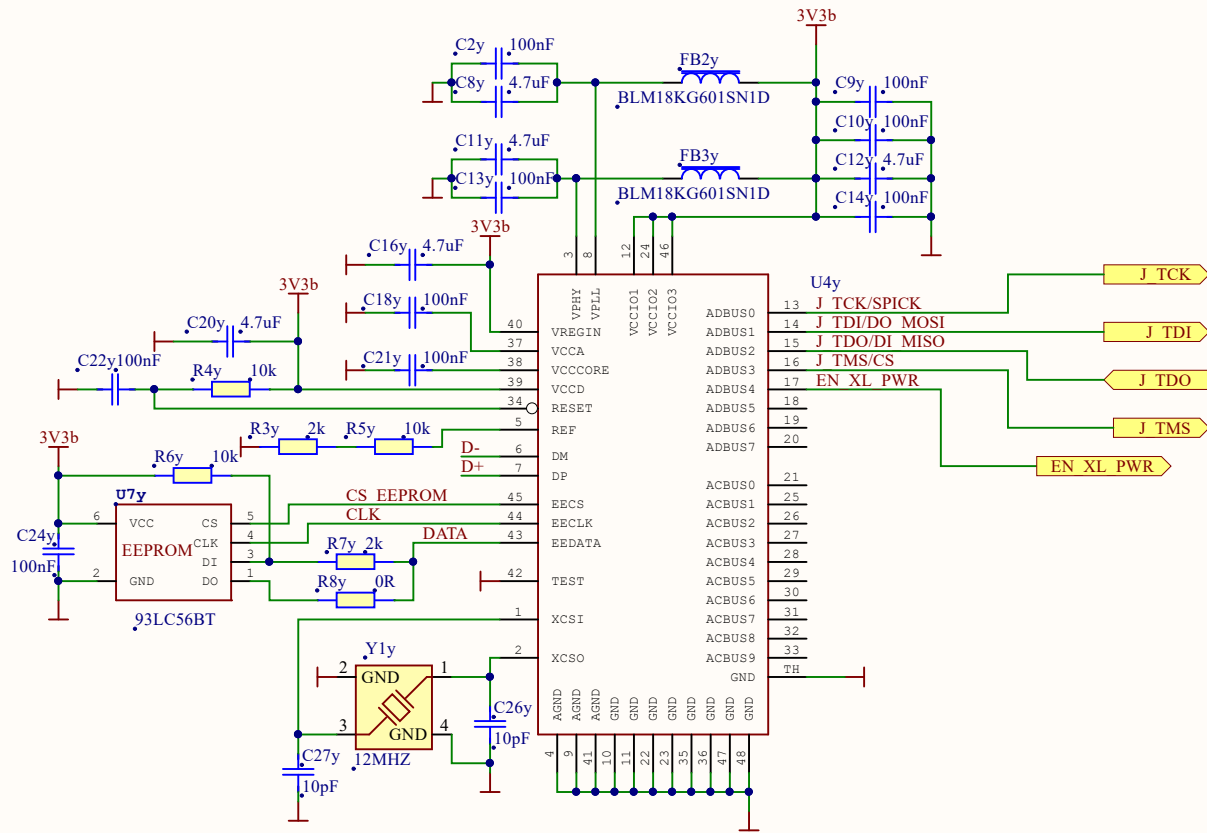
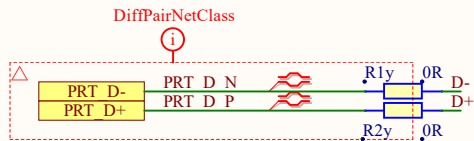
C

D



Title		
AD9360 1/2		
Size	Number	Revision
A4		
Date:	21/07/2022	Sheet of
File:	V:\Work\...\Ors 8.SchDoc	Drawn By:





Title		
Size	Number	Revision
A4		
Date:	21/07/2022	Sheet of
File:	V:\Work\...\Ors Prog.SchDoc	Drawn By:

A

B

C

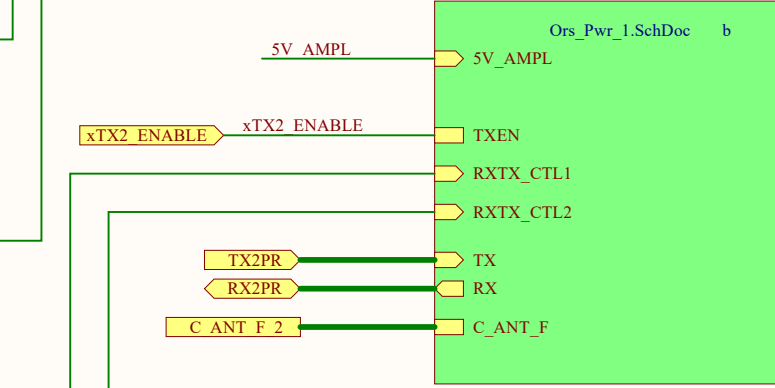
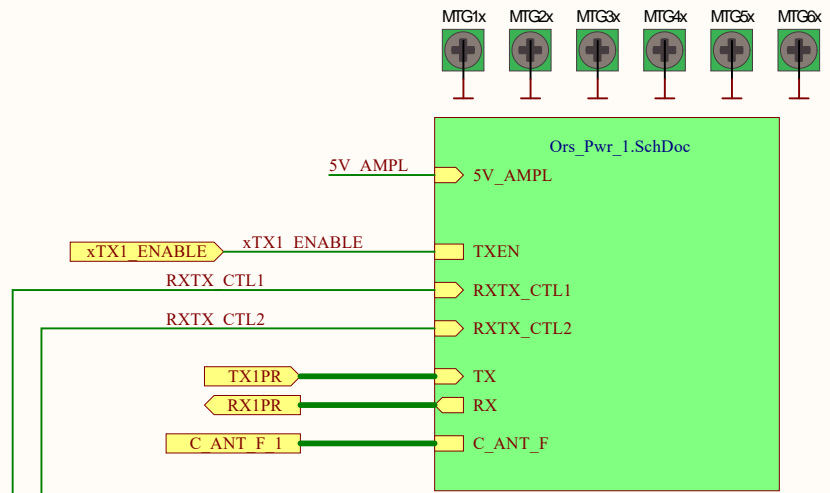
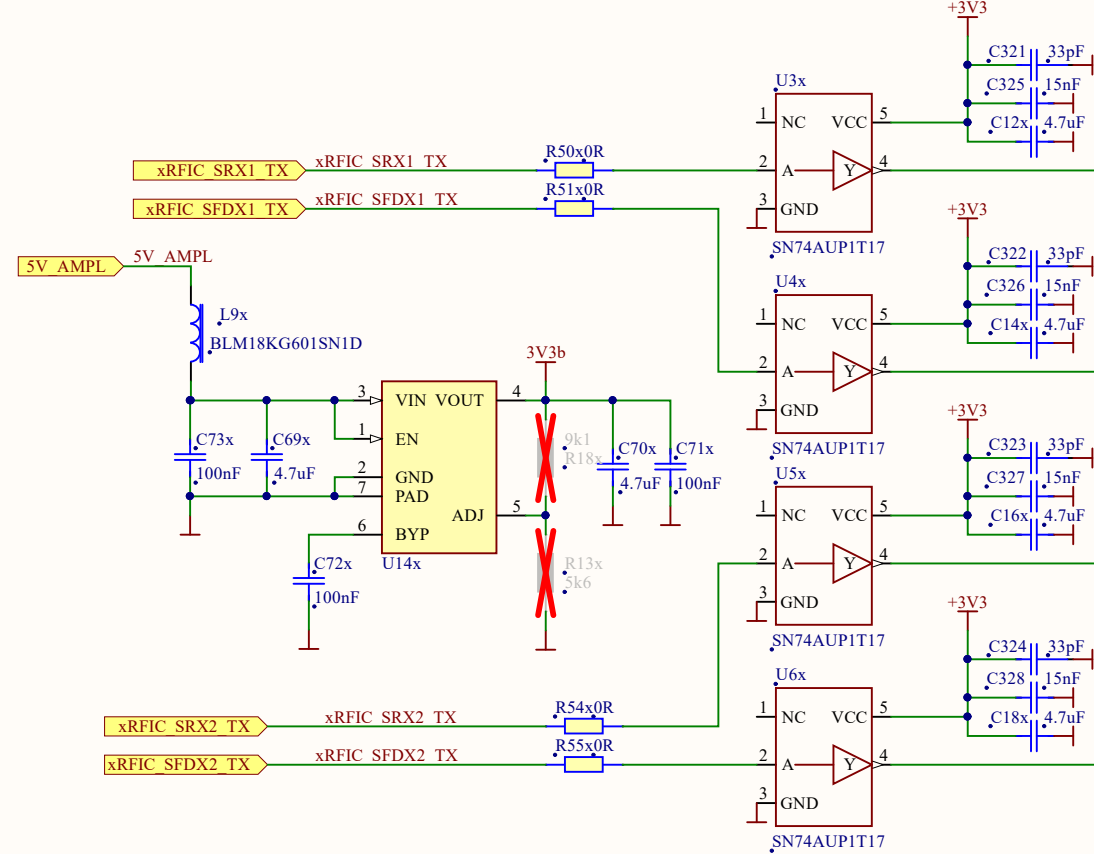
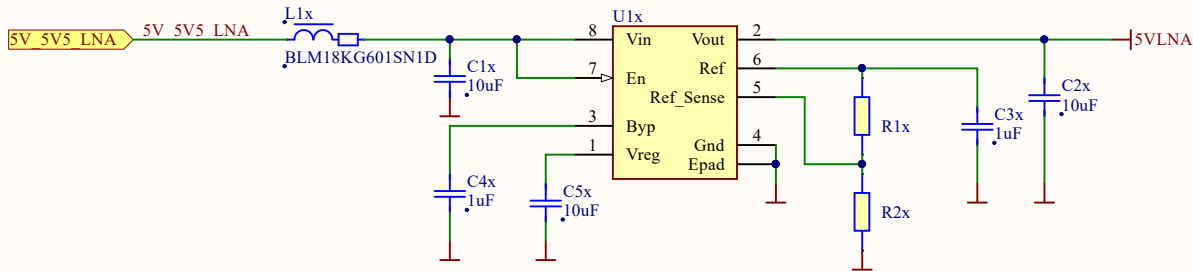
D

A

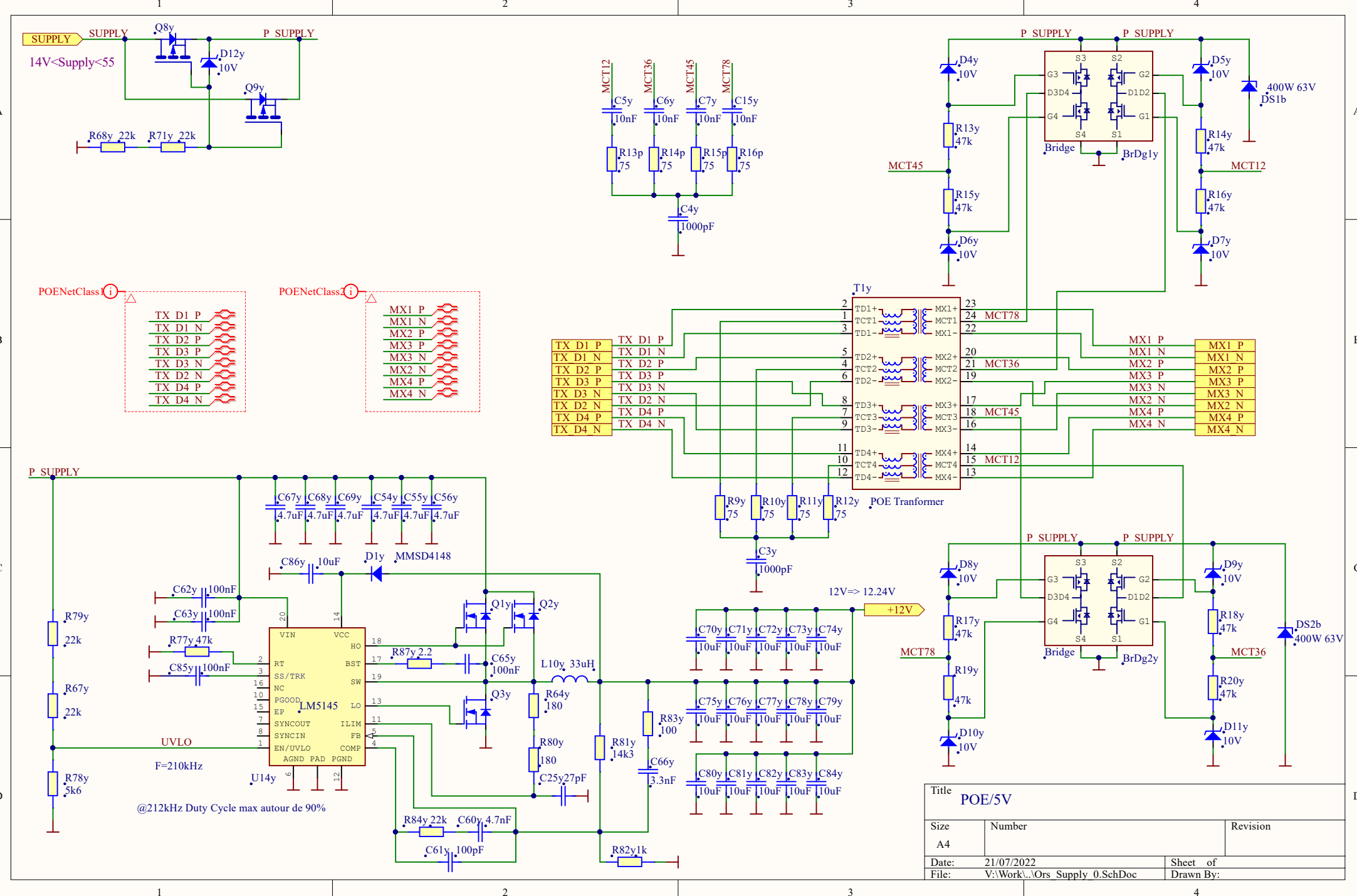
B

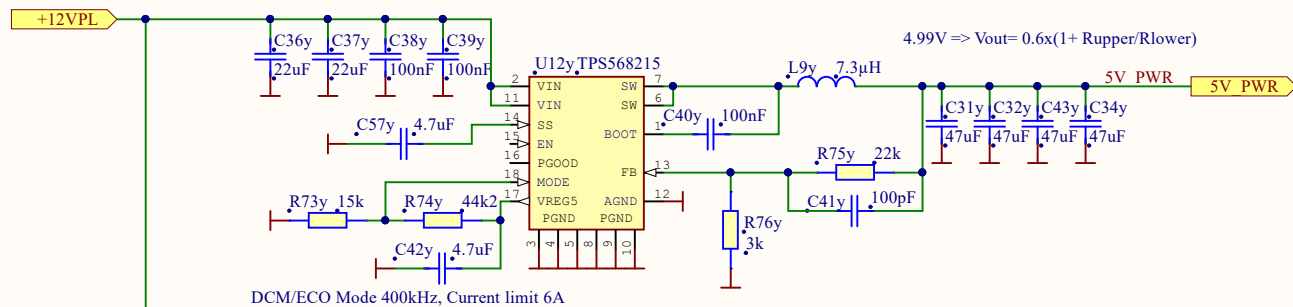
C

D



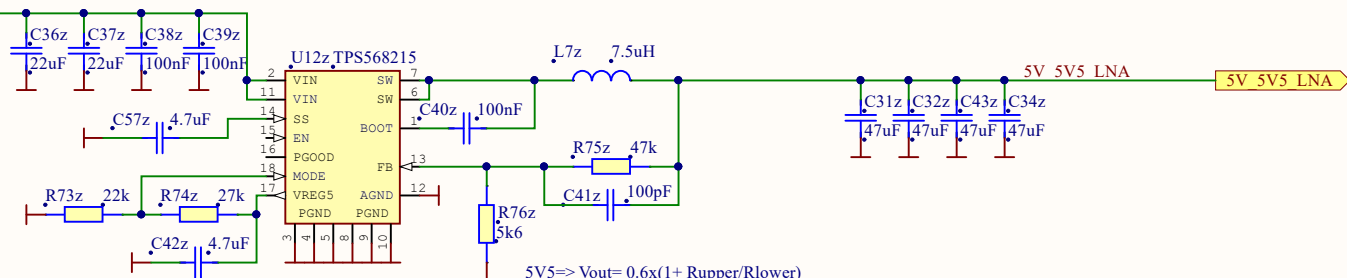
Title		
Size	Number	Revision
A4		
Date:	21/07/2022	Sheet of
File:	V:\Work\...\Ors_Pwr_0.SchDoc	Drawn By:





Soft Start
 $TSS(seconde)=CSS \times 0.6/6e-6 \Rightarrow 0.47s$

DCM/ECO Mode 400kHz, Current limit 6A



DCM/ECO Mode 1200kHz, Current limit 6A by chip, but @ <1A by the inductor

Title Main Connectors		
Size A4	Number	Revision
Date:	21/07/2022	Sheet of
File:	V:\Work\...\Ors Supply 1.SchDoc	Drawn By:

